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(54) Title: SYSTEM FOR EMULATING WANG VS PROGRAMS

(57) Abstract: A computer system (10) for executing Wang VS programs (28) without a Wang VS hardware system includes a host computer system (12), a Wang VS software system (22) and a hardware abstraction layer program (20). The host computer system (12) is independent of any Wang VS hardware system components. The Wang VS software system (22) includes at least a Wang VS operating system (24) stored in host memory (18). A hardware abstraction layer program (20) is functionally interposed between the Wang VS software system (22) and the host computer system (12) such that all machine instructions of the Wang VS software system (22) are first processed by the hardware abstraction layer program (20) before communicating with the host computer system (12).



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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US05/21026

A. CLASSIFICATION OF SUBJECT MATTER IPC: G06F 9/455 (2006.01) USPC: 703/23,27;718/1 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) U.S. : 703/23,27;718/1 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EAST, Google Scholar, IEEE, search terms: simos, simulator, emulator, simics, wang vs		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	Wang VS Emulator, Wang VS Emulator home page from April 5, 2003, retrieved from Internet address: http://web.archive.org/web/20030405072555/http://home.planet.nl/~ernest/vs.html	1 - 28
Y	US 6,496,847 (BUGNION et al) 17 December 2002 (17.12.2002), figures 1 and 2, and column 1 - 2, column 6.	1 - 28
A	ROSENBLUM, M. et al Complete Computer System Simulation: the SimOS approach, IEEE Parallel & Distributed Technology, Winter 1995, Volume 3, Issue 4, pages 34 - 43	1 - 28
A	MAGNUSSON, P.S. et al, Simics: A full system simulation platform, Computer, February 2002, Volume 35, Issue 2, pages 50 - 58	1 - 28
A	ALTMAN, E.R. et al, Welcome to the opportunities of binary translation, Computer, March 2000, Volume 33, Issue 3, pages 40 - 45	1 - 28
A	US 6,397, 242 (DEVINE et al) 28 May 2002 (28.05.2002), columns 1 - 12	1 - 28
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"O"	document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
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Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 Facsimile No. (571) 273-3201		Authorized officer Paul Rodriguez Telephone No. 571-272-3753 Jacqueline A. Whitfield Special Project Asst.

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C. (Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2004/0025158 A1 (TRAUT) 05 February 2004 (05.02.2004), paragraphs 1 - 29	1 - 28
A	US 2004/0123288 (BENNETT et al) 24 June 2004 (24.06.2004), paragraphs 1 - 23	1 - 28
A	US 2005/0081199 (TRAUT) 14 April 2005 (14.04.2005), paragraphs 1 - 40	1 - 28