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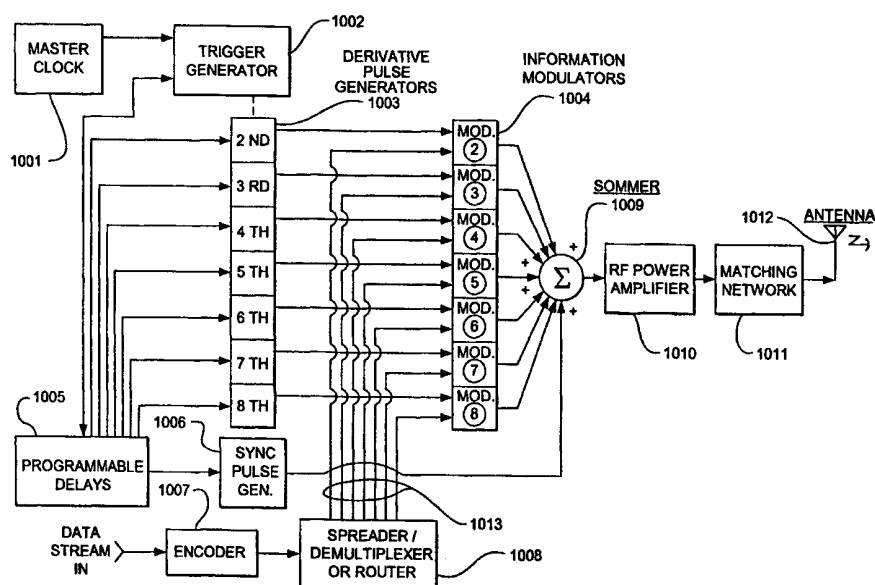
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(54) Title: PULSE TRANSMISSION TRANSCIVER ARCHITECTURE FOR LOW POWER COMMUNICATIONS



(57) Abstract: Systems and methods for pulse-transmission low-power communication modes are disclosed. A method of pulse transmission communications includes: generating a modulated pulse signal waveform; transforming said modulated pulse signal waveform into at least one higher-order derivative waveform; and transmitting said at least one higher-order derivative waveform as an emitted pulse. The systems and methods significantly reduce lower-frequency emissions from pulse transmission spread-spectrum communication modes, which reduces potentially harmful interference to existing radio frequency services and users and also simultaneously permit transmission of multiple data bits by utilizing specific pulse shapes.



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PULSE TRANSMISSION TRANSCEIVER ARCHITECTURE FOR LOW POWER COMMUNICATIONS

5 BACKGROUND OF THE INVENTION

1. Field of the Invention

The invention relates generally to the field of pulse transmission communications. More particularly, the invention relates to pulse transmission, spread-spectrum modes of low-power radio communications.

10 2. Discussion of the Related Art

Prior art time-domain communications techniques are known to those skilled in the art. The bandwidth and center frequency of a conventional time-domain transmission are both explicit functions of the pulse width.

In these techniques, the controllable parameters are the pulse width and power. These techniques normally allow only 1 bit to be conveyed per transmitted pulse, thereby limiting their communications usefulness.

A problem with this existing technology has been that the parameters of pulse width and power effectively provide only two degrees of freedom. This constraint severely limits the flexibility of the prior-art time-domain techniques. Therefore, what is required is an approach to time-domain communications that provides more degrees of freedom.

Another problem with this existing technology has been that only one bit can be encoded per transmitted pulse. This constraint severely limits the data bandwidth of the prior art time-domain techniques. Therefore, what is also required is an approach to time-domain communications that permits more than one bit per pulse to be communicated.

Heretofore, the requirements of providing additional degrees of freedom and communicating multiple bits per pulse have not been fully met. What is needed is an approach that can address both of these requirements. The invention is directed to meeting these requirements, among others.

SUMMARY OF THE INVENTION

The main object of the invention is to provide a versatile, multi-bit, very
5 broadband, high bit-rate data communications method. Another goal of the
invention is to use higher-order derivatives of pulsed (time-domain) signals to
satisfy the above-discussed requirements of providing additional degrees of
freedom and communicating multiple bits per pulse which, in the case of the
prior art, are not simultaneously satisfied.

10 One embodiment of the invention is based on a method of pulse-
transmission communications, comprising: generating a modulated pulse-signal
waveform; transforming said modulated pulse-signal waveform into at least one
higher-order derivative waveform; and transmitting said at least one higher-
order derivative waveform as an emitted pulse. Information is preferentially
15 encoded as the derivative order and phase; alternatively, time derivatives
between pulses can also encode information. In the latter case, the higher-order
derivative pulse provides flexible control over bandwidth and band center-
frequency, thus alleviating noise and other interference problems. Another
embodiment of the invention is based on an electromagnetic waveform,
20 comprising: an emitted pulse that is produced from at least one higher-order
derivative waveform of a modulated pulse-signal waveform. Another
embodiment of the invention is based on an apparatus based on an array of
various higher-order derivative-pulse generators, each of which is modulated by
an information signal. The modulated derivative pulses are summed, amplified,
25 and coupled to a transmission medium (antenna, cable, optical fiber, etc.) The
complementary receiver recovers the modulated pulses via standard correlation.

These, and other, goals and embodiments of the invention will be better
appreciated and understood when considered in conjunction with the following
description and the accompanying drawings. It should be understood, however,
30 that the following description, while indicating preferred embodiments of the
invention and numerous specific details thereof, is given by way of illustration

and not of limitation. Many changes and modifications may be made within the scope of the invention without departing from the spirit thereof, and the invention includes all such modifications.

5

BRIEF DESCRIPTION OF THE DRAWINGS

A clear conception of the advantages and features constituting the invention, and of the components and operation of model systems provided with the invention, will become more readily apparent by referring to the exemplary, and therefore nonlimiting, embodiments illustrated in the drawings accompanying and forming a part of this specification, wherein like reference characters designate the same parts. It should be noted that the features illustrated in the drawings are not necessarily drawn to scale.

Fig. 1 illustrates a series of Gaussian derivative wavelets in frequency space, representing an embodiment of the invention.

Fig. 2 illustrates a high-level schematic diagram of a circuit for generating a second-derivative Gaussian wavelet, representing an embodiment of the invention.

Fig. 3 illustrates center frequency as a function of derivative order, representing an embodiment of the invention.

Fig. 4 illustrates relative bandwidth as a function of derivative order, representing an embodiment of the invention.

Fig. 5 illustrates the power spectrum of a 7th-order Gaussian pulse, representing an embodiment of the invention.

Fig. 6 illustrates a series of Gaussian derivative time-domain pulses, representing an embodiment of the invention.

Fig. 7 illustrates a series of 7 superimposed Gaussian derivative pulses of orders 2-8 composing a set of symbol codes, representing an embodiment of the invention.

Fig. 8 illustrates a composite pulse representing the binary code 0 1 01 11 01, representing an embodiment of the invention.

Fig. 9 illustrates the power spectral density of the composite pulse depicted in Fig. 8.

Fig. 10 illustrates a high-level schematic diagram of a circuit for transmitting, representing an embodiment of the invention.

5 Fig. 11 illustrates a high-level schematic diagram of a circuit for receiving, representing an embodiment of the invention.

Fig. 12 illustrates a high-level schematic of a circuit for transmitting, representing an embodiment of the invention.

10 Fig. 13 illustrates a high-level schematic of another circuit for transmitting, representing an embodiment of the invention.

Fig. 14 illustrates a high-level schematic of another circuit for receiving, representing an embodiment of the invention.

DESCRIPTION OF PREFERRED EMBODIMENTS

15 The invention and the various features and advantageous details thereof are explained more fully with reference to the nonlimiting embodiments that are illustrated in the accompanying drawings and detailed in the following description of preferred embodiments. Descriptions of well known components and processing techniques are omitted so as not to unnecessarily obscure the
20 invention in detail.

The context of the invention is spread-spectrum communications. For instance, the invention can be used in the context of low-power spread-spectrum radio communications. The invention can also utilize data processing methods that transform the received pulse transmission signals so as to actuate
25 interconnected discrete hardware elements; for example, to remotely reconfigure repeater(s) and/or router(s).

The invention includes using higher-order derivatives of waveforms to implement pulse transmission communications, whereby the shape as well as the bandwidth and center frequency of the transmitted power pulse may be used
30 as adjustable parameters allowing a more versatile transmission architecture. In

addition, the higher-derivative pulses may be used as the basis of a multiple-bit symbol system, greatly increasing the transmitted information rate.

The phrase "higher-order derivative," as used herein, is defined as at least a second order derivative (i.e., 2nd order, 3rd order, ... nth order). The term "approximately", as used herein, is defined as at least close to a given value (e.g., preferably within 10% of, more preferably within 1% of, and most preferably within 0.1% of). The term "coupled", as used herein, is defined as connected, although not necessarily directly, and not necessarily mechanically. The term "programmable", as used herein, is as defined controllable by two or more lines of code that can be executed by a computer.

The term "orthogonal," as used herein, is defined as two or more functions or signals whose overlap is zero. The practical advantage of using orthogonal signals is that they do not interfere with each other and they are independent, which means that information carried by one can be inserted or extracted independently of the information in the other. For instance, a sine and a cosine signal each of the same frequency are orthogonal; quadrature signals are orthogonal.

The invention provides another degree of freedom (shape) to control the transmission's spectral properties, thereby allowing a wider range of applications, greater receiver selectivity, and the ability to avoid known interferers and reduce potentially harmful interference to existing radio frequency (RF) services and users. The invention allows reduced emissions in lower-frequency regions compared to those of the prior art. This will reduce interference to existing radio frequency services and users. The invention can (for a given spectral content) permit longer generating pulse widths and easier electronic implementation, ensuring compatibility with existing commercial devices and processes.

The invention can include the capability of implementing these controllable parameters directly in radio-frequency (RF) silicon bipolar or complementary metal-oxide semiconductor (CMOS) devices, as well as with silicon-germanium (SiGe), gallium arsenide (GaAs), or other suitable high-

frequency semiconductor processes. Use of these processes can make implementations of the invention inexpensive to fabricate and fast enough to be operable at VHF/UHF frequencies (hundreds of MHz) and beyond.

The invention can include generating waveforms from stored digital versions. This can simplify the hardware requirements for transmitters.

The invention can be combined with orthogonal time hopping, orthogonal frequency hopping, and hybrid frequency/time methods. By transmitting a data stream at various intervals and/or at various frequency bands, the pulse transmission can be made difficult to detect, much less decode.

Mathematical Background

By passing a rectangular pulse of chosen duration through a shaping circuit, a Gaussian waveform (or other type of waveform useful for communications) may be produced. Electronic circuitry for achieving such pulse shaping is widely described in the nuclear-detection literature. The resulting Gaussian pulse is then amplified as required and passed through further circuitry that effects a predetermined number of derivatives; the output pulse is a waveform closely approximating the desired-order derivative of an ideal Gaussian function. The pulse of desired derivative order is then linearly amplified and matched to the antenna of choice for transmission. Alternatively, lattice-filter structures can directly achieve the desired derivative order, starting with a simple bipolar input pulse.

Although the Gaussian-derivative pulse family is the preferred embodiment for practicing the invention, such practice is not limited to Gaussian pulses. In general, any family of pulses which derives from a single or multiple pulses that have a limited extent in both time and frequency are suitable for shaping and modulating as carriers of information as described herein. The derivative-derived family necessarily obeys a Rodriques' Formula; other band-limited and time-limited orthogonal functions do not, yet can serve as a family of pulses suitable to the purposes of the invention. The convenience of the Gaussian pulse stems from its unique property that both function and its Fourier transform have the same functional form, namely $\exp(-x^2/a)$ where a is

a constant and x is either time or frequency. Some functions that are similarly bounded in time and frequency are the "super-gaussian" functions described by $\exp(-x^{2n}/a)$ where n is an integer > 1 that represents the order of the function.

In addition, it is not required that the fundamental member(s) of the pulse family possess a convenient functional form. A time-limited square pulse can be shaped to restrict its frequency content by "rounding" the "corners," giving a smooth "square" pulse. Such a pulse meets the requirements of limited support in both time and frequency. Derivatives of such a pulse, while not necessarily orthogonal, can carry information and be demodulated as described herein.

Another class of functions that can serve as the orthogonal family for the practice of the invention is the so-called discrete functions. Members include the Chebyshev and Krawtchouk polynomials, which are defined on a finite lattice (sample points) rather than a continuous segment. These families are suitable candidates for practicing the invention using pulses reconstituted from stored samples as previously suggested.

In general mathematical terms, any "lump" sufficiently concentrated in both time and frequency can possess either a derivative family or a family stored as discrete samples meeting the practical orthogonality and practical spectral requirements on which the practice of the invention is based. The choice of the particular shape of the root or basic member of the family is purely a practical matter, having to do with the particular means of electrically generating the pulses and coupling them to a properly designed antenna. Issues of signal propagation and reception can also play a design role in specific and individual situations.

Fig. 1 shows Gaussian-derivative wavelets in frequency space. The derivative orders from 1 to 13 are shown as a function of frequency. As the derivative order is increased, the mode of the function (center frequency) moves from near zero (or dc frequency) to higher values as shown in Fig. 1. The first-derivative pulse, shown as the dashed curve, used in communication systems according to the prior art, has a slope of $\tau\sqrt{\pi}$ at zero frequency, indicating considerable spectral power at low frequencies; the higher derivatives all have

zero slope at zero frequency, indicating very little energy at low frequencies. If the initial square pulse is 1 ns wide, the abscissa is in GHz. The ordinate is in normalized power units.

The spectra of certain of these higher-order derivative waveforms have negligible content at lower frequencies and therefore possess the highly desirable property of avoiding radio frequency interference to lower-frequency services (e.g., television, radio-astronomy, cellular telephones, and general communications) in the VHF and UHF regions of the radio frequency spectrum. This constitutes a major advantage of the invention.

Since the derivative order and the shaping properties of the antenna, together with the width of the initial pulse, uniquely determine the spectral properties of the transmitted energy, a receiver employing these parameters can be designed to optimally locate and extract the transmitted energy and convert it into a sharply defined received pulse. The concept of processing gain, normally used in spread-spectrum applications, applies here also in that the bandwidth of the transmitted pulse can be very large, extending over many potential interferers, each of which overlaps the transmitted energy only marginally. The bandwidth of the transmitted pulse is typically made much larger than most interferers, even those usually thought of as broadband, such as standard direct-sequence spread-spectrum (DSSS) transmissions. Thus, even though there is very little energy per Hertz in the transmitted pulse, this energy per Hertz multiplied by the bandwidth of the pulse, which is also the receiver bandwidth, is sufficient to identify the presence or absence of a transmitted pulse, allowing both its relative arrival time and particular shape to be determined. Standard readily commercially implementable methods of time-domain correlation are sufficient for this purpose.

Bandwidth and Position as a Function of Pulse Width

The invention allows both the bandwidth and position in frequency of an individual pulse to be determined. The ability to generate higher-order derivatives is key to steering the bandwidth of the transmitted energy as desired in frequency space. An additional benefit of using derivatives is that, by

starting with a rectangular pulse of longer duration, which is much easier and cheaper to handle electronically, the location in frequency now becomes a function of the derivative order. By restricting the transmitter to the first-order case, as in existing art, bandwidth and center frequency are solely determined by pulse width.

Fig. 2 shows a high-level conceptual schematic of one possibility for generating a second-derivative Gaussian wavelet pulse, starting with a rectangular differential pulse of duration τ at the input. This circuit shapes and transmits the pulse. In more detail, the rectangular differential pulse is generated by a signal source 210. The signal source is coupled to a filter 220. The filter 220 is coupled to the output via a capacitor C. A resistor R is coupled to the output in parallel with the capacitor C. At the output is the desired pulse shape, which may then be amplified in a broadband radio frequency amplifier and passed to the antenna.

Fig. 3 shows center frequency as a function of derivative order. If the characteristic time, τ , is 1 ns, the units of the ordinate are in 10^9 Hz (GHz). The center frequency of a pulse with characteristic time τ and order n is . This variation with order is shown in Fig. 3 for $\tau = 1$.

Fig. 4 shows the behavior of the relative bandwidth as a function of derivative order. At derivative orders higher than about 5, the relative bandwidth is less than approximately 50%. The relative bandwidth is only weakly dependent on τ . The 3-dB bandwidth for a given pulse width, τ , is a smooth but complicated function of τ and n ; it is shown in Fig. 4. Note that the bandwidth for the $n = 1$ pulse is considerably larger than for pulses with higher n . This feature is desirable if only the first derivative Gaussian pulse is employed, but provides no particular benefit when higher or multiple orders are used.

Fig. 5 shows the power spectrum of a 7th order Gaussian pulse with characteristic time of 2 ns. The frequency (X-axis aka abscissa) is in units of 10^9 Hertz (GHz). The pulse is centered at 1.3229 GHz and has a bandwidth of 2.6677 GHz. For comparison, the first-order derivative used in certain existing

devices is shown as the dashed curve. Note that the pulse derived according to the existing art has much more energy at frequencies below about 500 MHz and will tend to contribute significant interference to services in that region (e.g., television, aircraft and public-service radios, and so forth).

5 Single Pulse—1 bit per symbol

Prior-art techniques in the field of time-domain communications make exclusive use of the first derivative of a Gaussian function. The relative arrival times, referred to that of the preceding pulse(s) received, contain the data (message or information) transmitted. The invention neither makes use of the first derivative pulse nor encodes information via relative arrival times, preferring to allow arrival times to be determined by the dispersive and reflective characteristics of the physical channels and specifically avoiding the first derivative pulse due to its low-frequency nature.

The presence of derivative pulses of various orders constitutes frequency-
spectrum modulation and is completely independent of the signaling-pulse
timing which defines prior-art pulse time modulation methods. Indeed, the
prior-art systems all employ pulse-position or pulse-time modulation of one
variety or another, including pulse edge-timing modulation. The time-averaged
power spectra of these prior-art signals are essentially stationary for
random-data modulation, although the overall spectral width is strongly
(inversely) dependent on the basic generating-pulse width. The average power
spectrum of the individual first-order Gaussian derivative pulses is also highly
dependent on the pulses-to-pulse time deviation (pulse time or position
modulation). For small deviations, the spectrum is more concentrated around
the center frequency corresponding to the average pulse repetition rate, whereas
at higher deviations (approaching the average pulse-to-pulse interval), the
spectrum is more spread out (dispersed).

The spectra of higher-order derivative pulses are, in general, much broader and more uniform (better dispersed) than the prior-art signals. Except for the low-frequency limitations, which are often highly desirable, the higher-order pulse spectra are more continuous and less "multi-line" in nature than

competing technologies; they therefore more closely resemble the spectrum of true random noise and thus provide better signal concealment (low probability of detection).

Multiple Pulses—Multiple Bits per symbol

5 Significantly, due to the different pulse shapes /occupying the different frequency bands, higher-order pulses are identifiable by shape as belonging to a particular derivative order. Fig. 6 shows Gaussian-derivative, time-domain pulses of order 4, 8, 12, 16, and 20. The underlying Gaussian function is shown as a dashed line. As n increases, the width at zero time decreases, indicating
10 increasing frequency. The abscissa values are in units of the width of the initial square pulse.

 The technique of transmitting several pulses, each with a different derivative order, at the same time, is equivalent to transmitting a vector pulse or a code-word symbol having more than one bit. This capability immediately
15 opens up a much higher data rate than is possible with a system based on a single Gaussian-derivative pulse.

 The invention circumvents both the noise-susceptibility and the increased-power problems of existing multi-state data quadrature amplitude (m-QAM) modulations by using mutually orthogonal states for each bit transmitted. In
20 addition, each pulse has two distinct quadrature phases, effectively doubling the bits-per-symbol figure of merit. This orthogonality is an inherent property of the Hermite polynomials that underlie the pulse shape of the Gaussian-derivative functions.

 Fig. 7 shows 7 time-domain pulses for the orders 2 through 8 (the dashed
25 curves are of even order). The seven superimposed Gaussian derivative pulses of orders 2 through 8 compose a set of symbol codes for transmitting $2^7 = 128$ different possible symbols in a single composite pulse, or 255 possible symbols when pulse phase is used.

 Fig. 8 shows the combination, by simple addition, of the pulses for orders
30 3, 5, 6, and 8, thereby encoding the binary symbol 0101101 in the sum for transmission. The abscissa is in units of the characteristic time. It can be

appreciated that the combined pulse looks nothing like any of the original pulses.

Fig. 9 shows the power spectral density of the composite pulse representing the binary code 0101101. The abscissa is in GHz for a
5 characteristic time of 1 ns. The power-spectral density extends (at the -20-dB level) or 1%-power point approximately over 3.85 GHz for a characteristic time τ of 1 ns. The information in this frequency band may be extracted at the receiver by simply effecting a correlation in parallel of the received pulse with the pulses comprising the set of symbol codes. Since set is orthogonal when all
10 members have the same characteristic time τ , the result of such a correlation will show maximally strong peaks at just the code values transmitted. A specific implementation of this approach is detailed in later pages.

Suppose that the above pulse is received and processed with an array of correlators. The output of the array, in the absence of interferers and other
15 noise, reproduces the binary code *exactly* at zero correlation lag. To obtain knowledge of the time of the zero lag, a precursor pulse based on a single derivative Gaussian wavelet can be transmitted at a precisely known time several time constants prior to the transmission of the composite pulse. The precursor pulse is then used by the receiver to synchronize on each message
20 pulse and is an integral part of the coding scheme. For example, a receiver tuned to a slightly different precursor would attempt to decode the composite pulse with an incorrect lag value, obtaining nonsense for the decoded symbol. Correct correlation also requires a suitable integrating function that is implemented in electronic circuitry much as the derivative pulses were derived:
25 decoding then becomes a simple matter of correlating the incoming pulse in parallel with multiple candidate pulses.

Thus, a series of non-interfering (orthogonal) pulses can be transmitted simultaneously and subsequently received and decoded simultaneously, allowing multiple bits per transmitted symbol to be communicated. A parallel
30 correlator will respond appropriately in the presence of both broad- and narrow-band interferers.

Examples

Specific embodiments of the invention will now be further described by the following, nonlimiting examples which will serve to illustrate in some detail various features of significance. The examples are intended merely to facilitate
5 an understanding of ways in which the invention may be practiced and to further enable those of skill in the art to practice the invention. Accordingly, the examples should not be construed as limiting the scope of the invention.

Transmitter Architecture

Fig. 10 shows a block diagram of a transmitter in the preferred
10 embodiment, where derivative-pulse generators of orders 2 through 8 are used to generate 7 separate parallel-path signals, each corresponding to a specific derivative-order signal. (The number seven is chosen by way of example and is in no way limiting.) Looking at the overall signal flow, a digital data stream at lower left enters encoder 1007 to be processed and augmented by error
15 detection/correction bits, framing bits, and such. The encoded output from 1007 feeds spreader/demultiplexer/router block 1008, which via a predetermined algorithm and/or logic configuration processes and sorts the encoded data bits or units into 7 separate data streams 1013, which in turn feed each of the 7 parallel data-modulator blocks 1004.

20 Meanwhile, a master clock oscillator 1001 generates stable timing signals to control trigger generator 1002, which determines the pulse transmission timing interval. In this embodiment of the invention, these pulses have uniform time spacing and thus constitute a stable, single-frequency, unmodulated trigger or clock source to drive the 7 parallel derivative pulse generators 1003 through
25 the intervening programmable delay generator 1005. This block permits the relative phases of the 7 derivative pulses to be individually adjusted in a static sense but in this implementation is not used for modulation purposes. An additional output from 1005 feeds synchronization pulse generator 1006, which at a preselected time produces a specific-format sync pulse to assist the
30 associated receiver in data recovery at the other end of the transmission link.

The 7 aforementioned derivative-pulse generators 1003, designated by the derivative orders (2 through 8), each take the 7 independent trigger pulses and produce a shaped derivative (Gaussian or otherwise) pulse of the shapes shown in Fig. 6. These shaped pulses are sent to the aforementioned information
5 modulator blocks 1004, designated by the number of the respective derivative-order pulse being processed. The group of 7 encoded and routed modulation signals of group 1013 are also fed to their respective modulators in block 1004. Each of the signals of 1013 may be analog or digital, binary, ternary or multistate, as appropriate for the specific application.

10 Each modulator block in the group 1004 may implement analog modulation (i.e., amplitude, frequency, or phase -- amplitude modulation (AM), frequency modulation (FM) or phase modulation (PM) or digital modulation forms such as binary phase-shift keying (BPSK), phase-shift keying, FSK, minimum-shift keying (MSK) for standard binary signals. In addition, more
15 complex multistate modulation formats such as quadrature phase-shift keying (QPSK), offset quadrature phase-shift keying (OQPSK), quadrature amplitude modulation, multi-state data quadrature amplitude modulation, multi-state data phase-shift keying (MPSK), multi-state data frequency-shift keying (MFSK) and others are also quite useful in some applications. In addition,
20 amplitude-shift-keying (ASK), on-off keying (OOK) and others may be applied via straightforward techniques familiar to those skilled in the art. Further, ternary (3-state) and other multi-level modulations can be extremely useful in achieving the derivative-pulse modulation. Since a key aspect to the invention is the mutual orthogonality (statistical independence) of the various
25 derivative-pulse signals, the use of on-off keying, or in combination with standard binary phase-shift keying modulation (0° or 180° shift) is especially beneficial in optimizing the inter-signal orthogonality, since the normal derivative signal (noninverted state), inverted derivative signal (inverted state), or no signal at all ("off" state) are all orthogonal, even with data modulation.
30 This combination of modulation states is particularly advantageous for use of

ternary data states (+1, 0, =1). This modulation may be standard data, PN sequences or spread-data sequences as well.

The outputs of the parallel data modulators are combined with the previously mentioned sync pulse (from 1006) in summer/combiner block 1009, which is typically a straightforward linear added device but which may in some cases be implemented via digital switching means (dependent on the relative timing of the pulse set or other system consideration). The output from summer 1009 is then amplified up to an appropriate power level by amplifier 1010, which in turn feeds the transmitting antenna 1012 through matching network 1011, according to typical practice.

Fig. 11 shows a corresponding receiver according to the present invention. The radio frequency signal is received by antenna 1101, coupled through matching network 1102 and processed by conventional high-gain radio-frequency circuitry in receiver front-end block 1103. This device provides tuning adjustments, filtering automatic gain- and frequency-control functions, and overall control to produce a stable-amplitude, relatively low-noise wideband signal at 1104 which is routed to the 7 parallel synchronous pulse-correlation detectors in group 1109. Meanwhile, the output of 1103, line 1104, feeds a synchronization subsystem block 1105 which recovers the predetermined sync pulse, locks to the timing signal embedded therein, and corrects the frequency of the master receiver-system clock 1106. The master clock in turn controls the timing of the trigger generators (one for each derivative-pulse generator) at 1107. Each trigger pulse initiates the generation of the appropriate derivative-pulse generator block in group 1108, which is designed to produce the same precise pulse shape as its corresponding unit 1003 in the transmitter system of Fig. 10. Again in Fig. 11, the outputs of all 7 pulse generators at 1108 are fed into the reference inputs of the corresponding synchronous pulse correlators in group 1109. These detectors correlate, integrate, and filter the respective received pulses with their locally generated versions and implement decision logic functions to produce the respective data outputs at 1113. These 7 output-decision signals, representing the 7 detected information streams (assumed

digital for the moment), are combined/decoded/assembled in data multiplexer block 1110 to produce a final series bitstream output at 1111. Meanwhile, the assembled data stream at 1111 is fed back through line 1112 to the synchronizer block at 1105 to assist in system sync lock-up (acquisition) and tracking. For spread-spectrum signals, the final output stream 1111 may be sent to a common despreader 1113 to extract the original data from the composite spread-spectrum clipstream. Alternatively, each corrector/detector module in group 1109 can be equipped with a conventional amplitude-shift keying, frequency-shift keying, phase-shift keying, quadrature phase-shift keying, offset quadrature phase-shift keying, minimum-shift keying, multi-state data phase-shift keying, multi-state data frequency-shift keying, multi-state data quadrature amplitude modulation, or other appropriate type of data demodulator to extract modulation information from the correlated derivative-pulse outputs; in this case, the demodulated (unspread) data would appear on the parallel paths designated 1113. In the case of spread-spectrum modulation on each of the derivative signals, a spread-spectrum decoder would be inserted within each detector block in 1109 between the data-demodulation and the output points feeding lines 1113. An optional despreader 1113 is coupled to the stream 1111 and despread data out 1114.

Fig. 12 shows a first transmitter block diagram where the higher-order derivative-pulse shaping features of the present invention are applied to existing-art devices to improve their bandwidth efficiency or reduce interference to other users and bands. The output of the fast-pulse block is presented to a specialized Nth-order Gaussian (or other) filter circuit (either passive or active) of an appropriate shape to produce the desired higher-order (i.e., 2nd or above) Gaussian-derivative or other selected pulse shape.

Fig. 12 shows a high-speed clock 1210 used to provide triggering and synchronization that is coupled through a digital frequency divider to both a pseudorandom polynomial generator block 1220 and a programmable delay circuit 1240 to permit adjustment of the actual fast-pulse timing. The delay circuit 1240 is coupled to a fast-pulse generator block 1250. The frequency of the high-speed clock 1210 is on the order of the inverse of the generator-stage

output pulse duration. Serial digital data to be transmitted is modulo-2 multiplied by an exclusive-OR gate 1230 with the pseudorandom polynomial data stream emerging from the PN-generator block 1220. Internally, this includes a standard, serial shift-register with programmable feedback interconnects placed at appropriate points to produce one of several desired digital polynomials; the detailed design of this type of generator is well known in the spread-spectrum communications and coding art and will not be discussed further. Given that the block 1220 produces a desirable pseudorandom code sequence (e.g., a Gold, maximal-length, or Kasami code), the output bit-stream (often termed chipping sequence to distinguish it from the conventional data bits), is XORed with the true data bits and used to control the programmable delay circuit 1240 in a similarly pseudorandom manner. This in turn forces the timing of the emerging narrow pulses from the pulse generator block 1250 to be varied according to the statistics of the PN code (altered by the data, obviously). Note that the actual width of the narrow (fast) pulses the PW control line can also be modulated by another external signal, such as that obtained from a second PN generator circuit; this could employ the same or different codes as the original PN unit, and may also be modulated by the actual data stream as required. The pulse generator block 1250 is coupled to a Gaussian filter 1260 which is coupled to a radio-frequency amplifier 1270 which is in-turn coupled to a matching network 1280 and an antenna 1290.

Fig. 13 shows a second transmitter block diagram of the same variety. The only significant difference from the previous example is that a master clock 1310 drives a programmable-delay monostable multivibrator circuit 1320 (commonly termed a one-shot), which generates a pseudorandom variable pulse width at its output. This pseudorandom variable pulse width in turn time-modulates the trigger signal for the separate fast-pulse generator 1350, which also may have its output pulse width controlled as before. The symbol "O/S," in Fig. 13 indicates the programmable delay "one-shot." The master clock 1310 is also coupled to a divider 1330 which in-turn is coupled to the generator 1320 and the exclusive-OR gate 1330. As in the previous example, the pulse

generator block 1350 is coupled to the Gaussian filter 1360 which is coupled to the radio frequency amp 1370 which is in-turn coupled to the matching net 1380 and the antenna 1390. The principal reason for this alternate version is to facilitate partitioning of the standard-speed and very high-speed portions of the transmitter circuitry for optimum layout and fabrication as a custom integrated-circuit chip (ASIC).

Receiver Architecture

Fig. 14 shows a wideband or ultra-wideband (UWB) receiver for implementing an embodiment of the invention corresponding to the previous transmitters of Figs. 12 and 13. A broadband radio frequency signal from a receiving antenna 1400 is bandpass-filtered by a wideband filter/low-noise amp 1410 to admit the desired frequency range and simultaneously reject out-of-band signals and interference. Appropriate Gaussian pulse-shaping and/or equalization may also be performed by the wideband filter/low-noise amp 1410. The following low-noise, front-end amplifier (LNA) then boosts the signal amplitude to a useful level. An integral automatic gain-control (AGC) loop 1420 that includes a peak detector 1430 regulates the LNA's output to accommodate both high and low input-signal levels while maintaining good amplifier linearity. The LNA output also drives a synchronization-trigger detector circuit 1440 which is used to start the synchronization process in the downstream portion of the receiver system. Typically, a sync burst or preamble (or perhaps a specially configured data sequence) will be transmitted at or near the beginning of each data block to facilitate rapid synchronization and acquisition of the data stream in the receiver, although this is not absolutely mandatory. If a valid trigger signal is detected, the pulse is gated by a gate 1450 through to a correlator 1460. The correlator 1460 circuit performs the template-matching of the incoming Gaussian pulse stream with the selected-order pulse shape. The output from the correlator 1460 then drives the following data demodulator/decoder 1465 to secure the desired output data stream. Additional signal tracking and synchronization are handled by a combination of a feedforward phase-lock synchronizer driven by the correlator output and a

feedback locking path derived from the final received data bitstream. The combined outputs drive a variable-frequency clock 1480, which in turn, regulates the readout rate of a PN generator 1490, thus acquiring (and maintaining) lock with the local PN sequence in time and phase to the incoming
5 PN chipstream arriving from the transmitter. Thus, Fig. 14 shows a receiver block diagram with synchronization and demodulation details.

Note that adaption of standard readily commercially available direct-sequence spread spectrum receiver technology is suitable for some embodiments of the invention. Specifically, receiver methods presently known
10 to be used in spread-spectrum receivers are sufficient. An alternative approach is to employ an analog correlator based on the spectral properties of the precursor synchronization pulse. To avoid issues of time synchronization, this correlator 1460 can take the form of a matched filter having maximal response, relative to its input power, when a pulse of the desired shape is present.

15 While not being limited to any particular performance indicator or diagnostic identifier, preferred embodiments of the invention can be identified one at a time by testing for the ability to equalize the derivative shapes to various antenna and transmission-channel characteristics. Another way to seek preferred embodiments is to test the ability of the broad spectrum being
20 transmitted to be notch filtered. Another way to seek preferred embodiments is to test for minimal dispersive effects.

Practical Applications of the Invention

A practical application of the invention that has value within the technological arts is clandestine communications due to its very low transmitted
25 radio frequency power. Further, the invention is useful in conjunction with reliable communications in the presence of severe multipath interference, or in conjunction with high data rate spread spectrum communications, or the like. There are virtually innumerable uses for the invention, all of which need not be detailed here.

Advantages of the Invention

A pulse transmission transceiver architecture for low power communications, representing an embodiment of the invention, can be cost effective and advantageous for at least the following reasons. The invention makes more practical (i.e., commercially feasible) the class of time-domain, spread spectrum communications. The invention significantly reduces lower-frequency emissions from time-domain spread spectrum communication modes, thereby reducing potentially harmful interference to existing radio frequency services and users. The invention can make greater receiver selectivity possible. The invention also makes transmission of multiple bits per time-domain symbol possible.

All the disclosed embodiments of the invention described herein can be realized and practiced without undue experimentation. Although the best mode of carrying out the invention contemplated by the inventors is disclosed above, practice of the invention is not limited thereto. Accordingly, it will be appreciated by those skilled in the art that the invention may be practiced otherwise than as specifically described herein.

For example, although the time-domain transceiver architecture described herein can be a separate module, it will be manifest that the time-domain transceiver architecture may be integrated into the hardware/software with which it is associated. Further, all the disclosed elements and features of each disclosed embodiment can be combined with, or substituted for, the disclosed elements and features of every other disclosed embodiment except where such elements or features are mutually exclusive.

It will be manifest that various additions, modifications and rearrangements of the features of the invention may be made without deviating from the spirit and scope of the underlying inventive concept. It is intended that the scope of the invention as defined by the appended claims and their equivalents cover all such additions, modifications, and rearrangements. The appended claims are not to be interpreted as including means-plus-function limitations, unless such a limitation is explicitly recited in a given claim using

the phrase "means-for." Expedient embodiments of the invention are differentiated by the appended subclaims.

CLAIMS

What is claimed is:

1. A method of pulse transmission communications, comprising:
generating a pulse-signal waveform;
5 transforming said pulse-signal waveform into at least one higher-order derivative waveform; and
transmitting said at least one higher-order derivative waveform as an emitted pulse.
- 10 2. The method of claim 1, wherein said at least one higher-order derivative waveform is modulated by an on/off information signal.
3. The method of claim 2, further comprising modulating said at least one higher-order derivative waveform with an analog technique selected from the
15 group consisting of amplitude modulation, frequency modulation, and phase modulation.
4. The method of claim 2, further comprising modulating said at least one higher-order derivative waveform with a digital binary technique selected from
20 the group consisting of phase-shift keying, binary phase-shift keying, quadrature phase-shift keying, offset quadrature phase-shift keying, frequency-shift keying, minimum-shift keying, amplitude shift keying, and on-off keying.
5. The method of claim 2, further comprising modulating said at least one
25 higher-order derivative waveform with a multi-state technique selected from the group consisting of multi-state data frequency-shift keying, multi-state data phase-shift keying, multi-state data quadrature amplitude modulation, ternary digital modulation, and n-state digital modulation.

6. The method of claim 1, wherein transforming said pulse-signal waveform into said at least one higher-order derivative waveform includes shaping a frequency spectrum of said emitted pulse to reduce lower frequency emissions.
- 5 7. The method of claim 1, wherein transforming said pulse-signal waveform into said at least one higher-order derivative waveform includes orthogonally encoding a single bit signal into said emitted pulse.
8. The method of claim 1, wherein said at least one higher-order derivative
10 waveform is modulated by a multiple-bit signal.
9. The method of claim 7, wherein orthogonally encoding a multiple-bit signal into said emitted pulse includes adding a plurality of higher-order derivative waveforms that are a function of said pulse-signal waveform.
15
10. The method of claim 1, wherein said pulse-signal waveform represents a Gaussian function.
11. The method of claim 1, wherein said pulse-signal waveform is a time-
20 domain function $\exp(-x^{2n}/2a)$, where x is the function in each domain, a is a constant, and n is an integer that represents an order.
12. The method of claim 1, wherein transforming said pulse-signal into said at least one higher-order derivative includes transforming said pulse signal with
25 active circuitry.
13. The method of claim 1, wherein generating said pulse-signal waveform includes generating said pulse signal waveform from a stored digital version.
14. The method of claim 1, further comprising repeating the step of generating
30 said pulse-signal waveform to achieve orthogonal time hopping.

15. The method of claim 1, further comprising repeating at least one step selected from the group consisting of generating said pulse-signal waveform and transforming said pulse-signal waveform into said at least one higher-order derivative waveform, to achieve orthogonal frequency hopping.
16. The method of claim 1, further comprising repeating at least one step selected from the group consisting of generating said pulse-signal waveform and transforming said pulse-signal waveform into said at least one higher-order derivative waveform, to achieve combined orthogonal frequency-time hopping.
17. An electromagnetic waveform, comprising: an emitted pulse that is produced from at least one higher-order derivative waveform of a pulse-signal waveform.
18. The electromagnetic waveform of claim 17, wherein a frequency spectrum of said emitted pulse is shaped to reduce at least one member selected from the group consisting of lower frequency emissions and higher frequency emissions.
19. The electromagnetic waveform of claim 17, wherein said emitted pulse orthogonally encodes a multiple-bit signal.
20. The electromagnetic waveform of claim 19, wherein said multiple-bit signal is encoded into said emitted pulse by adding a plurality of higher-order derivative waveforms that are a function of said pulse-signal waveform.
21. The electromagnetic waveform of claim 17, wherein said pulse-signal waveform is a Gaussian function.

22. The electromagnetic waveform of claim 17, wherein said pulse-signal waveform represents a time-domain function $\exp(-x^{2n}/a)$, where x is the function in each domain, a is a constant, and n is an integer that represents an order.
- 5 23. An orthogonal time-hopping electromagnetic waveform that includes the electromagnetic waveform of claim 17.
24. An orthogonal frequency-hopping electromagnetic waveform that includes the electromagnetic waveform of claim 17.
- 10 25. A combined orthogonal frequency-time hopping electromagnetic waveform that includes the electromagnetic waveform of claim 17.
26. A pulse transmission transmitter, comprising:
- 15 a clock;
a trigger signal generator coupled to said clock;
at least one higher-order derivative pulse generator coupled to said trigger signal generator;
a selector circuit coupled to said at least one higher-order derivative pulse
20 generator; and
an output-interface device coupled to said at least one higher-order derivative pulse generator.
27. The pulse transmitter of claim 26, further comprising a programmable
25 delay circuit coupled to said trigger signal generator.
28. The pulse transmitter of claim 26, further comprising at least one information modulator circuit coupled to said at least one higher-order pulse generator wherein said at least one information modulator circuit provides at
30 least one function selected from the group consisting of amplitude modulation, frequency modulation, phase modulation, digital binary modulation, phase-shift

keying, binary phase-shift keying, quadrature phase-shift keying, offset quadrature phase-shift keying, frequency-shift keying, minimum-shift keying, amplitude shift keying, on-off keying, multi-state data frequency-shift keying, multi-state data phase-shift keying, multi-state data quadrature amplitude modulation, ternary digital modulation, and n-state digital modulation.

10

29. The pulse transmitter of claim 26, further comprising at least one summation circuit coupled to said at least one higher-order derivative pulse generator.

30. The pulse transmitter of claim 29, wherein said at least one summation circuit includes at least one member selected from the group consisting of a passive combiner, an active summing circuit, and a digital combining circuit.

15

31. The pulse transmitter of claim 26, further comprising a power amplifier coupled to said at least one higher-order derivative pulse generator.

32. The pulse transmitter of claim 26, further comprising data encoder circuitry coupled to said at least one higher-order derivative pulse generator.

20

33. The pulse transmitter of claim 28, further comprising data encoder circuitry coupled to said at least one information modulator circuit.

25

34. The pulse transmitter of claim 28, further comprising a spread-spectrum data modulator coupled to said at least one information modulator circuit.

35. The pulse transmitter of claim 34, wherein said spread-spectrum data modulator provides at least one spread-spectrum mode selected from the group consisting of direct sequence, frequency hopping, and time-hopping.

30

36. The pulse transmitter of claim 26, including a demultiplexer coupled to said at least one higher-order derivative pulse generator.
37. The pulse transmitter of claim 28, further comprising a demultiplexer
5 coupled to said at least one information modulator circuit.
38. The pulse transmitter of claim 29, further comprising a synchronization-pulse generator coupled to said at least one summation circuit.
- 10 39. The pulse transmitter of claim 26, further comprising a matching network connected to said output-interface device.
40. The pulse transmitter of claim 26, wherein said output-interface includes an antenna.
15
41. The pulse transmission transmitter of claim 27, wherein said programmable delay circuit includes a programmable-delay monostable multivibrator circuit.
- 20 42. The pulse transmission transmitter of claim 26, wherein said at least one higher-order derivative pulse generator includes a filter.
43. The pulse transmission transmitter of claim 42 where said filter includes a Gaussian filter.
25
44. A one-chip integrated circuit including the pulse transmission transmitter of claim 26.
45. A pulse transmission receiver, comprising:
30 a front-end amplification/processing circuit;

a synchronization circuit coupled to said front-end amplification/processing circuit;

a clock coupled to said synchronization circuit;

a trigger signal generator coupled to said clock; and

5 at least one higher-order derivative pulse generator coupled to said trigger signal generator.

46. The pulse transmission receiver of claim 45, further comprising at least one received pulse detection circuit coupled to said at least one higher-order
10 derivative pulse generator.

47. The pulse transmission receiver of claim 46, wherein said at least one received pulse detection circuit includes a synchronous pulse correlator.

15 48. The pulse transmission receiver of claim 47, wherein said at least one higher-order derivative pulse generator includes a data demodulator.

49. The pulse transmission receiver of claim 48, wherein said data demodulator provides at least one function selected from the group consisting of
20 amplitude demodulation, frequency demodulation, phase demodulation, digital binary demodulation, phase-shift keying, binary phase-shift keying, quadrature phase-shift keying, offset quadrature phase-shift keying, frequency-shift keying, minimum-shift keying, amplitude shift keying, on-off keying, multi-state data frequency-shift keying, multi-state data phase-shift keying, multi-state data
25 quadrature amplitude demodulation, ternary digital demodulation, and n-state digital demodulation.

50. The pulse transmission receiver of claim 45, further comprising a programmable delay circuit connected between said trigger signal generator and
30 said at least one higher-order derivative pulse generator.

51. The pulse transmission receiver of claim 45, including a multiplexer coupled to said at least one higher-order derivative pulse generator.
52. The pulse transmission receiver of claim 51, further comprising a data
5 despreaders coupled to said multiplexer.
53. The pulse transmission receiver of claim 52, further comprising a connection between said data despreaders and said synchronization circuit adapted to provide automatic frequency/synchronization control.
- 10 54. The pulse transmission receiver of claim 45, further comprising a matching network connected to said front-end amplification/processing circuit;.
55. The pulse transmission receiver of claim 54, further comprising an
15 antenna coupled to said matching network.
56. The pulse transmission receiver of claim 45, wherein said at least one higher-order derivative pulse generator includes a filter.
- 20 57. The pulse transmission receiver of claim 56 where said filter includes a Gaussian filter.
58. A one-chip integrated circuit including the pulse transmission receiver of claim 45.
- 25 59. A method of pulse transmission communications, comprising:
generating a pulse position modulated signal waveform;
transforming said pulse position modulated signal waveform into at least one higher- order derivative waveform; and
30 transmitting said at least one higher-order derivative waveform as an emitted pulse.

60. The method of claim 59, wherein transforming said pulse position modulated signal waveform into said at least one higher-order derivative waveform includes shaping a frequency spectrum of said emitted pulse to
5 reduce lower frequency emissions.
61. The method of claim 59, wherein transforming said pulse position modulated signal waveform into said at least one higher-order derivative waveform includes orthogonally encoding or modulating a single bit signal into
10 said emitted pulse.
62. The method of claim 61, wherein orthogonally encoding a multiple-bit signal into said emitted pulse includes adding a plurality of higher-order derivative waveforms that are a function of said pulse-signal waveform.
15
63. The method of claim 59, wherein said pulse position modulated signal waveform represents a Gaussian function.
64. The method of claim 59, wherein said pulse-signal waveform is a time-
20 domain function $\exp(-x^{2n}/a)$, where x is the function in each domain, a is a constant, and n is an integer that represents an order.
65. The method of claim 59, wherein transforming said pulse position modulated signal into at least one higher-order derivative includes transforming
25 said pulse position modulated signal with active circuitry.
66. The method of claim 59, wherein generating said pulse position modulated signal waveform includes generating said pulse position modulated signal from a stored digital version.

30

67. The method of claim 59, further comprising repeating the step of generating said pulse position modulated signal waveform to achieve orthogonal time hopping.
- 5 68. The method of claim 59, further comprising repeating at least one step selected from the group consisting of generating said pulse position modulated signal waveform and transforming said pulse position modulated signal waveform into said at least one higher-order derivative waveform to achieve orthogonal frequency hopping.
- 10 69. An electromagnetic waveform, comprising: an emitted pulse that is produced from at least one higher-order derivative waveform of a pulse position modulated signal waveform.
- 15 70. The electromagnetic waveform of claim 69, wherein a frequency spectrum of said emitted pulse is shaped to reduce lower frequency emissions.
71. The electromagnetic waveform of claim 69, wherein said emitted pulse orthogonally encodes a multiple bit signal.
- 20 72. The electromagnetic waveform of claim 71, wherein said multiple bit signal is encoded into said emitted pulse by adding a plurality of higher-order derivative waveforms that are a function of said modulated pulse-signal waveform.
- 25 73. The electromagnetic waveform of claim 69, wherein said pulse position modulated signal waveform represents a Gaussian or other appropriate mathematical function.
- 30 74. An orthogonal time-hopping electromagnetic waveform that includes the electromagnetic waveform of claim 69.

75. An orthogonal frequency-hopping electromagnetic waveform that includes the electromagnetic waveform of claim 69.

- 5 76. A pulse transmission transmitter, comprising:
a clock;
a pseudorandom polynomial generator coupled to said clock, said
pseudorandom polynomial generator having a polynomial load input;
an exclusive-OR gate coupled to said pseudorandom polynomial
10 generator, said exclusive-OR gate having a serial data input;
a programmable delay circuit coupled to both said clock and said
exclusive-OR gate;
a pulse generator coupled to said programmable delay circuit;
a filter coupled to said pulse generator; and
15 an antenna coupled to said matching network.

77. The pulse transmission transmitter of claim 76, wherein said
programmable delay circuit includes a programmable-delay monostable
multivibrator circuit.

20

78. The pulse transmission transmitted or claim 76, herein said filter includes
a Gaussian filter.

79. The pulse transmitter of claim 76, wherein said clock, said pseudorandom
25 polynomial generator, said exclusive-OR gate, said programmable delay circuit,
said pulse generator and said filter all compose a one-chip integrated circuit.

80. A pulse transmission receiver, comprising:
a correlator;
30 a demodulation decoder coupled to said correlator;
a clock coupled to said demodulation decoder; and

a pseudorandom polynomial generator coupled to both said correlator and said clock.

81. The pulse transmission receiver of claim 80, further comprising a gate
5 coupled to said correlator.

82. The pulse transmission receiver of claim 81, further comprising a trigger
coupled to
said correlator.

10

83. The pulse transmission receiver of claim 82, further comprising an
integral automatic gain-control loop coupled to both said gate and said trigger.

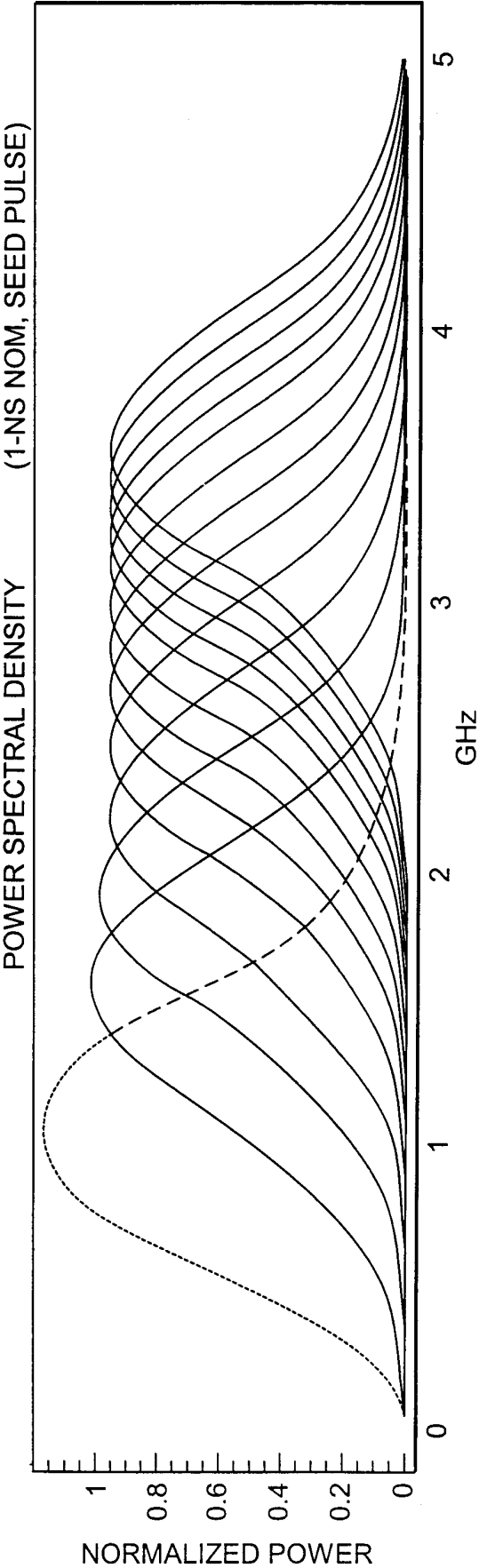


FIG. 1

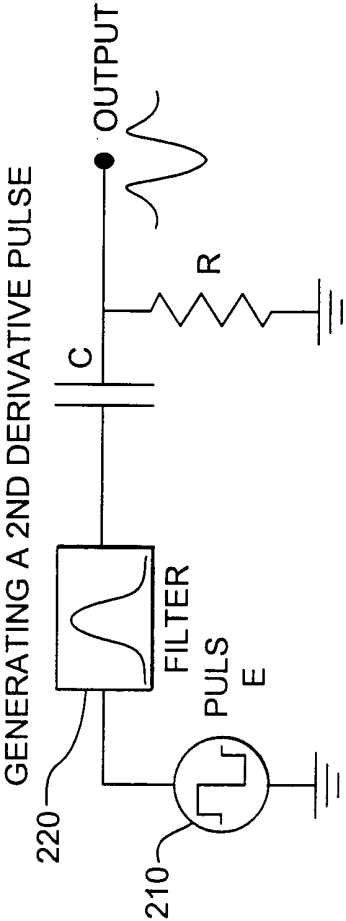
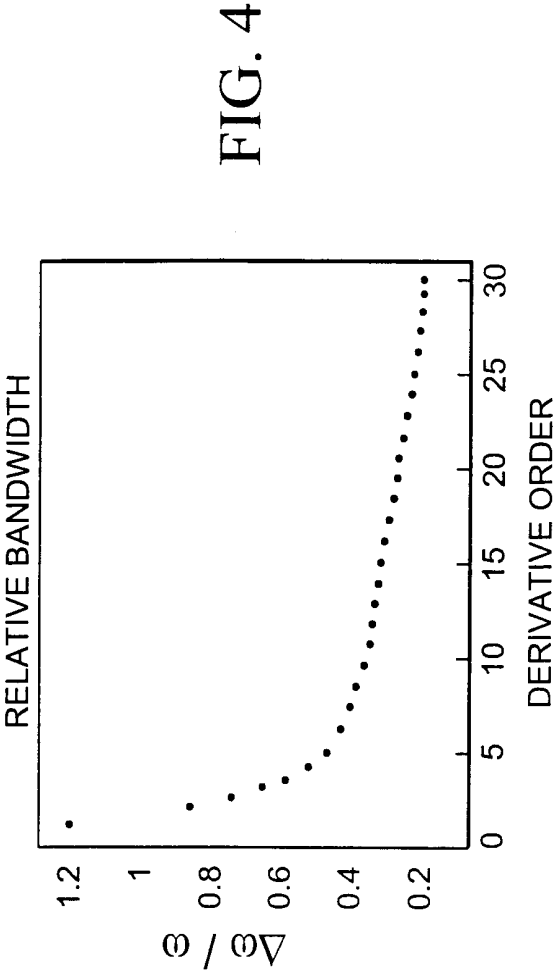
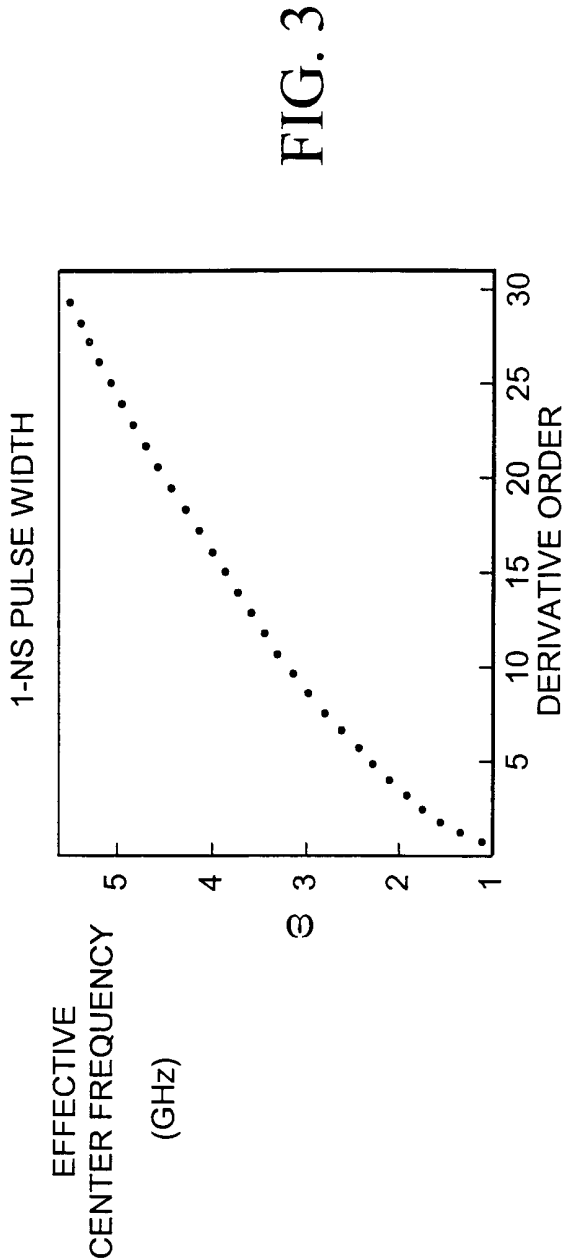
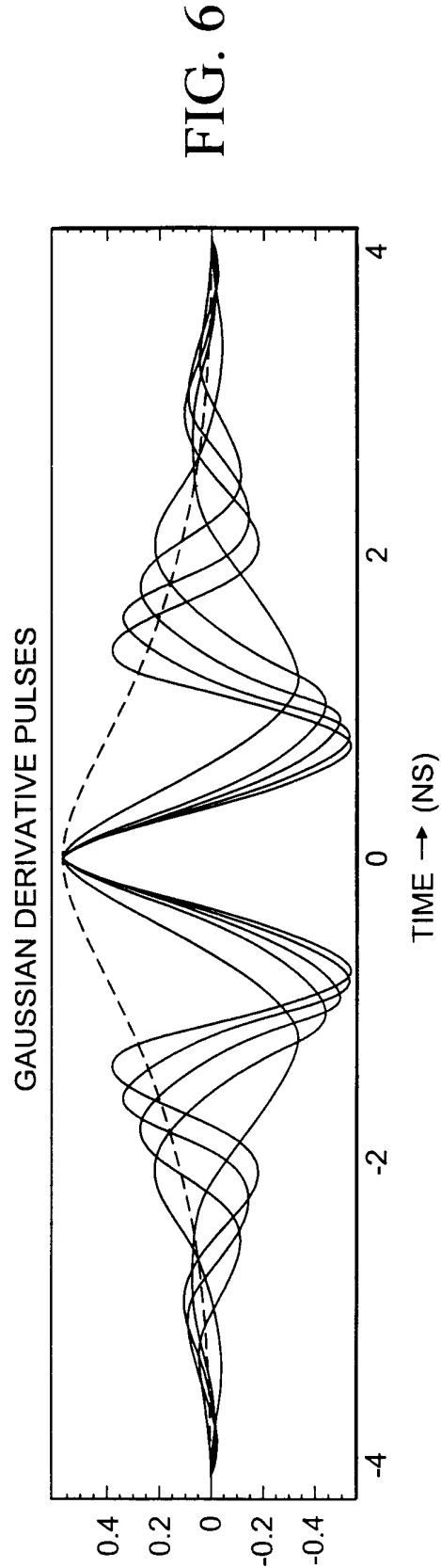
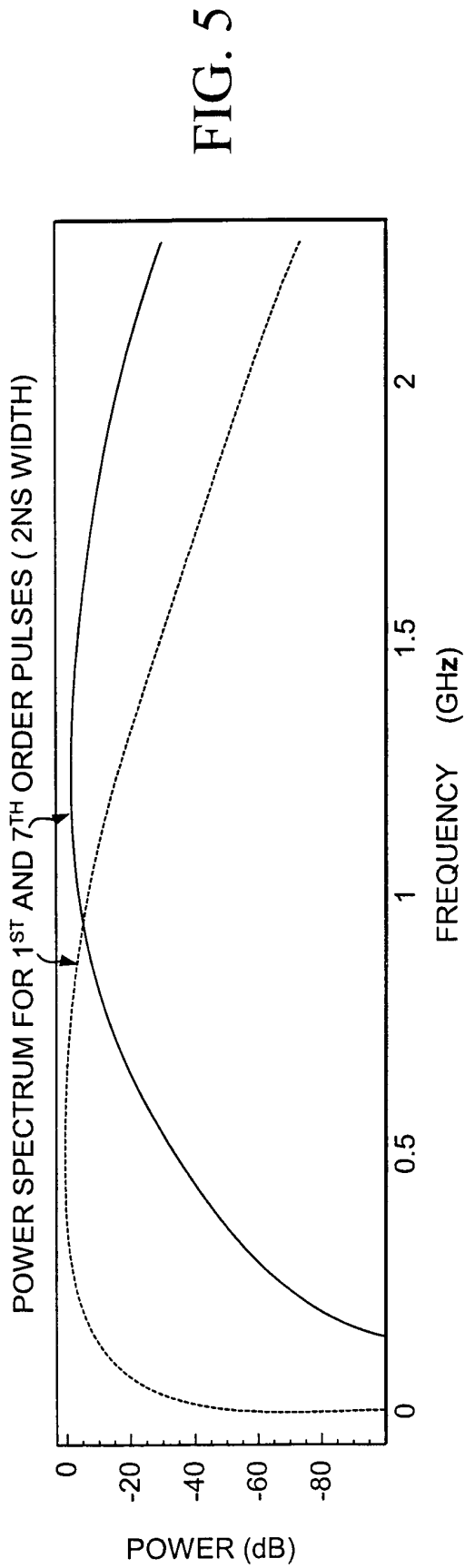
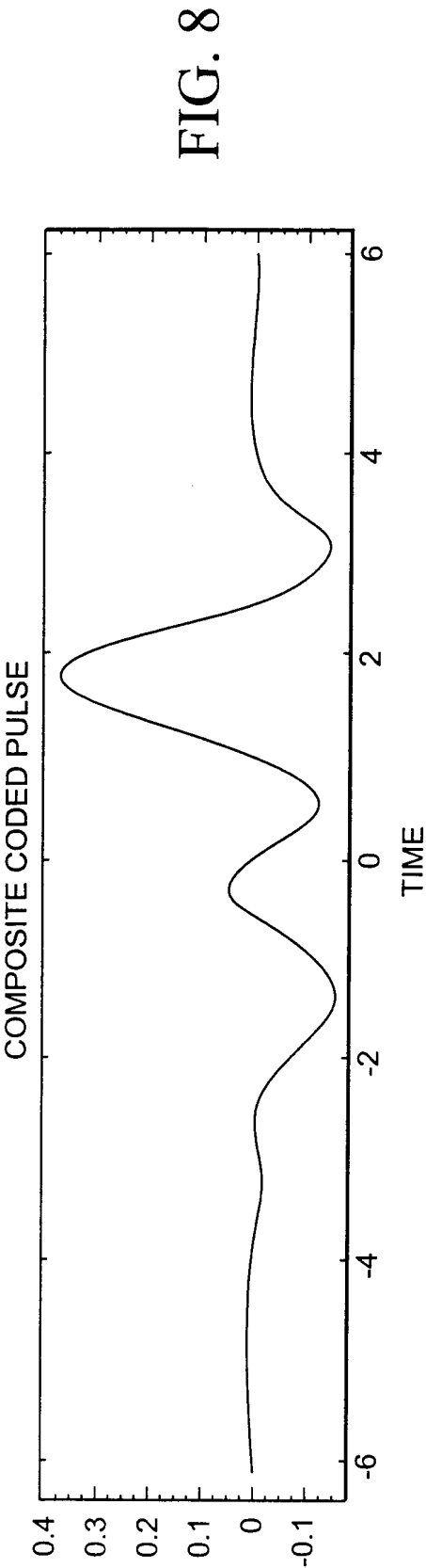
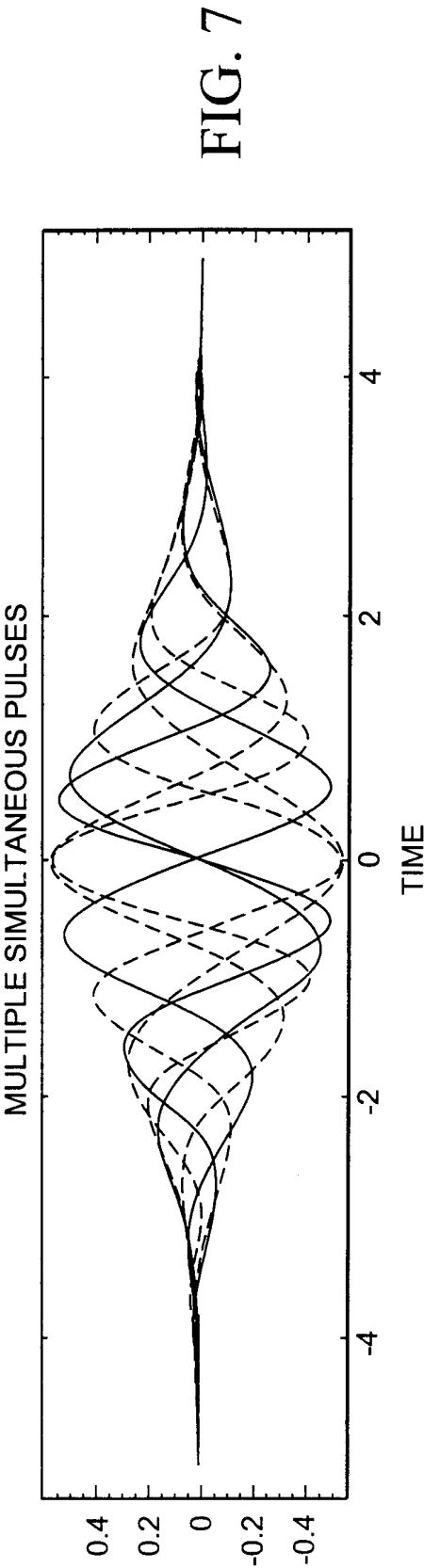


FIG. 2







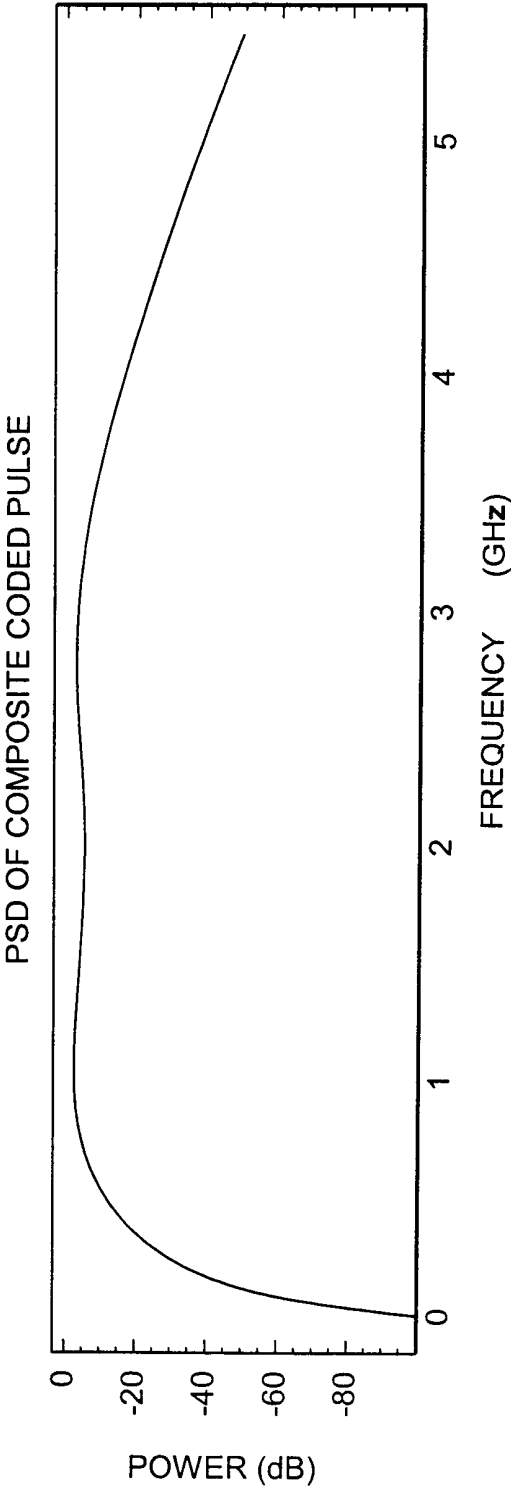


FIG. 9

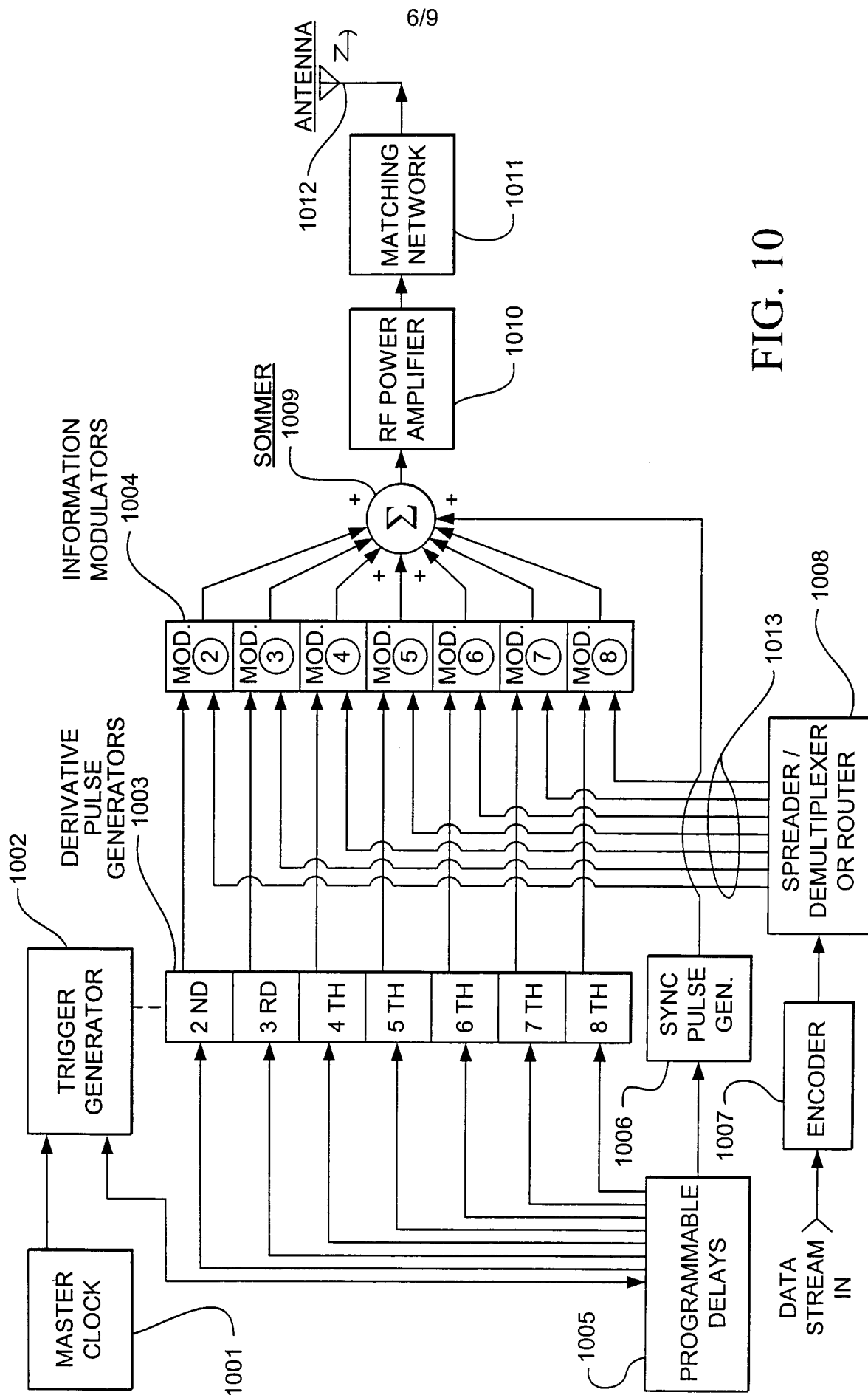
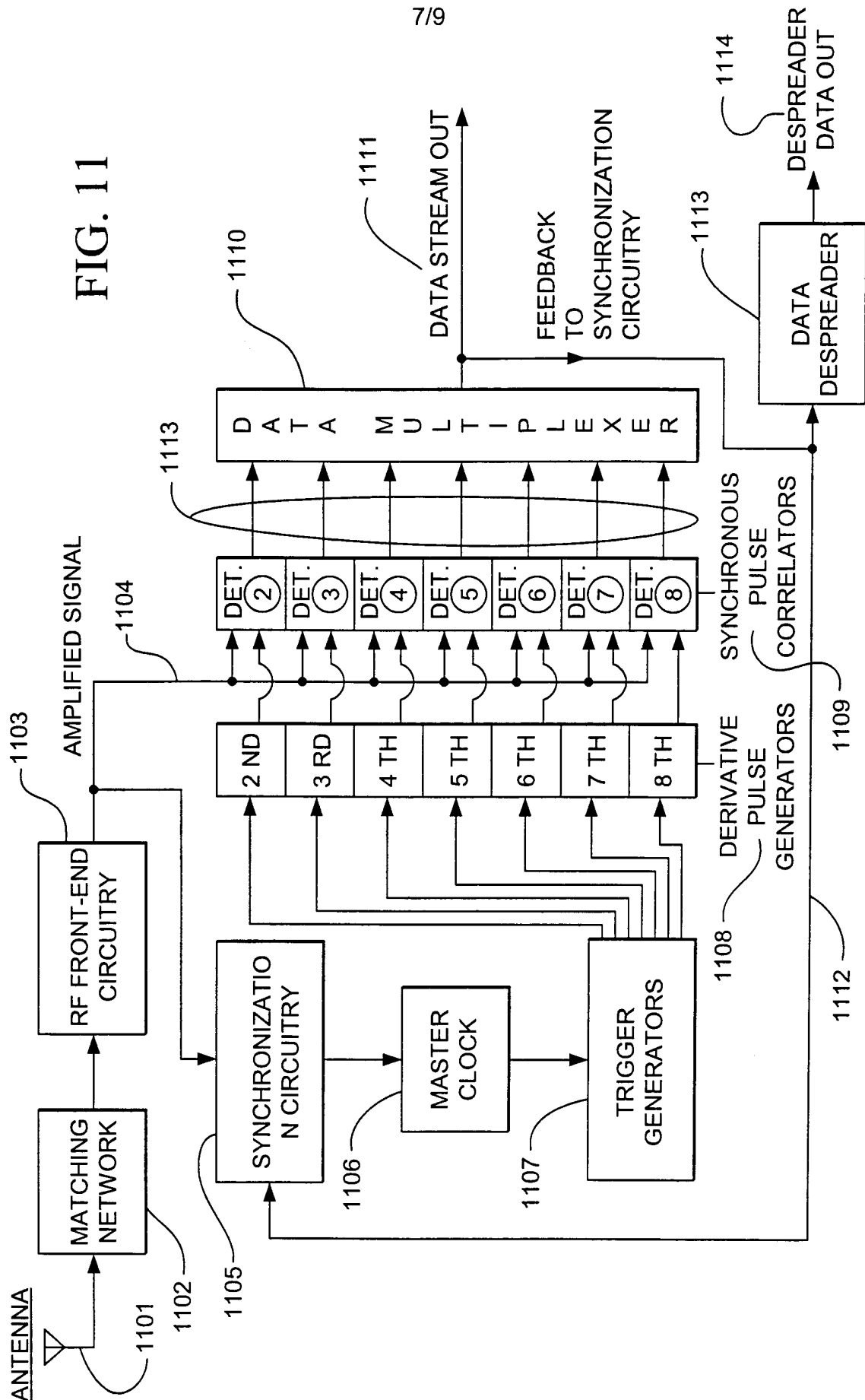


FIG. 10

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FIG. 11



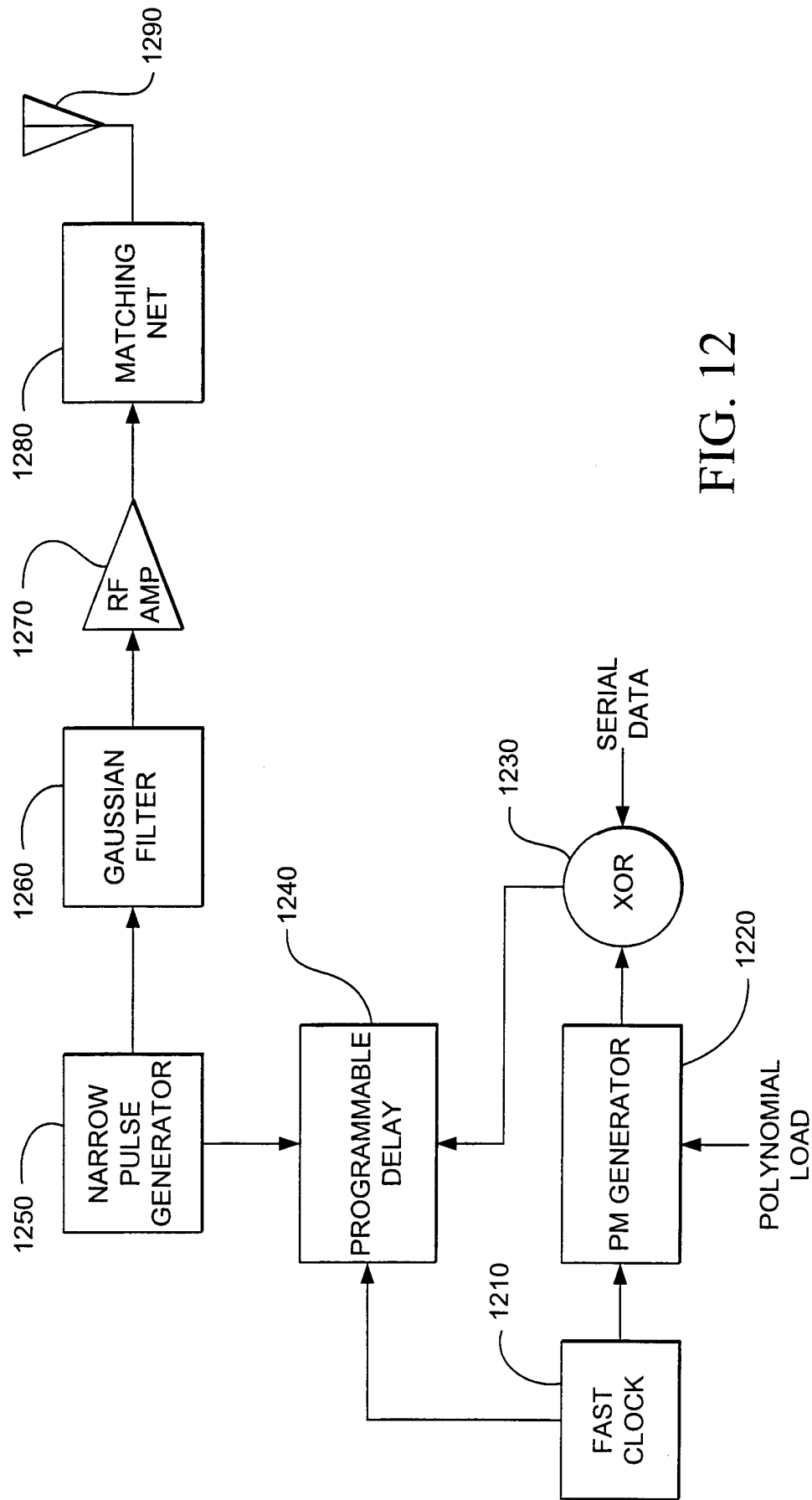
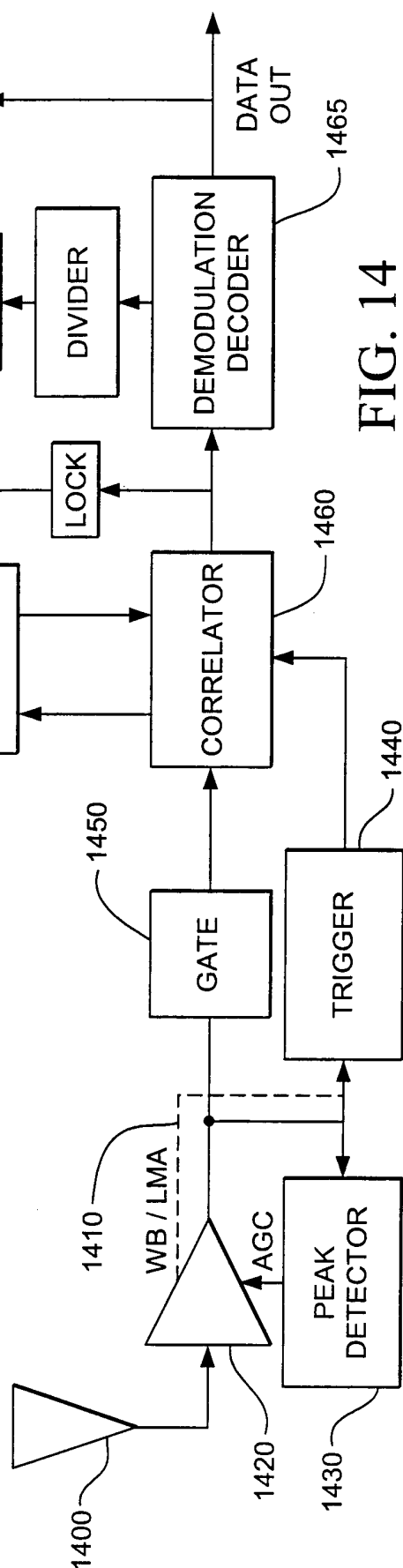
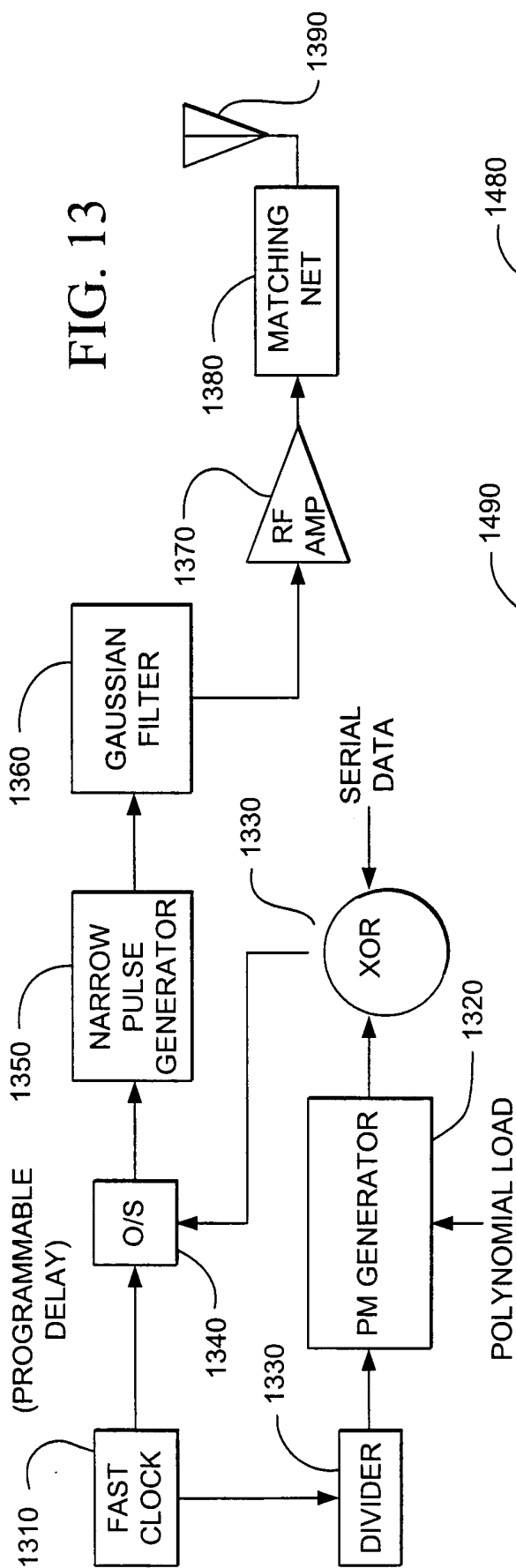


FIG. 12



INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 00/24726

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H04L25/49 H04L25/03 H04B1/69 H04B14/02

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04L H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 98 34339 A (MASSACHUSETTS INST TECHNOLOGY) 6 August 1998 (1998-08-06)	1-8, 10-14, 17-19, 21-23
Y	abstract	26, 28, 31-35, 39, 40, 42-49, 54-61, 63-67, 69-71, 73, 74
A	page 12, line 5 -page 13, line 13	9, 15, 16, 20, 24, 25, 27, 29, 30, 36-38, 41, 50-53,
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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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Date of the actual completion of the international search

15 January 2001

Date of mailing of the international search report

25/01/2001

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INTERNATIONAL SEARCH REPORT

Internat Application No

PCT/US 00/24726

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
	<p>page 17, line 25 -page 19, line 25 figures 1,2A,3A,3B,3C,4,6A,6B,6C ----</p>	62,68, 72,75
X	EP 0 851 603 A (LUCENT TECHNOLOGIES INC) 1 July 1998 (1998-07-01)	76,77, 79,80
Y	column 2, line 21 - line 55 column 6, line 26 -column 9, line 27 figures 2-4	78
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A	abstract	1-25,27, 29,30, 36-38, 41, 50-53,76
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Y	ELMIRGHANI J ET AL: "SLOT AND FRAME SYNCHRONISATION IN OPTICAL FIBER PPM SYSTEMS WITH DISPERSED PULSE SHAPES" JOURNAL OF OPTICAL COMMUNICATIONS,DE,FACHVERLAG SCHIELE & SCHON, BERLIN, vol. 15, no. 1, 1994, pages 5-10, XP000422686 ISSN: 0173-4911	59-61, 63-67, 69-71, 73,74
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INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 00/24726

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