



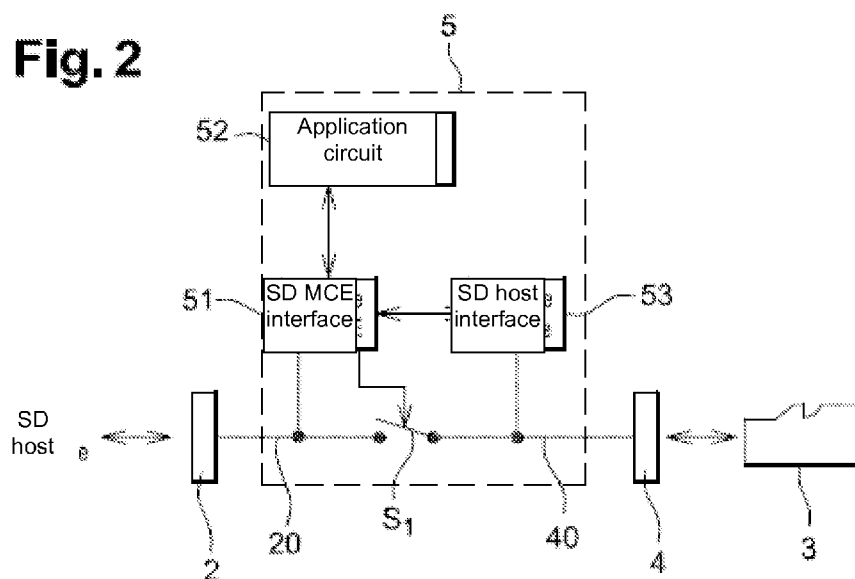
- (51) **International Patent Classification:**
G11C 5/00 (2006.01) *G06F 13/00* (2006.01)
- (21) **International Application Number:**
PCT/EP2009/058286
- (22) **International Filing Date:**
1 July 2009 (01.07.2009)
- (25) **Filing Language:** English
- (26) **Publication Language:** English
- (30) **Priority Data:**
08290660.3 4 July 2008 (04.07.2008) EP
- (71) **Applicant** (for all designated States except US):
GEMALTO SA [FR/FR]; Intellectual Property Dpt, 6 rue de la Verrerie, F-92190 Meudon (FR).
- (72) **Inventors; and**
- (75) **Inventors/Applicants** (for US only): **THILL, Michel** [FR/FR]; c/o GEMALTO SA, Intellectual Property Dpt, 6 rue de la Verrerie, F-92197 Meudon Cedex (FR). **MAR-SEILLE, François-Xavier** [FR/FR]; c/o GEMALTO SA, Intellectual Property Dpt, 6 rue de la Verrerie, F-92197 Meudon Cedex (FR).
- (81) **Designated States** (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PE, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

- (84) **Designated States** (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

- with international search report (Art. 21(3))
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))

(54) **Title:** MEMORY CARD ADAPTER**Fig. 2**

(57) **Abstract:** An adapter allows to connect two electronic devices at the same port, at the same time. The device allows to actuate two devices at one single port which originally allows the connection of one single device. The adapter device includes a primary connector 2 to be connected to a host device taking up said adapter device, a secondary connector 4 to take up the memory card 3, an interrupter S1 connecting the primary connector and the secondary connector, and a device interface circuit 51 connected to the primary connector and controlling the opening and closing of the interrupter S1 depending on the commands arriving at the primary connector.

MEMORY CARD ADAPTER

The invention refers to an adapter for memory cards. More precisely, it refers to an adapter allowing to connect a second device to a memory card port in
5 addition to a memory card.

The market for memory cards has grown significantly based on the possibility of storing large amounts of data on Flash cards. There are different standards for such memory cards, for instance the SD standard (Secure Digital).
10 Like all standards for memory cards, the SD standard defines a card format, a connector type and a protocol of communication between the host device designed to take up the SD card, as well as the card itself. The SD standard as used in this document refers to publications of the SD Card Association, including specifications of "SD memory card, Physical layer specification", "SDHC memory
15 card", "miniSD card", "microSD card", "SDIO card" and "SD host controller specification". Furthermore, certain memory card standards use an extended communication protocol which allows to use additional instructions for using different devices than memory cards. In particular, the SD standard includes an extended command set called SDIO (Secure Digital Input Output device). With this
20 SDIO extension, one can connect different input/output devices to the host.

Within the realm of SD, it is common to use SDIO type cards to allow other services than just data storage. This is also known as SD-MCE (Secure Digital – Mobile Commerce Extension). With this card, one can implement other services than just memory storage, for instance pay TV, on mobile devices such as mobile
25 phones or portable computers.

However, mobile devices usually only have one such memory card port, and a second port for such devices is not expected in the future. This means that a user who wants to use pay TV on their phone cannot use a memory card at the same time. Changing the cards as needed is not a good solution because the
30 memory card is not available while the TV function is being used. Users might want to store data while using the TV function. The SD standard is not designed to allow the parallel connection of two devices at one single port at the same time.

The invention consists of an adapter allowing to connect two electronic devices at the same port, at the same time. Thus the host device communicates with one single device, allowing to implement an electronic function on one side while using the storage device on the other side. More generally, the invention constitutes a solution to actuate two devices at one single port which originally allows the connection of only one device.

This means that the invention is an adapter including a primary connector, a secondary connector, an interrupter and a device interface circuit. The primary connector is connected with the host taking up said adapter device. The second connector is taking up a memory card. The interrupter links the primary connector and the secondary connector. The device interface circuit is connected to the primary connector and controls the opening or closing of the interrupter depending on the commands received by the first connector.

The invention preferably includes an application circuit connected to the device interface circuit. The adapter includes a host interface circuit connected to the secondary connector for communication with the memory card. The device interface circuit and the host interface circuit are interconnected to exchange data and determine common features while taking the features of the card and the own features of the device into account.

In accordance with a special way of carrying out the invention, the device interface circuit closes the interrupter when a command is received by the primary connector and keeps it closed to let the response from the card pass if the command is directed toward the card. If no card is connected, the device interface circuit reacts to all commands. The device interface circuit closes the interrupter when a command is received by the first connector and opens the interrupter to block the response from the card if the command is directed toward the application circuit.

The invention is described in more detail by the following description, which describes further advantages and special features and makes reference to the annexed figures, including:

Figure 1, presenting a preferred way of carrying out the invention of an adapter in SD card format,

Figure 2 representing a functional diagram of the invention,

5 Figure 1 displays a preferred way of carrying out an adapter according to the invention. Adapter 1 is realised here in SD memory card format. Adapter 1 includes a primary connector 2 in the format of an SD card connector. Adapter one includes a device to take up a memory card 3 in microSD format, connected via a secondary connector 4. Adapter 1 includes a chip 5 connected to the primary and
10 the secondary connector 2 and 4. The chip 5 includes the electronic circuitry of adapter 1. In the preferred way of Figure 1, the electronic circuitry of adapter 1 is all placed on one single chip, yet it is also possible to distribute the circuitry over several chips.

Figure 2 provides a functional description of the electronic circuits on the
15 chip 5. The primary connector 2 and the secondary connector 4 are connected on the chip, respectively, by a primary bus 20 and a secondary bus 40 containing as many connectors as there are contacts for an SD card. For reasons of clarity, one single wire is depicted.

The primary bus and the secondary bus are interconnected by a primary
20 interrupter S1 consisting of interrupters allowing to connect or disconnect each conductor of the buses 20 and 40. The interrupter S1 thus allows to establish a direct connection between the two connectors 2 and 4, or to disconnect them from each other.

A device interface circuit 51 is connected to the primary bus 20. The device
25 interface circuit 51 is a circuit designed to communicate with a host connected to the primary connector 2. The user interface circuit provides protocol and communication control for an application circuit 52. The application circuit 52 can be of any type, for instance a pay TV control circuit of the SD-MCE type. Other application circuits are possible; circuit 52 can be a modem with another
30 connection, a card reader according to the ISO7816 standard, or any other application that can be connected via an SD bus.

The device interface circuit 51 analyses all commands exchanged at the level of the primary connector 2. However, this interface circuit does not react to all commands. In fact, certain commands are processed directly by the card 3. The interface circuit is furthermore connected to the interrupter S1 to control the same and determine if the card responds directly or not, depending on the commands observed.

A host interface circuit 53 is connected on one side to the secondary bus 40 and on the other side to the device interface circuit 51. The host interface circuit 53 allows to control the card 3 if the interrupter S1 is open. The connection between the host interface circuit 53 is linked to the device interface circuit 51 to be able to integrate data coming from the card 3 into data returned by the device interface circuit 51.

At the start, the adapter 1 contacts the host, which accepts it as a single device integrating the features of the application circuit 53 and the features of the card 3 at the same time. During this start-up phase, the same principle as detailed above is realised, but certain commands sent by the host device to the card should also be anticipated.

The following table lists the exchange of commands and responses during the start and initialisation phase in chronological order. The commands and responses listed in the table, as well as the device flags, are defined in the SD standard, which can be consulted for more detailed information.

Command sent by SD Host	Response from interface 51	Processing by interfaces 51 and 53	State of S1	Command sent by interface 53	Response by card 3
Cmd0			Closed		
Cmd8			Closed		
			Closed		R7
Acmd41			Closed		
	Busy	Storage of response data of card 3 in the OCR flag of interface 51	Open		OCR
Acmd41			Open	Cmd2	

	Busy	Storage of response data of card 3 in the CID flag of interface 51	Open		CID
Acmd41			Open		
	OCR		Open		
Cmd2			Open		
	CID		Open		
Cmd3			Closed		
			Closed		RCA
Cmd9			Closed		
		Storage of the data of the CSD response for communication with card 3 and the functions of the application circuit 52	Closed		CSD
Cmd4		Update DSR flag in interface 51	Closed		Update DSR

It should be noted that the information in the CDS and OCR flags of the device interface circuit 51 is linked intelligently: The Vdd is selected to correspond at the same time to the card 3 and the adapter 1, the maximum currents are added up and the transmission rates are determined depending on the slowest one between the device interface circuit 51 etc. The CSD and OCR flags are rebuilt to show features that are compatible with the card 3 and the adapter 1 at the same time.

For the CID flag, adding up the data is also recommended. The manufacturer of the adapter and of the card are not the same. Furthermore, the card 3 can be removed from the adapter and the ID data is not the same depending on the card being present or not.

As far as the content of the RCA flag is concerned, the adapter has to store this information since the address provided by the card can also be used to actuate the application circuit.

At the end of the start phase, the commutation circuit 54 detects the command Cmd4 on the bus 20 and keeps the interrupter S1 closed. Thus, if the SD host sends read or write commands or other commands solely to the destination of the card 3, these commands are received respectively by the card 3 and the interface circuit 51 does not respond and keeps the interrupter S1 closed so that the card responds directly. Finally, if the response refers to a state of the card that can be used in a device flag of the device interface 51, the host interface circuit 53 intercepts the response and transmits the data to the device interface circuit 51 for the storage of the respective data.

If the command received is not a command that is exclusively related to the card 3, the interrupter S1 is opened as soon as the command has passed completely. The host interface circuit receives the possible response from the card 3. The device interface circuit 51 responds, possibly while taking the response from the card 3 into account. When the device interface circuit 51 has finished its response, it closes the interrupter S1 again so that the card can receive the next command.

If the SD host wants to send one or several commands to the application circuit 52, it must use an extended command set. For this purpose, it sends a command, for instance Cmd6, which is not supported by the card 3. When the commutation circuit 54 detects this command Cmd6, it opens the interrupter S1. The card 3, having received the command Cmd6, may respond that it does not support that command but its response is not transmitted to the SD host. The response is given by the interface circuit 51, then the interrupter S1 is closed again so that the card can receive the next command intended for it.

If the power supply is cut off, the adapter will re-initiate the start phase.

It is also possible to use the adapter without the card 3. In such a case, the card never responds to the command sent to it during the start phase. In relation to the table above, this corresponds to the response to the commands Cmd8 and Acmd41 missing. The response to the command Cmd8 is optional and therefore does not mean that there is no card. In contrast, the failure to provide OCR response within a predetermined time means that there is no card 3 and that

therefore the device interface 51 can respond with its own OCR flag immediately after the previous command Acmd41.

In one variant option, it is possible that the application circuit 52 needs to store information on the card 3. In such a case, the proper reaction would be to
5 open the interrupter S1 and address the card 3 with the help of the host interface 53. If the SD host sends a command to the card while the card 3 is being used, the device interface circuit will respond that the card is not available with the response "busy".

The example described above refers to an adapter in SD card format taking
10 up a micro SD card. It is possible to modify these format aspects. A card in the SDIO format can have a part that is larger than an SD card and it is possible to replace the microSD card 3 with any other SD or miniSD card format.

As mentioned earlier, the application circuit 52 can also be of any type supported by the SDIO standard, and the application circuit 52 may have a
15 connection to another external circuit.

In this preferred way to carry out the invention, the chip 5 corresponds to several electronic circuits implemented on one single chip. Naturally, the electronic circuits can also be realised on separate chips, depending on the type of application realised with the adapter.

20 The problem outlined here, as well as the preferred way to carry out the invention, refer to the SD standard. Nevertheless, there are other, similar, memory card standards with similar features to the SD standard, for which the same problem arises and for which a corresponding solution can be applied. If the card standard allows communication with something other than a memory card and the
25 communication protocol allows communication only with one single device, an adapter of the same type can be used. Other commands suitable for the standard used would have to be applied in such a case.

CLAIMS

1. Adapter device (1) consisting of:
 - one primary connector (2) to be connected with a host taking up said adapter device,
 - one secondary connector (4) to take up the memory card (3),
 - one interrupter (S1) connecting the primary connector to the secondary connector,
 - one device interface circuit (51) connected to the primary connector and controlling the opening or closing of the interrupter (S1) depending on the commands received by the first connector.
2. Device according to claim 1, including furthermore an application circuit (52) connected to the device interface circuit (51).
3. Device according to claim 1 or 2, including furthermore a host interface circuit (53) connected to the secondary connector (4) to communicate with the memory card (3).
4. Device according to claim 3, whereby the device interface circuit (51) and the host interface circuit (53) are interconnected to exchange data and determine common features while taking the features of the card (3) and the own features of the device into account.
5. Device according to claim 1, whereby the primary connector and the secondary connector comply with the SD card standard.
6. Device according to claim 5, whereby the adapter device has the SD standard format and the memory card has the microSD format.
7. Device according to claim 1, whereby the device interface circuit (51) closes the interrupter (3) when a command is received by the primary connector (2) and

keeps it closed to let the response from the card (3) pass if the command is directed toward the card.

8. Device according to claim 7, whereby, if no card (3) is connected, the device interface circuit reacts to all commands.
- 5 9. Device according to claim 2, whereby the device interface circuit (51) closes the interrupter (3) when a command is received by the primary connector (2) and opens the interrupter to block the response from the card (3) if the command is directed toward the application circuit (52).

1/1

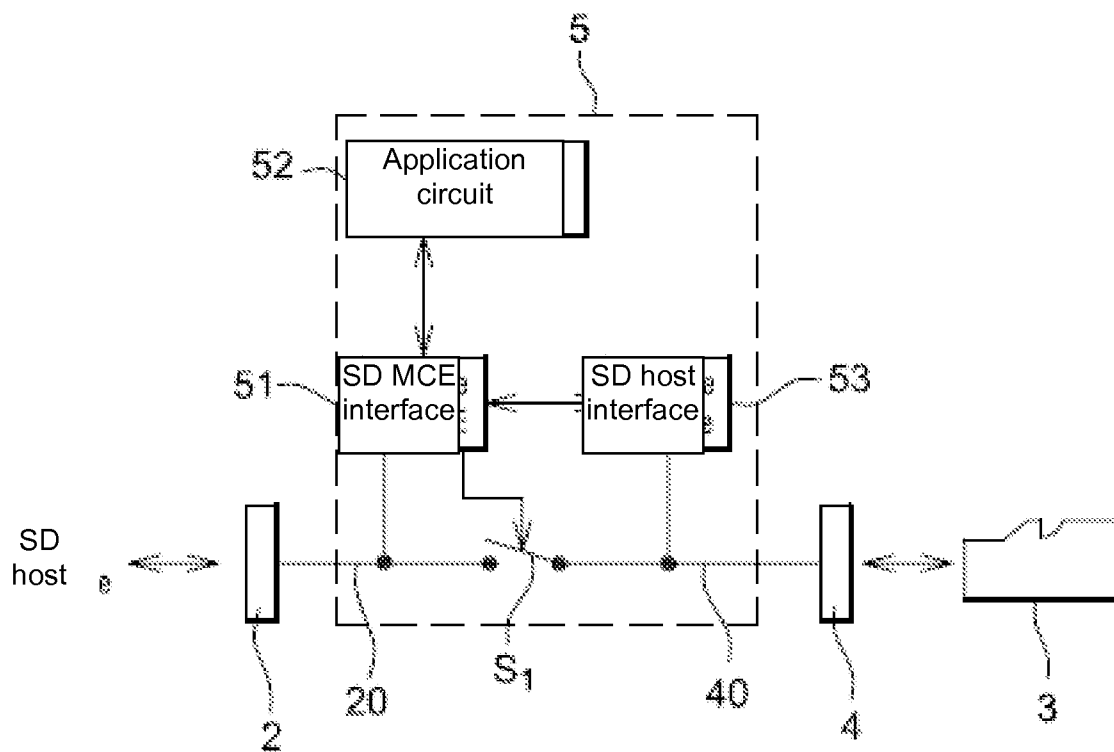
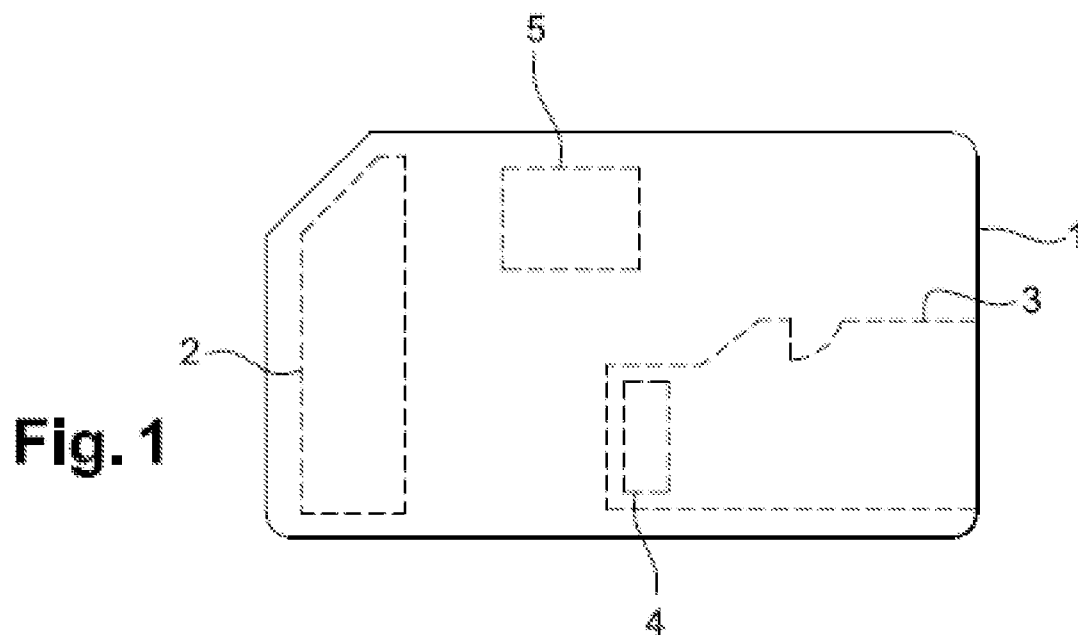


Fig. 2

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2009/058286

A. CLASSIFICATION OF SUBJECT MATTER
 INV. G11C5/00 G06F13/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G11C

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EP0-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2006/164891 A1 (MILLS KEVIN J [US] ET AL) 27 July 2006 (2006-07-27) abstract figure 26 paragraphs [0087] - [0094] -----	1-9
A	WO 02/05102 A (ONSPEC ELECTRONIC INC [US]) 17 January 2002 (2002-01-17) abstract figure 6 the whole document -----	1-9
A	US 5 936 226 A (AUCSMITH DAVID [US]) 10 August 1999 (1999-08-10) abstract figure 5 the whole document -----	1-9

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

- *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- *Z* document member of the same patent family

Date of the actual completion of the international search

19 October 2009

Date of mailing of the international search report

26/10/2009

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
 NL - 2280 HV Rijswijk
 Tel. (+31-70) 340-2040.
 Fax: (+31-70) 340-3016

Authorized officer

Operti, Antonio

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2009/058286

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 2006164891	A1	27-07-2006	NONE	
WO 0205102	A	17-01-2002	AU 7367701 A	21-01-2002
			CA 2408442 A1	17-01-2002
			CN 1452743 A	29-10-2003
			EP 1305716 A1	02-05-2003
			EP 2085887 A1	05-08-2009
			JP 2004503015 T	29-01-2004
			JP 2006202315 A	03-08-2006
			TW 222564 B	21-10-2004
			US 2003084221 A1	01-05-2003
			US 2008250174 A1	09-10-2008
			US 2003041203 A1	27-02-2003
			US 6438638 B1	20-08-2002
US 5936226	A	10-08-1999	US 5663553 A	02-09-1997