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(54) Title: TOOL-LEVEL AND HARDWARE-LEVEL CODE OPTIMIZATION AND RESPECTIVE HARDWARE MODIFICATION

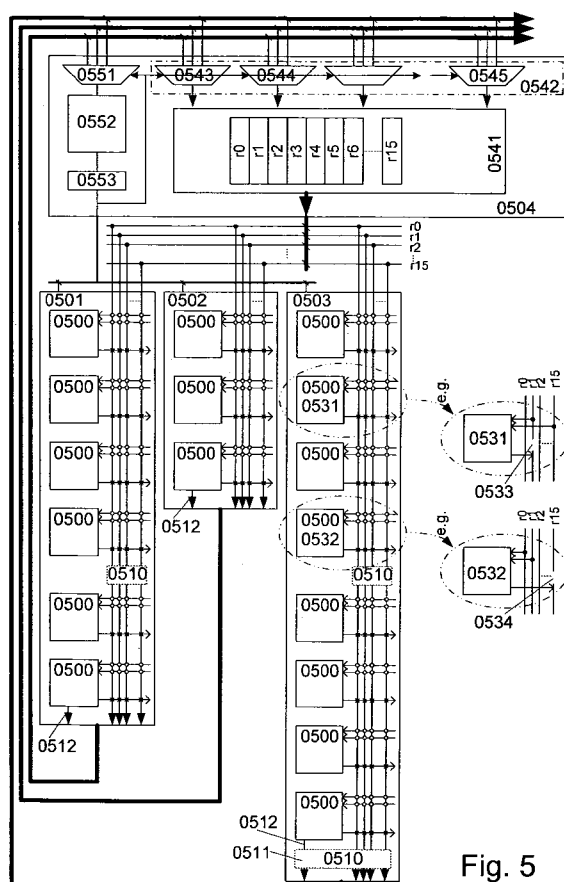


Fig. 5

(57) Abstract: The present invention relates to a method, for compiling high-level software code into hardware, transforming each instruction into a respective hardware block and using an execution control signal representing the program pointer for triggering the execution within each respective hardware block.



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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2010/131934 A1 (KIM SEON WOOK [KR] ET AL) 27 May 2010 (2010-05-27) abstract figures 6-7,13, 17a,17b paragraph [0021] paragraphs [0099] - [0107] paragraphs [0144] - [0145] -----	1-3
X	US 7 823 117 B1 (BENNETT DAVID W [US]) 26 October 2010 (2010-10-26) column 10, line 4 - line 9 column 10, line 44 - line 56 column 4, line 30 - line 47 -----	1-3
A	US 7 565 631 B1 (BANERJEE PRITH [US] ET AL) 21 July 2009 (2009-07-21) the whole document -----	1-3



Further documents are listed in the continuation of Box C.



See patent family annex.

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2010131934	A1	27-05-2010	NONE
US 7823117	B1	26-10-2010	NONE
US 7565631	B1	21-07-2009	NONE