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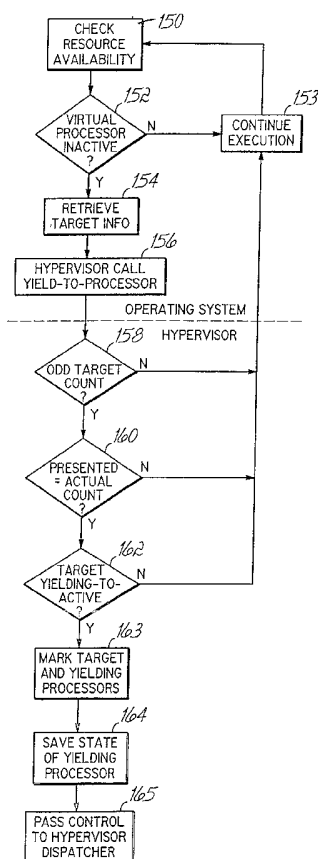
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(54) Title: SYSTEM FOR YIELDING TO A PROCESSOR



(57) Abstract: An apparatus, program product and method for coordinating the distribution of CPUs as among logically-partitioned virtual processors. A virtual processor may yield a CPU to precipitate an occurrence upon which its own execution may be predicated. As such, program code may dispatch the surrendered CPU to a designated virtual processor.



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F9/50 G06F9/48

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, IBM-TDB, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|------------|---|---------------------------------|
| Y | <p>US 5 692 193 A (JAGANNATHAN S ET AL) 25 November 1997 (1997-11-25)</p> <p>column 3, line 59 -column 4, line 6 column 4, line 59 -column 5, line 4 column 11, line 23 - line 46 column 16, line 55 -column 17, line 30 column 22, line 59 -column 24, line 7 --- -/--</p> | <p>1-4, 8-13, 17-20</p> |

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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- *Z* document member of the same patent family

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INTERNATIONAL SEARCH REPORT

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| C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT | | |
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| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
| Y | <p>BLACK D L: "SCHEDULING SUPPORT FOR CONCURRENCY AND PARALLELISM IN THE MACH OPERATING SYSTEM"</p> <p>COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH., CA, US, US,</p> <p>vol. 23, no. 5, 1 May 1990 (1990-05-01), pages 35-43, XP000128603</p> <p>ISSN: 0018-9162</p> <p>page 38, left-hand column, line 50</p> <p>-right-hand column, line 3</p> <p>page 38, right-hand column, line 54 -page 39, middle column, line 32</p> <p>page 41, middle column, line 16</p> <p>-right-hand column, line 22</p> <p style="text-align: center;">---</p> | <p>1-4, 8-13, 17-20</p> |
| A | <p>BORDEN T L ET AL: "MULTIPLE OPERATING SYSTEMS ON ONE PROCESSOR COMPLEX"</p> <p>IBM SYSTEMS JOURNAL, IBM CORP. ARMONK, NEW YORK, US,</p> <p>vol. 28, no. 1, 1989, pages 104-122, XP000046164</p> <p>ISSN: 0018-8670</p> <p>abstract</p> <p>page 109, right-hand column, line 23 - line 45</p> <p>page 112, left-hand column, line 21 -page 115, right-hand column, line 12</p> <p style="text-align: center;">---</p> | <p>1,11,19, 20</p> |
| A | <p>INOHARA S ET AL: "A thread facility based on user/kernel cooperation in the XERO operating system"</p> <p>COMPUTER SOFTWARE AND APPLICATIONS CONFERENCE, 1991. COMPSAC '91., PROCEEDINGS OF THE FIFTEENTH ANNUAL INTERNATIONAL TOKYO, JAPAN 11-13 SEPT. 1991, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US,</p> <p>11 September 1991 (1991-09-11), pages 398-405, XP010023397</p> <p>ISBN: 0-8186-2152-4</p> <p>page 401, left-hand column, line 44</p> <p>-right-hand column, line 19</p> <p style="text-align: center;">-----</p> | <p>5-7, 14-16</p> |

INTERNATIONAL SEARCH REPORT

Information on patent family members

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| Patent document cited in search report | | Publication date | | Patent family member(s) | Publication date |
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