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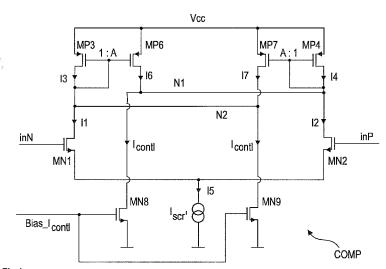


Fig.1

(57) Abstract: A comparator comprises a differential input stage, a current source coupled to the differential input stage for providing a tail current to one side of the differential input stage, and a differential load coupled to the differential pair and comprising at least one diode coupled load transistor per differential side. A load current through either one of the at least one diodecoupled load transistor on either differential side is mirrored out with a current mirror configuration to provide a current be fed to a respective node, each node being coupled to a respective variable biasing current source and respective other side of the differential input stage, so as to provide a variable positive feedback to the differential input stage.



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B. FIELDS	SEARCHED								
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C. DOCUMENTS CONSIDERED TO BE RELEVANT									
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