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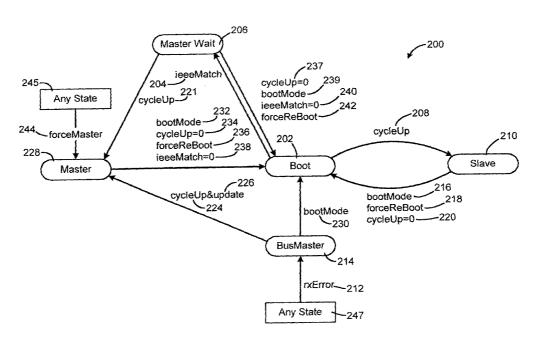
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(54) Title: HARDWARE BOOT PROTOCOL



(57) Abstract: The method is a herdware boot protocol for a ring topology (200, 240, 280, 300) that has a first node (204, 244, 284, 304) connected to a second node (206, 246, 286, 306) connected to a third node (202, 242, 282, 302). The boot protocol provides not only a seamless bus-to-ring configuration and ring-to-bus configuration but also a very fast and reliable master node and slave node selection routine. The switch may between bus and ring configuration may be performed without the need for rebooting the system.



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER IPC(7) :G06F 9/44 US CL :717100				
According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED				
Minimum documentation searched (classification system followed by classification symbols) U.S.: 717100-103; 713/1-2				
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched				
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) ACM online IEEE online				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where ap	ppropriate, of the relevant passages	Relevant to claim No	
A,P	US 6,282,642 B1 (CROMER et al) 28	August 2001.	1-10	
A,P	US 6,317,826 B1 (MCCALL et al) 13 November 2001.		1-10	
A	US 6,052,779 A (JACKSON et al) 18 April 2000.		1-10	
A	US 5,838,907 A (HANSEN) 17 November 1998.		1-10	
A	US 6,163,876 A (ASHAR et al) 19 December 2000.		1-10	
A	IEEE Standard for Boot (Initialization Configuration) Firmware: Bus Supplement for IEEE 896 (Futurebus). IEEE. 1996.		1-10	
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Further documents are listed in the continuation of Box C. See patent family annex.				
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