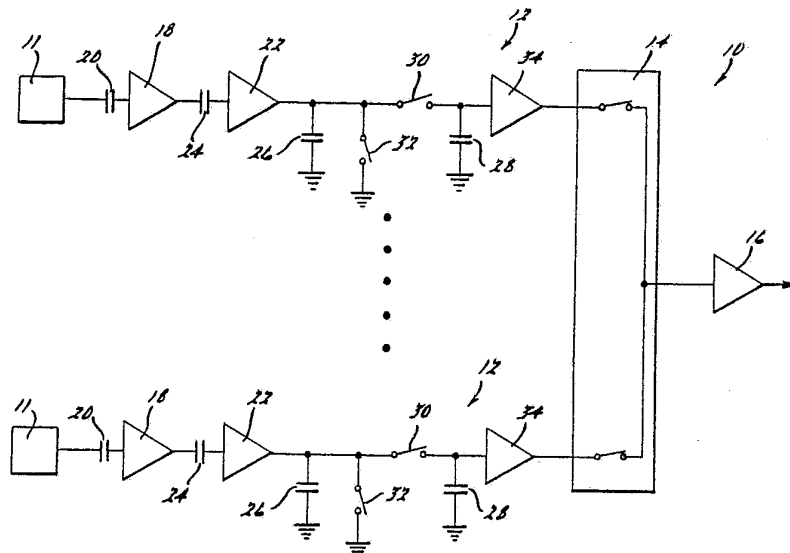


## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

<p>(51) International Patent Classification<sup>4</sup> : <b>H01N 5/33, 5/335</b></p>	<p><b>A3</b></p>	<p>(11) International Publication Number: <b>WO 87/ 07769</b></p> <p>(43) International Publication Date: 17 December 1987 (17.12.87)</p>
<p>(21) International Application Number: PCT/US87/01004</p> <p>(22) International Filing Date: 4 May 1987 (04.05.87)</p> <p>(31) Priority Application Number: 874,253</p> <p>(32) Priority Date: 13 June 1986 (13.06.86)</p> <p>(33) Priority Country: US</p> <p>(71) Applicant: HUGHES AIRCRAFT COMPANY [US/US]; 7200 Hughes Terrace, Los Angeles, CA 90045-0066 (US).</p> <p>(72) Inventor: KLATT, Robert, W. ; 32350 Searaven, Rancho Palos Verdes, CA 90274 (US).</p> <p>(74) Agents: TAYLOR, Ronald, L. et al.; Hughes Aircraft Company, Post Office Box 45066, Bldg. C1, M.S. A126, Los Angeles, CA 90045-0066 (US).</p>	<p>(81) Designated States: BE (European patent), CH (European patent), DE (European patent), FR (European patent), GB (European patent), IT (European patent), JP, NL (European patent), SE (European patent).</p> <p><b>Published</b> <i>With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i></p> <p>(88) Date of publication of the international search report: 30 December 1987 (30.12.87)</p>	

(54) Title: PARALLEL PROCESSOR MODULE FOR THERMAL IMAGING SENSORS



(57) Abstract

An apparatus (10) for converting a plurality of parallel input signals from a thermal detector array into a serial output signal. The apparatus (10) comprises a converter circuit (22) for converting the parallel input signals into a plurality of current signals. An integration circuit (26) is also provided for integrating the current signals for a predetermined length of time. The integration circuit (26) is operable to generate a plurality of voltages in response to the current signals. A storage circuit (28) is also provided to generate and store a plurality of storage signals which are proportional to the plurality of voltages generated by the integration circuit (26). A multiplexing circuit (14) is also provided which is operable to generate the serial output signal from the storage signals generated by the storage circuit (28).

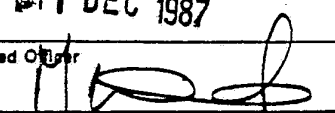
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# INTERNATIONAL SEARCH REPORT

International Application No PCT/US 87/01004

<b>I. CLASSIFICATION OF SUBJECT MATTER</b> (if several classification symbols apply, indicate all) *		
According to International Patent Classification (IPC) or to both National Classification and IPC		
IPC <sup>4</sup> : H 04 N 5/33; 5/335		
<b>II. FIELDS SEARCHED</b>		
Minimum Documentation Searched <sup>7</sup>		
Classification System	Classification Symbols	
IPC <sup>4</sup>	H 04 N	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched <sup>8</sup>		
<b>III. DOCUMENTS CONSIDERED TO BE RELEVANT</b> <sup>9</sup>		
Category <sup>10</sup>	Citation of Document, <sup>11</sup> with indication, where appropriate, of the relevant passages <sup>12</sup>	Relevant to Claim No. <sup>13</sup>
Y	Proceedings of the 1980 IEEE International Symposium on Circuits and Systems, Pt III, Houston, 28-30 April 1980 IEEE, 1980 (New York, US) Kansy et al.: "Analog MOS applications to IR signal processing", pages 723-727 see page 725, left-hand column, lines 26-44; right-hand column, lines 1-27; figure 1	1-14, 17-19
A	--	15, 16, 20
Y	IEEE Transactions on Electron Devices, ED32, no. 8, August 1985 (New York, US) P.A. Levine: "Low-noise CCD signal recovery", pages 1534-1537, see page 1534, left-hand column, lines 17-31; figure 1	1-14, 17-19
A	--	15, 16, 20
A	Proceedings of MELECON '85, vol. II: Digital Signal Processing, Elsevier Science Publishers B.V. (North-Holland)	./.
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<b>IV. CERTIFICATION</b>		
Date of the Actual Completion of the International Search	Date of Mailing of this International Search Report	
8th October 1987	11 DEC 1987	
International Searching Authority	Signature of Authorized Officer	
EUROPEAN PATENT OFFICE	M. VAN MOL 	

## III. DOCUMENTS CONSIDERED TO BE RELEVANT (CONTINUED FROM THE SECOND SHEET)

Category *	Citation of Document, with indication, where appropriate, of the relevant passages	Relevant to Claim No.
	IEEE Catalog No. 85 CH 2185-7 (New York, US) H. Wey et al.: "Correlated triple sampling: a digital low-noise readout-method for CCD'S", pages 209-212, see figure 1  -----	1-4,6-18,20