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(71) Applicant: **ZHEJIANG DAHUA TECHNOLOGY CO., LTD.** [CN/CN]; No. 1187 Binan Road, Binjiang District, Hangzhou, Zhejiang 310051 (CN).

(72) Inventors: **ZENG, Feiyang**; No. 1187 Binan Road, Binjiang District, Hangzhou, Zhejiang 310051 (CN). **JIANG, Dong**; No. 1187 Binan Road, Binjiang District, Hangzhou, Zhejiang 310051 (CN). **LIN, Jucui**; No. 1187 Binan Road, Binjiang District, Hangzhou, Zhejiang 310051 (CN). **YIN, Jun**; No. 1187 Binan Road, Binjiang District, Hangzhou, Zhejiang 310051 (CN). **FANG, Cheng**; No. 1187 Binan Road, Binjiang District, Hangzhou, Zhejiang 310051 (CN).

(74) Agent: **CHINA WISPRO INTELLECTUAL PROPERTY LLP.**; Room A806, Zhongdi Building, China University of Geosciences Base, No. 8 Yuexing 3rd Road, High-Tech Industrial Estate, Nanshan District, Shenzhen, Guangdong 518057 (CN).

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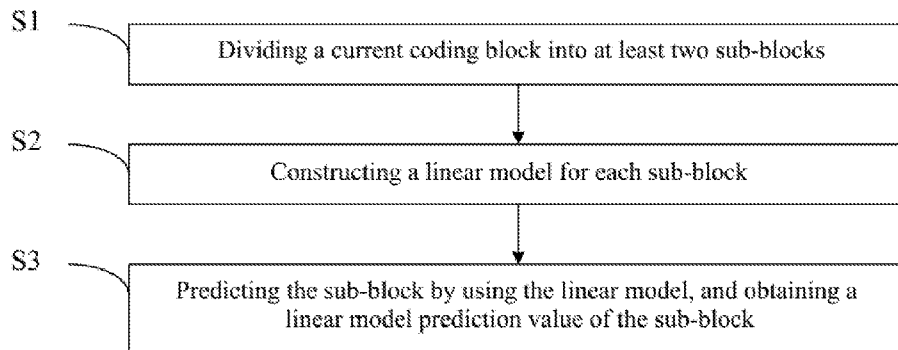


FIG. 1

(57) Abstract: A linear model (LM) prediction method and a coder are disclosed. The method includes dividing a current coding block into at least two sub-blocks; for each of the sub-blocks, constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.



LINEAR MODEL PREDICTION METHOD AND CODER

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] The present disclosure claims a priority to Chinese Patent Application No. 5 201910775848.4, filed on August 21, 2019, the content of which is herein incorporated by reference in its entirety.

TECHNICAL FIELD

[0002] The present disclosure generally relates to the technical field of video-coding, and in 10 particular to a linear model prediction method, a coder, and a non-transitory storage medium.

BACKGROUND

[0003] As a video image has a large amount of data, it is usually necessary to be encoded and compressed, and then be transmitted or stored. The encoded data is called a video stream. 15 Limited by hardware and other conditions, such as limited storage space and limited transmission bandwidth, the encoder always hopes to keep the video stream as small as possible.

[0004] Video coding mainly includes video-acquisition, prediction, transform quantization and entropy coding. The prediction can be divided into intra prediction, inter prediction and linear prediction.

[0005] Linear prediction refers that a linear model between a reference block and a current 20 coding block is constructed, and then pixel values of the current block are predicted from reconstructed pixels of the reference block through the linear model. Parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the current coding block and adjacent reconstructed pixels of the reference block of the current coding block.

[0006] Common linear prediction modes include the cross-component linear model (CCLM) 25 mode and the local illumination compensation (LIC) mode. In the CCLM mode, a linear model between the luma component and the chroma component of the same block is constructed to use the luma component to predict the chroma component of the same block, and this reduces redundancy between cross-color spaces. In the LIC mode, adjacent reconstructed pixels of the 30 current block and adjacent reconstructed pixels of the reference block are used to construct a

linear model to compensate difference in brightness between the reference frame and the current frame.

[0007] According to relative position relationships between the adjacent reconstructed pixels for calculating parameters of the linear model and the current coding block, the CCLM mode is further divided into three types: LM, LM_L, and LM_T. Adjacent reconstructed pixels for calculating the model in LM are located on the left and top side of the current coding block. Adjacent reconstructed pixels for calculating the model in LM_L are located on the left side of the current coding block. Adjacent reconstructed pixels for calculating the model in LM_T are located at the top side of the current coding block.

[0008] Currently, the linear prediction has certain limitations. The farther a pixel in the current coding block is from the adjacent reconstructed pixels, the lower prediction accuracy this pixel has. This affects overall linear prediction accuracy, and especially for a coding block with a large size.

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SUMMARY

[0009] According to one aspect of the present disclosure, a linear model (LM) prediction method is provided is disclosed. The method includes dividing a current coding block into at least two sub-blocks; for each of the sub-blocks, constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.

[0010] According to another aspect of the present disclosure, a coder is provided, which includes a processor. The processor is configured to execute instructions to perform dividing a current coding block into at least two sub-blocks; for each of the sub-blocks, constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.

[0011] According to yet another aspect of the present disclosure, a non-transitory storage

medium is provided, which stores instructions, when executed, causing a processor to perform dividing a current coding block into at least two sub-blocks; for each of the sub-blocks, constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.

[0012] According to yet another aspect of the present disclosure, a device for linear model prediction is provided, which includes a dividing module, configured for dividing a current coding block into at least two sub-blocks; for each of the sub-blocks, a constructing module, configured for constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and a predicting module, configured for predicting the sub-block using the linear model and obtaining a LM prediction value of the sub-block.

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BRIEF DESCRIPTION OF THE DRAWINGS

[0013] FIG. 1 is a schematic flowchart of a linear model prediction method according to some embodiments of the present disclosure.

[0014] FIG. 2 is a schematic diagram illustrating the current block is divided into sub-blocks using the LM_T mode.

[0015] FIG. 3 is a schematic diagram illustrating the current block is divided into sub-blocks using the LIC mode.

[0016] FIG. 4 is a schematic diagram illustrating the current block with a size of 16x8 is divided into sub-blocks.

[0017] FIG. 5 is a schematic diagram illustrating an L-shaped division of the current block with a size of 16x16.

[0018] FIG. 6 is a schematic diagram illustrating a stepped division of the current block with a size of 16x16.

[0019] FIG. 7 is a schematic flowchart of a linear model prediction method according to some embodiments of the present disclosure.

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[0001] FIG. 8 is a schematic diagram of a device for linear model prediction according to some embodiments of the present disclosure.

[0002] FIG. 9 is a schematic diagram a coder according to some embodiments of the present disclosure.

5 [0003] FIG. 10 is a schematic diagram a storage device according to some embodiments of the present disclosure.

DETAILED DESCRIPTION

[0004] The technical solutions in embodiments of the present disclosure will be clearly and
10 completely described below in conjunction with the drawings in embodiments of the present disclosure. Obviously, the described embodiments are only a part of the embodiments of the present disclosure, rather than all the embodiments. Based on the embodiments in the present disclosure, all other embodiments obtained by those of ordinary skill in the art without creative work shall fall within the protection scope of the present disclosure.

15 [0005] The terms “first”, “second” and “third” in the present disclosure are only used for descriptive purposes, and cannot be understood as indicating or implying relative importance or implicitly indicating the number of indicated technical features. Therefore, the features defined with “first”, “second”, and “third” may explicitly or implicitly include at least one of the features. In the description of the present disclosure, “a plurality of” means at least two, such as two, three,
20 etc., unless otherwise specifically defined. All directional indications (such as up, down, left, right, front, back) in embodiments of the present disclosure are only used to explain the relative positional relationships and movements between components in a specific posture as shown in the drawing, and the directional indication will also change accordingly as the specific posture changes. In addition, the terms “including”, “having”, and any variations thereof are intended
25 to cover non-exclusive inclusion. For example, a process, method, system, product, or device that includes a series of actions or units is not limited to the listed actions or units, but optionally includes unlisted actions or units, or optionally also includes other actions or units inherent to the process, method, product, or device.

[0006] Reference to “embodiments” herein means that a specific feature, structure, or
30 characteristic described in conjunction with the embodiments may be included in at least one

embodiment of the present disclosure. The phrase in various places in the specification neither necessarily refer to the same embodiment, nor an independent or alternative embodiment mutually exclusive with other embodiments. Those skilled in the art clearly and implicitly understand that the embodiments described herein can be combined with other embodiments if there is no conflict.

[0007] FIG. 1 is a schematic flowchart illustrating a linear model prediction method according to some embodiments of the present disclosure. It should be noted that, if there are substantially same results, this embodiment is not limited to an order of a process shown in FIG. 1. As shown in FIG. 1, the method includes actions/operations in the following.

[0008] At S1, the method divides a current coding block into at least two sub-blocks.

[0009] The current coding block refers to a block currently to be coded, which can be referred to as the current block for short. In some cases, the current coding block may be called a coding unit (CU).

[0010] Different sub-blocks do not overlap with each other, and different sub-blocks are not necessarily the same in size. All the sub-blocks may be rectangular, or at least part of the sub-blocks is irregular in shape.

[0011] A division-manner of the current coding block may be selected according to parameters of the current coding block. For example, the current coding block may be divided according to at least one of a linear model type and size information of the current coding block. Of course, other division-manner can also be used, which are not limited here. The current coding block and its reference block are divided in the same manner. With the current coding block being divided according to parameters of the current coding block, the current coding block can be divided into at least two one sub-blocks effectively, then it is facilitated to construct a linear model and perform linear prediction for each sub-block, and thus, compared with linear prediction being performed for the current coding block directly, this improves prediction accuracy using linear model for the current coding block.

[0012] The division according to the linear model type may specifically be that the current coding block is divided horizontally and/or vertically if the linear model type of the current coding block is LM or LIC. If the linear model type of the current coding block is LM_L, the current coding block is divided vertically. If the linear model type of the current coding block is

LM_T, the current coding block is divided horizontally.

[0013] Vertical division means that the dividing-line is in the vertical direction, and horizontal division means that the dividing-line is in the horizontal direction. In the LM/LIC mode, the adjacent reconstructed pixels for calculating parameters of the linear model are located on the top side and the left side of a sub-block and its reference block. In the LM-L mode, the adjacent reconstructed pixels for calculating parameters of the linear model are located on the left side of the sub-block and its reference block. In the LM-T mode, the adjacent reconstructed pixels for calculating parameters of the linear model are located on the top side of the sub-block and its reference block. Due to the above-mentioned division according to the linear model type, it can ensure that at least part of adjacent reconstructed pixels of at least one sub-block belong to another sub-block of the same current coding block.

[0014] A specific example of division according to the linear model type will be explained in conjunction with the drawings.

[0015] As shown in FIG. 2, the current block with a size of 16x16 adopts the LM_T mode, and the current block can be horizontally divided into two sub-blocks sub_block_0 and sub_block_1, sizes of which are 16x4 and 16x12, respectively.

[0016] As shown in FIG. 3, the current block with a size of 16x16 adopts the LIC mode, and the current block can be horizontally and vertically divided into three sub-blocks: sub_block_0, sub_block_1, and sub_block_2, sizes of which are 16x4, 4x12 and 12x12, respectively.

[0017] Division according to size information may specifically be that, the current coding block is divided vertically if a width of the current coding block is greater than a height of the current coding block, the current coding block is divided horizontally if the width of the current coding block is less than the height of the current coding block, and the current coding block is divided horizontally and/or vertically if the width of the current coding block is equal to the height of the current coding block. There is no limit to the number of sub-blocks and sizes of sub-blocks obtained through the dividing. At the same time, the linear model type of the current coding block is not limited.

[0018] What is determined based on the size information may be the first division-manner, and then one or some sub-blocks may be further divided vertically and/or horizontally.

[0019] A specific example of sub-block division according to the size information will be

explained in conjunction with the drawings.

[0020] As shown in FIG. 4, the size of the current block is 16x8, and the current block can be vertically divided into two sub-blocks sub_block_0 and sub_block_1, sizes of which are 8x8 and 8x8, respectively.

5 [0021] Of course, the current coding block can also be irregularly divided, and at least part of the obtained sub-blocks has an irregular shape, for example, L-shape, or stepped shape.

[0022] Specific examples of irregular-division are described in conjunction with the drawings.

[0023] As shown in FIG. 5, the size of the current block is 16x16, and the current block can be divided into two sub-blocks sub_block_0 and sub_block_1. The division-manner is L-shaped
10 division, a size of the sub-block sub_block_1 is 12x12, and the L-shaped sub-block sub_block_0 can be regarded as a combination of a block with a size of 16x4 and a block with a size of 4x16.

[0024] As shown in FIG. 6, the size of the current block is 16x16, and the current block can be divided into two sub-blocks sub_block_0 and sub_block_1. The division-manner is diagonal-stepped division, the sub-block sub_block_0 can be regarded as a combination of 10
15 blocks with a size of 4x4, and the sub-block sub_block_1 can be regarded as a combination of 6 blocks with a size of 4x4.

[0025] At S2, the method constructs a linear model for each sub-block.

[0026] Parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and its reference sub-block. A reference sub-block of a
20 sub-block refers to a part corresponding to the sub-block after the reference block of the current coding block is divided in the same manner as the current coding block is divided. Of course, it can also be understood that, a reference sub-block of a sub-block is obtained as the way of obtaining the reference block from the current coding block is applied to the sub-block. Adjacent reconstructed pixels are located on the coded side of the sub-block or reference
25 sub-block, which is specifically determined by an order of the encoding/decoding. For example, in a case where the order of encoding/decoding is from left to right and from top to bottom, the coded side is the left side and/or the top side.

[0027] The linear model type of each sub-block may be the same as the linear model type of the current coding block. In this case, since the adjacent reconstructed pixels used for calculating
30 linear model parameters may be different, different sub-blocks may have different linear model

parameters, such that different sub-blocks have different linear models. Of course, at least one sub-block can select a linear model different from the linear model type of the current coding block. For example, the current coding block adopts the LM mode, and a sub-block may adopt LM, LM_L, or LM_T.

5 [0028] For each sub-block, adjacent reconstructed pixels are obtained and then parameters of the linear model are calculated in a way consistent with a way in which parameters of the linear model of the current coding block. Examples are illustrated in the following to describe specific calculation-manners of linear model parameters of two current coding blocks.

[0029] Specifically, in the CCLM mode, the luma component of the same block is used to predict the chroma component, which means that the Y component is used to predict the Cb or Cr component, so as to reduce the redundancy between cross-color spaces. The current coding block is a chroma block. Since the size of a luma block corresponding to a same image-area is generally twice that of the chroma block, the luma block and its adjacent reconstructed pixels must be down-sampled first.

15 [0030] In CCLM mode, the constructed linear model between luminance and chroma components is in equation (1)

$$pred_c(i, j) = \alpha \cdot rec_L'(i, j) + \beta \quad (1)$$

[0031] rec_L' is a reference block obtained by down-sampling the luma block, and $pred_c(i, j)$ is the linear model prediction value (referred to as the prediction value) of the current coding block.

20 α and β are the parameters of the linear model, which can be calculated from the adjacent reconstructed pixels of the current coding block and a corresponding reference block.

[0032] If all adjacent reconstructed pixels cannot be obtained, then $\alpha = 0$, $\beta = (1 \lll BitDepth) \ggg 1$, where $BitDepth=10$, \lll represents a left shift operation, and \ggg represents a right shift operation.

25 [0033] If it is possible to obtain adjacent reconstructed pixels, 4 adjacent reconstructed pixels of the reference block are selected. x_A^0 and x_A^1 with minimum pixel values are selected from these 4 pixels, and correspondingly, reference pixels of the current encoding block are y_A^0 and y_A^1 (which have same locations with the current block/reference block). x_B^0 and x_B^1 with maximum pixel values are selected, and correspondingly, reference pixels of the current
30 encoding block are y_B^0 and y_B^1 . The equation (2) is as follows.

$$\begin{aligned}
x_A &= (x_A^0 + x_A^1 + 1) \gg 1; x_B = (x_B^0 + x_B^1 + 1) \gg 1; \\
y_A &= (y_A^0 + y_A^1 + 1) \gg 1; y_B = (y_B^0 + y_B^1 + 1) \gg 1
\end{aligned} \tag{2}$$

[0034] α and β are calculated in equations (3) as follows.

$$\begin{aligned}
\alpha &= \frac{y_B - y_A}{x_B - x_A} \\
\beta &= y_A - \alpha x_A
\end{aligned} \tag{3}$$

[0035] The 4 adjacent reconstructed pixels are selected in following rules.

[0036] [a]. In the LM mode, if there exist adjacent reconstructed pixels on both the left and top sides, four adjacent reconstructed pixels $[W/4, -1]$, $[3W/4, -1]$, $[-1, H/4]$, $[-1, 3H/4]$ are selected, W represents widths of the current coding block and reference block, H represents heights of the current coding block and reference block. If there only exist adjacent reconstructed pixels on the left or top side, the existed adjacent reconstructed pixels are used to fill sides at which there exist no adjacent reconstructed pixels to form four pixels.

[0037] [b]. In LM_L mode, four adjacent reconstructed pixels $[-1, (W+H)/8]$, $[-1, 3(W+H)/8]$, $[-1, 5(W+H)/8]$, and $[-1, 7(W+H)/8]$ are selected.

[0038] [c]. In LM_T mode, four adjacent reconstructed pixels $[(W+H)/8, -1]$, $[3(W+H)/8, -1]$, $[5(W+H)/8, -1]$, $[7(W+H)/8, -1]$ are selected.

[0039] The coordinates of these adjacent reconstructed pixels are relative coordinates with respect to the top-left pixel of the current block or reference block, i.e. coordinates in a case where the top-left pixel of the current block/reference block are set to $[0,0]$. After determining the four adjacent reconstructed pixels, α and β can be calculated according to the equation (2) and equation (3).

[0040] Specifically, in the LIC mode, a linear model is constructed by using adjacent reconstructed pixels of the current block and adjacent reconstructed pixels of the reference block to compensate for the difference in brightness between the reference frame and the current frame.

[0041] In a case of uni-prediction, the linear model is as follows.

$$P(x, y) = \alpha \cdot P_r(x + v_x, y + v_y) + \beta \tag{4}$$

[0042] $P(x, y)$ is the prediction value of the current coding block, and $P_r(x+v_x, y+v_y)$ is the

reference block in the reference frame to which is pointed by the motion vector $[v_x, v_y]$.

[0043] α and β are calculated in equation (5) as follows.

$$\alpha = \left(\frac{\sum \text{ref}(s) \times \text{cur}(r) - \frac{\sum \text{ref}(s) \times \sum \text{cur}(r)}{N}}{\sum \text{cur}(r)^2 - \frac{\sum \text{ref}(s) \times \sum \text{ref}(s)}{N}} \right)$$

$$\beta = \frac{\sum \text{cur}(r)}{N} - \alpha \times \frac{\sum \text{ref}(s)}{N} \quad (5)$$

5 [0044] $\text{ref}(s)$ is an adjacent reconstructed pixel of the reference block, $\text{cur}(r)$ is an adjacent reconstructed pixel of the current block, and N is the number of adjacent reconstructed pixels.

[0045] In a case of bi-prediction, α and β are calculated as the uni-prediction in each direction, and the bi-prediction linear model is constructed as follows:

$$P(x, y) = (1 - w) \times (\alpha_0 \cdot P_r^0(x + v_x^0, y + v_y^0) + \beta_0) + w \times (\alpha_1 \cdot P_r^1(x + v_x^1, y + v_y^1) + \beta_1) \quad (6)$$

10 [0046] $P_r^0(x+v_x^0, y+v_y^0)$ and $P_r^1(x+v_x^1, y+v_y^1)$ are a reference block in the L0 and L1 directions, respectively, α_0 , β_0 and α_1 , β_1 are linear model parameters calculated according to equation (5) with the adjacent reconstructed pixels of the current block and the adjacent reconstructed pixels of the reference block in the L0 and L1 directions, respectively. $(1-w)$ and w are weights for prediction values in the L0 and L1 directions.

15 [0047] In the process of calculating linear model parameters for a sub-block, pixel values of adjacent reconstructed pixels of the sub-block and its reference sub-block are used. If an edge of a sub-block/reference sub-block adjacent to the adjacent reconstructed pixels is a straight line, for example, when the sub-block is rectangular or L-shaped, the calculation manner of linear model parameters of the current coding block can be directly adopted. If the edge of the sub-block/reference sub-block adjacent to the adjacent reconstructed pixels is not a straight line, but a stepped line, and a linear model type of the sub-block is CCLM, the calculation manner of the linear model parameters of the current coding block is directly adopted, in which at least one of the selected 4 adjacent reconstructed pixels is not actually adjacent to the sub-block/reference sub-block. At this time, the selection manner of adjacent reconstructed pixels can be adjusted
25 to ensure that the selected adjacent reconstructed pixels are actually adjacent to the sub-block/reference sub-block.

[0048] For example, in FIG. 6, four adjacent reconstructed pixels A0, A1, A2, and A3 selected for sub_block_1 are located at corners of the stepped edge of sub_block_1.

[0049] At S3, the method predicts the sub-block by using the linear model, and obtains a linear model prediction value of the sub-block.

5 [0050] The reference sub-block of the sub-block can be substituted into the linear model to calculate the linear model prediction value of the sub-block.

[0051] In this embodiment, for each sub-block, a linear model is independently constructed and linear prediction is performed, so that a more suitable linear model can be used for prediction of each sub-block, which improves the accuracy of prediction using linear model.

10 [0052] FIG. 7 is a schematic flowchart of a linear model prediction method according to some embodiments of present disclosure. This embodiment is a further extension based on the linear model prediction method according to above embodiments of the present disclosure. It should be noted that, if there are substantially same results, this embodiment is not limited to the order of a process shown in FIG. 7. As shown in FIG. 7, the method includes actions/operations in
15 the following.

[0053] At S11, the method divides a current coding block into at least two sub-blocks.

[0054] At S12, the method constructs a linear model for each sub-block.

[0055] At S13, the method predicts the sub-block by using the linear model, and obtains a linear model prediction value of the sub-block.

20 [0056] At S14, the method calculates prediction-cost of sub-block prediction by using the linear model prediction values of all the sub-blocks.

[0057] The prediction cost of sub-block prediction can be calculated using sub-block linear model prediction values of the current block. The linear model prediction values of all sub-blocks are contained into the sub-block linear model prediction value of the current block.

25 [0058] At S15, the method compares the prediction cost of sub-block prediction with prediction cost of block-prediction, and selects a prediction mode with less prediction cost as a prediction mode of the current coding block.

[0059] The prediction cost of block-prediction is prediction cost for which linear model prediction is performed on the current coding block directly.

30 [0060] At S16, the method codes the current coding block by using the selected prediction

mode, and obtains a code-stream of the current coding block.

[0061] In this embodiment, as the prediction-cost of sub-block prediction is calculated compared with the prediction cost of block-prediction, a prediction mode with less prediction cost is selected as a prediction mode of the current coding block, and thus, the current coding block can be coded with less coding cost. That is, this can reduce coding cost when the current coding block is coded.

[0062] In some embodiments, the code-stream of the current coding block includes a sub-block prediction flag, and the sub-block prediction flag is used to indicate whether the current coding block adopts sub-block prediction. With the sub-block prediction flag, whether the current coding block adopts sub-block prediction is achieved simply and effectively, and thus, it is easy to distinguish what prediction is used as the current coding block is coded. The sub-block prediction flag can be implemented in many manners. Three examples are given below.

[0063] In an example, the sub-block prediction flag includes a sub-block prediction syntax element, and a value of the sub-block prediction syntax element are used to indicate whether the current coding block adopts sub-block prediction and a manner in which the current coding block is divided.

[0064] For example, a new syntax element `CCLM_SUB_PRED` can be added to indicate whether the current block needs to perform sub-block prediction and the manner in which the current coding block is divided when the current block adopts the CCLM mode.

[0065] When `CCLM_SUB_PRED` is 0, it means that the current block is predicted according to the existing technology when CCLM is used, and no division is required, and when `CCLM_SUB_PRED` is 1, it means that the current block needs to be horizontally divided when CCLM is used for prediction. When both the width and height of the current block ≥ 16 , the current block is divided into 4 sub-blocks with a same size, otherwise, the current block is divided into 2 sub-blocks with same size. When `CCLM_SUB_PRED` is 2, it indicates that the current block needs to be divided vertically when CCLM is used for prediction. When both the width and height of the current block ≥ 16 , the current block is divided into 4 sub-blocks with the same size, otherwise, the current block is divided into 2 sub-blocks with the same size. When `CCLM_SUB_PRED` is 3, it indicates that the current block needs to be divided in L-shaped division when CCLM is used for prediction. When both the width and height of the

current block ≥ 16 , the current block be divided in L-shaped division twice, otherwise, the current block is divided in L-shaped division once. The width and height of each divided rectangular block is $3/4$ of the width and height of the original block. Other values of CCLM_SUB_PRED can be added to correspond to other division-manners.

5 [0066] In another example, the sub-block prediction flag includes a sub-block prediction syntax element. The value of the sub-block prediction syntax element is used to indicate whether the current coding block adopts sub-block prediction, and when the current coding block adopts sub-block prediction, the sub-block prediction flag further includes a manner in which the current coding block is divided.

10 [0067] For example, a syntax element CCLM_SUB_PRED can be added to indicate whether the current block needs to perform sub-block prediction when the CCLM mode is adopted.

[0068] When CCLM_SUB_PRED is 0, it means that the current block is predicted according to the existing technology when CCLM is used, and no division is required, and when CCLM_SUB_PRED is 1, it means that the current block needs to be divided into sub-blocks
15 when CCLM is used for prediction, and another syntax element is added to indicate the manner in which the current coding block is divided.

[0069] In yet another example, the sub-block prediction flag includes a sequence number of intra-prediction mode of the current coding block, where when the current coding block adopts sub-block prediction, the sequence number of intra-frame prediction mode of the current coding
20 block is a newly added sequence number.

[0070] For example, K newly added intra-prediction modes are used to indicate CCLM which uses sub-block prediction, and $1 \leq K \leq 3$. This scheme can only be applied to CCLM. Assume that sequence numbers of the existing intra prediction modes (including DC, Planar, Angle, etc.) are 0~N, then the sequence number for indicating the CCLM which uses sub-block
25 prediction is greater than N.

[0071] FIG. 8 is a schematic diagram of a device for linear model prediction according to some embodiments of the present disclosure. As shown in FIG. 8, the device includes a dividing module 11, a constructing module 12, and a predicting module 13.

[0072] The dividing module 11 is configured for dividing a current coding block into at least
30 two sub-blocks.

[0073] For each of the sub-blocks, the constructing module 12 is configured for constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block.

5 [0074] the predicting module 13 is configured for predicting the sub-block using the linear model and obtaining a linear model prediction value of the sub-block.

[0075] FIG. 9 is a schematic diagram a coder according to some embodiments of the present disclosure. As shown in FIG. 9, the coder 9 includes a processor 31.

[0076] The processor 31 may also be referred to as a CPU (Central Processing Unit). The
10 processor 31 may be an integrated circuit chip with signal processing capabilities. The processor 31 may also be a general-purpose processor, a digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA) or other programmable logic device, a discrete gate or transistor logic component, or a discrete hardware component. The general-purpose processor may be a microprocessor, or the processor may also
15 be any conventional processor.

[0077] The coder may further include a memory (not shown in the drawings) for storing instructions and data required for operations of the processor 31.

[0078] The processor 31 is configured to execute instructions to implement the method according to any embodiment and any non-conflicting combination of the linear model
20 prediction method of the present disclosure.

[0079] FIG. 10 is a schematic diagram a storage device according to some embodiments of the present disclosure. The storage device 40 of the present disclosure stores instructions, which when executed, the method according to any embodiment or any non-conflicting combination of the linear model prediction method of the present disclosure is performed. The instruction may
25 form a program file and be stored in the above-mentioned storage device in the form of a software product, so that a computer device (e.g. a personal computer, a server, or a network device, etc.) or a processor executes all or a part of actions/operations in the method according to various implementations of the present disclosure. The aforementioned storage device includes
30 a U disk, a mobile hard disk, a read-only memory (ROM), a random access memory (RAM), magnetic disks or optical disks, and other media that can store program codes.

[0080] In several embodiments provided in the present disclosure, it should be understood that, the disclosed system, device, and method may be implemented in other ways. For example, the device described above are merely illustrative, for example, the division of units is only a logical function division, and there may be other divisions in actual implementation, for example, multiple units or components can be combined or integrated into another system, or some features can be ignored or not implemented. In addition, the displayed or discussed mutual coupling or direct coupling or communication connection may be indirect coupling or communication connection through some interfaces, devices, or units, and may be in electrical, mechanical, or other forms.

10 [0081] In addition, the functional units in the various embodiments of the present disclosure may be integrated into one processing unit, or each unit may exist alone physically, or two or more units may be integrated into one unit. The above-mentioned integrated unit can be implemented in the form of hardware or software functional unit. The above are only implementations of the present disclosure, and do not limit the scope of the present disclosure.

15 Any equivalent structure or equivalent process transformation made by using the description and drawings of the present disclosure, or directly or indirectly applied to other related technical fields, are included in the scope of the present disclosure similarly.

CLAIMS

What is claimed is:

1. A linear model (LM) prediction method, comprising:
dividing a current coding block into at least two sub-blocks;
for each of the sub-blocks,
 constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and
 predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.
2. The method of claim 1, wherein the dividing a current coding block comprises:
dividing the current coding block according to a linear model type of the current coding block.
3. The method of claim 2, wherein the dividing the current coding block according to a linear model type of the current coding block comprises:
 if the linear model type of the current coding block is LM or LIC, dividing the current coding block horizontally or vertically;
 if the linear model type of the current coding block is LM or LIC, dividing the current coding block horizontally and vertically;
 if the linear model type of the current coding block is LM_L, dividing the current coding block vertically; or
 if the linear model type of the current coding block is LM_T, dividing the current coding block horizontally.
4. The method of claim 1, wherein the dividing a current coding block comprises:
dividing the current coding block according to size information of the current coding block.
5. The method of claim 4, wherein the dividing the current coding block according to a size of the current coding block comprises:
 if a width of the current coding block is greater than a height of the current coding block, dividing the current coding block vertically;
 if the width is less than the height, dividing the current coding block horizontally;

if the width is equal to the height, dividing the current coding block horizontally and vertically; or

if the width is equal to the height, dividing the current coding block horizontally or vertically.

6. The method of claim 1, wherein at least one of the sub-blocks has a shape of non-rectangle.

7. The method of claim 1, wherein if a linear model type of the sub-block is CCLM and a coded side of the sub-block has a stepped edge, the adjacent reconstructed pixels of the sub-block used for calculating the parameters of the linear model of the sub-block belong to another sub-block adjacent to the sub-block.

8. The method of claim 1, wherein the LM prediction value of the sub-block is calculated by substituting the reference sub-block of the sub-block into the linear model.

9. The method of claim 1, further comprising:

calculating prediction cost of sub-block prediction by using LM prediction values of all the sub-blocks;

comparing the prediction cost of sub-block prediction with prediction cost of block-prediction and selecting a prediction mode with less prediction cost as a prediction mode of the current coding block, wherein the prediction cost of block-prediction indicates prediction cost for which linear model prediction is performed on the current coding block; and

coding the current coding block by using the selected prediction mode, and obtaining a code-stream of the current coding block, wherein the code stream of the current coding block comprises a sub-block prediction flag, and the sub-block prediction flag is configured for indicating whether the current coding block adopts sub-block prediction.

10. The method of claim 9, wherein the sub-block prediction flag comprises a sub-block prediction syntax element, and a value of the sub-block prediction syntax element is configured for indicating whether the current coding block adopts sub-block prediction and a manner in which the current coding block is divided; or

the sub-block prediction flag comprises a sub-block prediction syntax element, and a value of the sub-block prediction syntax element is configured for indicating whether the current coding block adopts sub-block prediction, and the sub-block prediction flag further comprises a

manner in which the current coding block is divided when the current coding block adopts sub-block prediction; or

the sub-block prediction flag comprises a sequence number of intra-prediction mode of the current coding block, wherein the sequence number is newly added when the current coding block adopts sub-block prediction.

11. A device for linear model prediction, comprising:

a dividing module, configured for dividing a current coding block into at least two sub-blocks;

for each of the sub-blocks,

a constructing module, configured for constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and

a predicting module, configured for predicting the sub-block using the linear model and obtaining a LM prediction value of the sub-block.

12. A coder, comprising a processor, wherein the processor is configured to execute instructions to perform:

dividing a current coding block into at least two sub-blocks;

for each of the sub-blocks,

constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and

predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.

13. The coder of claim 12, wherein the current coding block is divided according to a linear model type of the current coding block.

14. The coder of claim 13, wherein if the linear model type of the current coding block is LM or LIC, the current coding block is divided horizontally or vertically;

if the linear model type of the current coding block is LM or LIC, the current coding block is divided horizontally and vertically;

if the linear model type of the current coding block is LM_L, the current coding block is divided vertically; or

if the linear model type of the current coding block is LM_T, the current coding block is divided horizontally.

15. The coder of claim 12, wherein the current coding block is divided according to size information of the current coding block.

16. The coder of claim 15, wherein if a width of the current coding block is greater than a height of the current coding block, the current coding block is divided vertically;

if the width is less than the height, the current coding block is divided horizontally;

if the width is equal to the height, the current coding block is divided horizontally or vertically; or

if the width is equal to the height, the current coding block is divided horizontally and vertically.

17. The coder of claim 12, wherein if a linear model type of the sub-block is CCLM and a coded side of the sub-block has a stepped edge, the adjacent reconstructed pixels of the sub-block used for calculating the parameters of the linear model of the sub-block belong to another sub-block adjacent to the sub-block.

18. The coder of claim 12, wherein the LM prediction value of the sub-block is calculated by substituting the reference sub-block of the sub-block into the linear model.

19. The coder of claim 12, wherein the processor is configured to execute instructions to further perform:

calculating prediction cost of sub-block prediction by using LM prediction values of all the sub-blocks;

comparing the prediction cost of sub-block prediction with prediction cost of block-prediction and selecting a prediction mode with less prediction cost as a prediction mode of the current coding block, wherein the prediction cost of block-prediction indicates prediction cost for which linear model prediction is performed on the current coding block; and

coding the current coding block by using the selected prediction mode, and obtaining a code-stream of the current coding block, wherein the code stream of the current coding block

comprises a sub-block prediction flag, and the sub-block prediction flag is configured for indicating whether the current coding block adopts sub-block prediction.

20. The coder of claim 19, wherein the sub-block prediction flag comprises a sub-block prediction syntax element, and a value of the sub-block prediction syntax element is configured for indicating whether the current coding block adopts sub-block prediction and a manner in which the current coding block is divided; or

the sub-block prediction flag comprises a sub-block prediction syntax element, and a value of the sub-block prediction syntax element is configured for indicating whether the current coding block adopts sub-block prediction, and the sub-block prediction flag further comprises a manner in which the current coding block is divided when the current coding block adopts sub-block prediction; or

the sub-block prediction flag comprises a sequence number of intra-prediction mode of the current coding block, wherein the sequence number is newly added when the current coding block adopts sub-block prediction.

21. A non-transitory storage medium storing instructions, when executed, causing a processor to perform:

dividing a current coding block into at least two sub-blocks;

for each of the sub-blocks,

constructing a linear model, wherein parameters of the linear model are calculated by using pixel values of adjacent reconstructed pixels of the sub-block and adjacent reconstructed pixels of a reference sub-block of the sub-block; and

predicting the sub-block by using the linear model, and obtaining a LM prediction value of the sub-block.

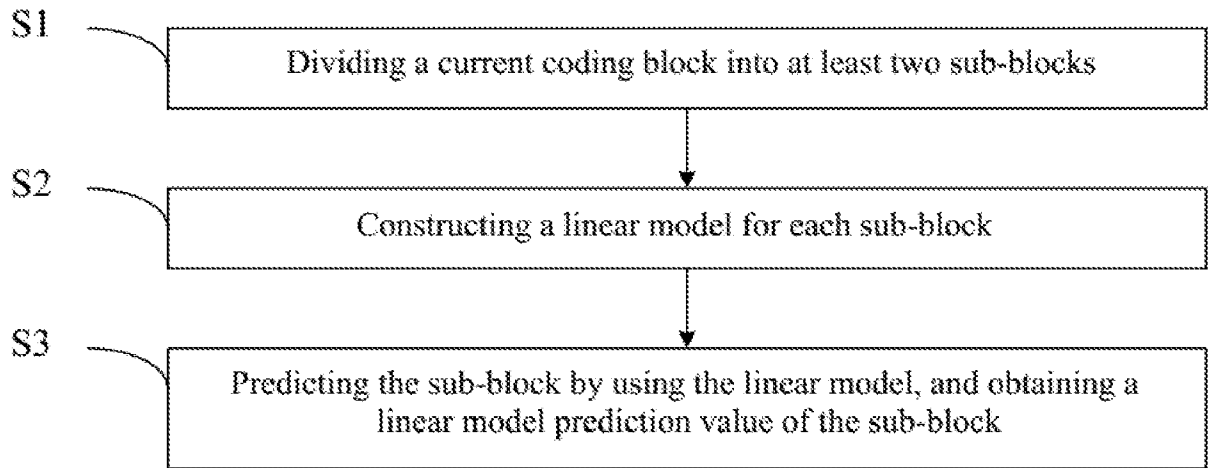


FIG. 1

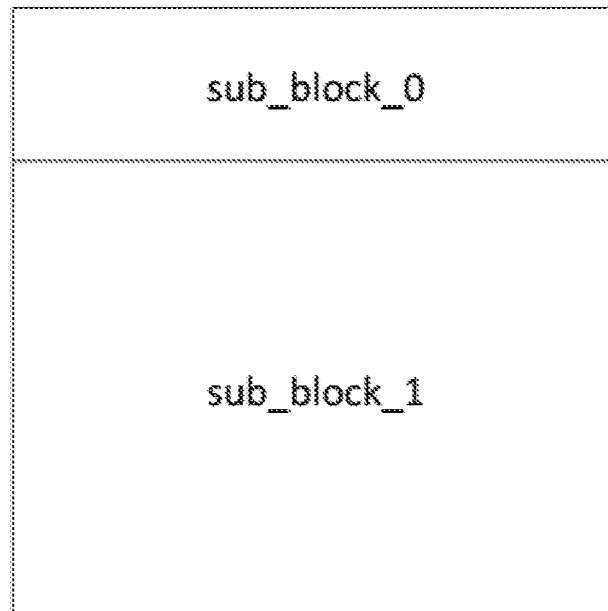


FIG. 2

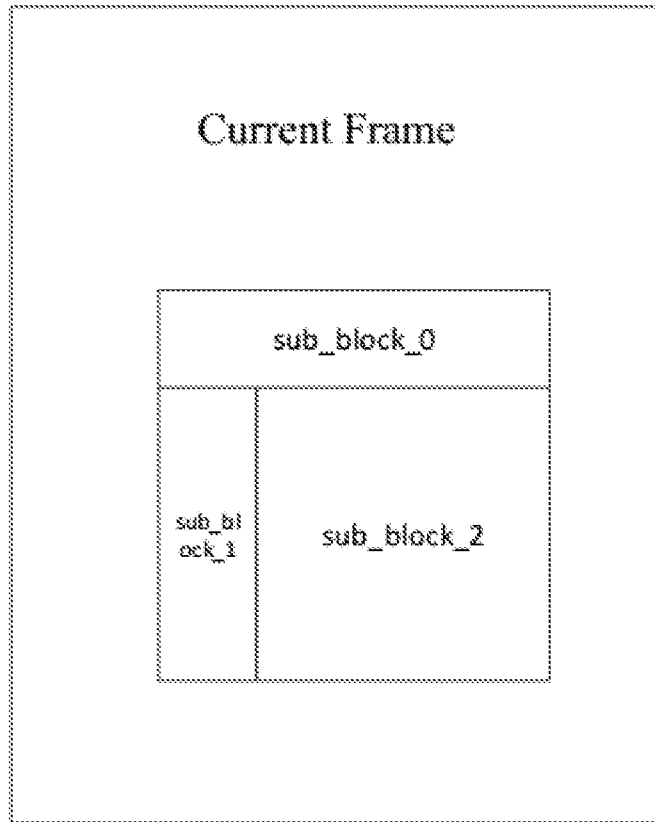


FIG. 3

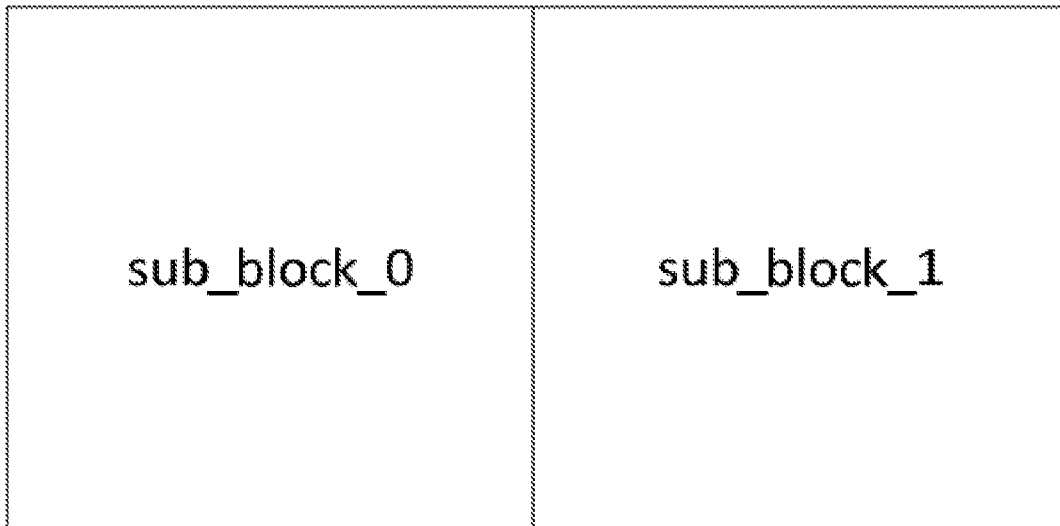


FIG. 4

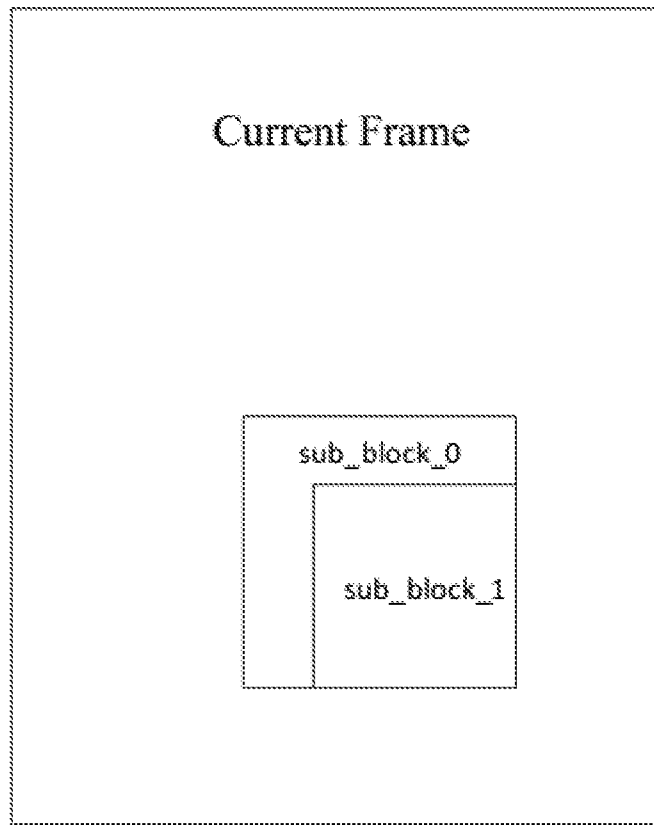


FIG. 5

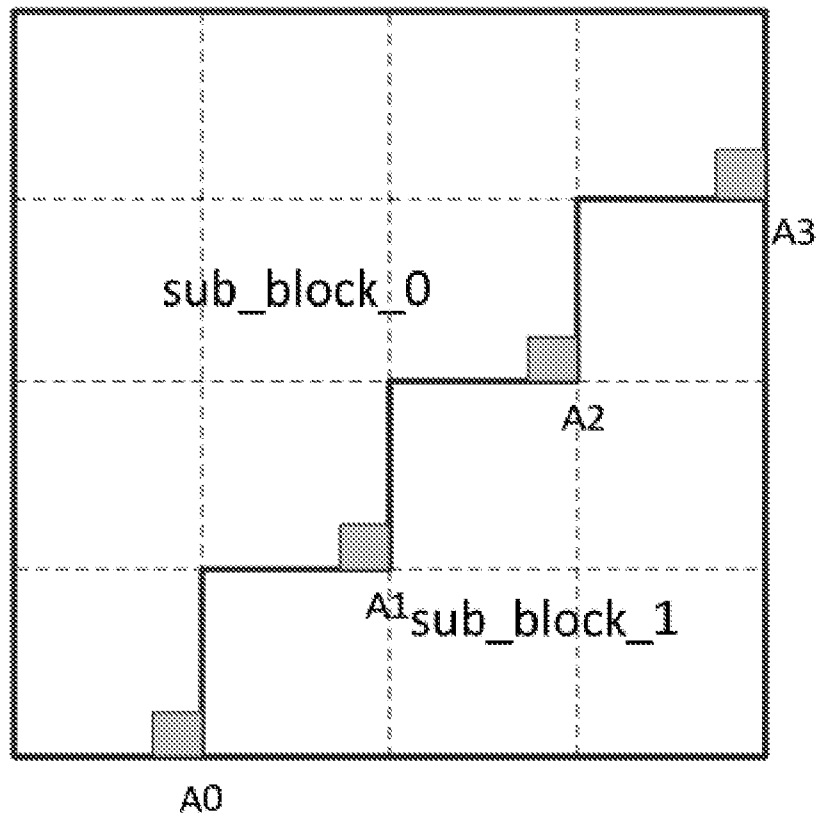


FIG. 6

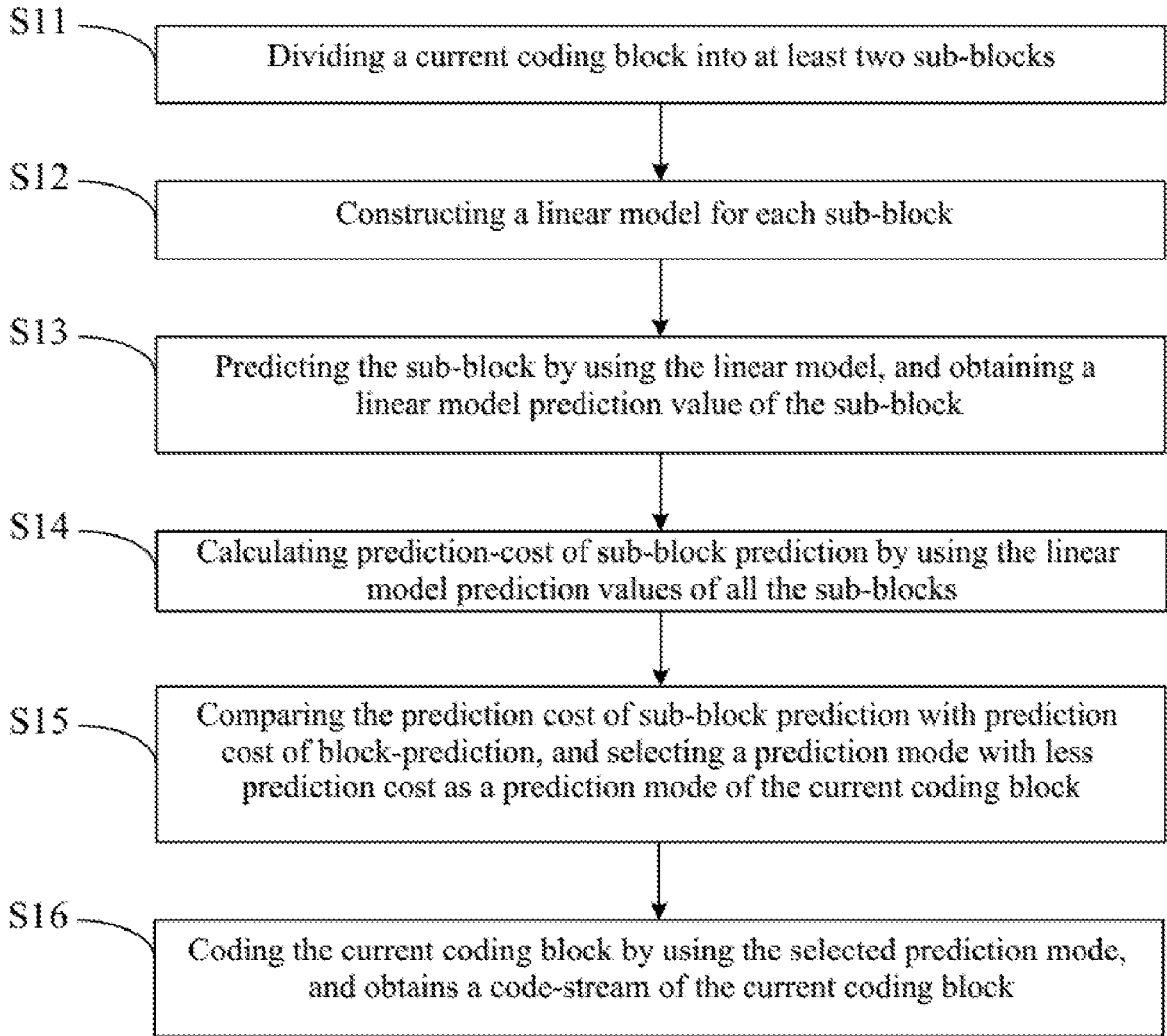


FIG. 7

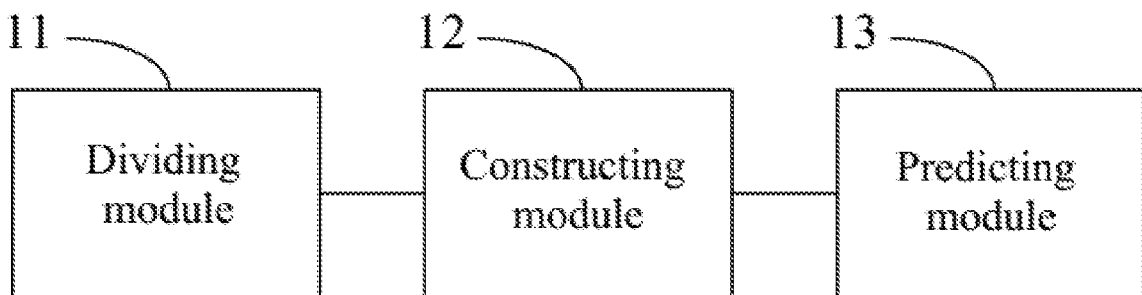


FIG. 8

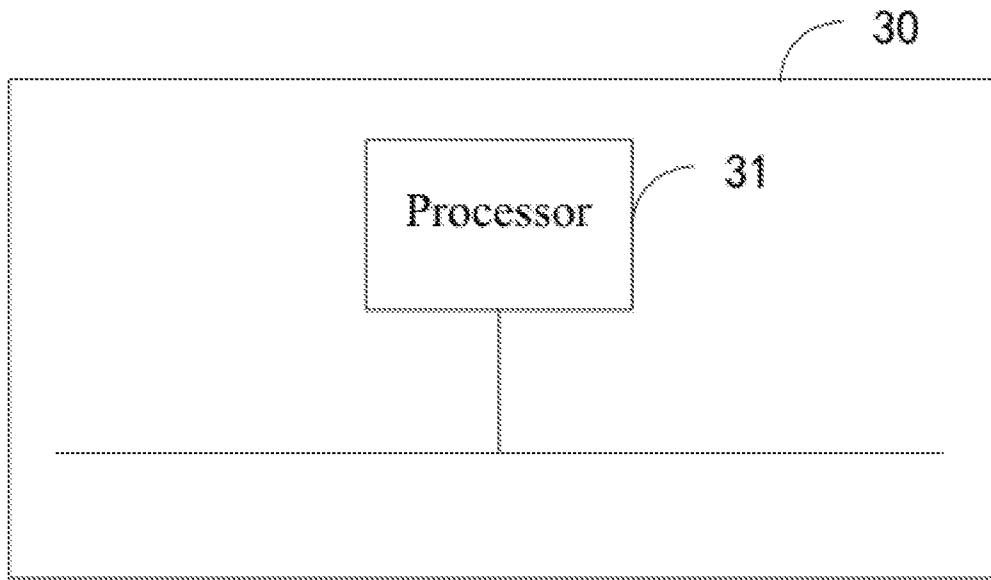


FIG. 9

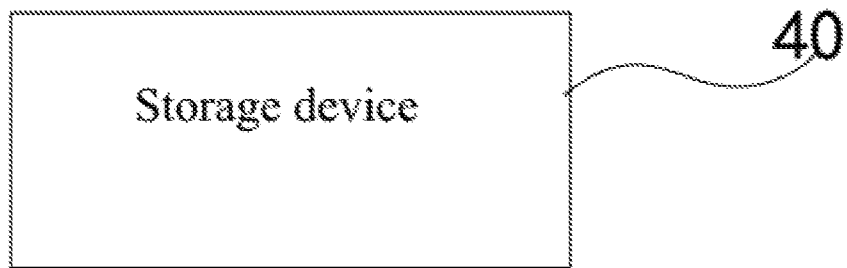


FIG. 10

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2020/110309

A. CLASSIFICATION OF SUBJECT MATTER

H04N 19/176(2014.01)i; H04N 19/119(2014.01)i; H04N 19/186(2014.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNKI, CNPAT, WPI, EPODOC, JVET:sub+, block, cod+ unit, CU, pu, predict+ unit, divid+, segment+, partit+, linear, model, cclm, local 2d illuminat+ 2d compensat+, LIC, horizon+, vertical+, cod+, encod+, decod+, compress+, decompress+

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	CN 110087087 A (UNIV. TONGJI) 02 August 2019 (2019-08-02) the whole document	1-21
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 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:

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"O" document referring to an oral disclosure, use, exhibition or other means

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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

28 October 2020

Date of mailing of the international search report

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Name and mailing address of the ISA/CN

National Intellectual Property Administration, PRC
6, Xitucheng Rd., Jimen Bridge, Haidian District, Beijing
100088
China

Authorized officer

QI, Ying

Facsimile No. (86-10)62019451

Telephone No. 86-10-53961718

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2020/110309**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

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A	UNNO, Kyohei et al. "'CE3-1.1: CU-based prediction for 1xN and 2xN sub-partitions'" <i>Joint Video Experts Team (JVET) of ITU-T SG 16 WP 3 and ISO/IEC JTC 1/SC 29/WG 11, 15th Meeting: Gothenburg, SE, 3-12 July 2019, JVET-00080, 12 July 2019 (2019-07-12), sections 1-3</i>	1-21
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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2020/110309

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				EP	3381189	A1	03 October 2018
				JP	2019500778	A	10 January 2019
				BR	112018010660	A2	13 November 2018
				AU	2016359168	A1	26 April 2018
				AU	2016359171	A1	10 May 2018
				EP	3381191	A1	03 October 2018
				US	2017150186	A1	25 May 2017
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