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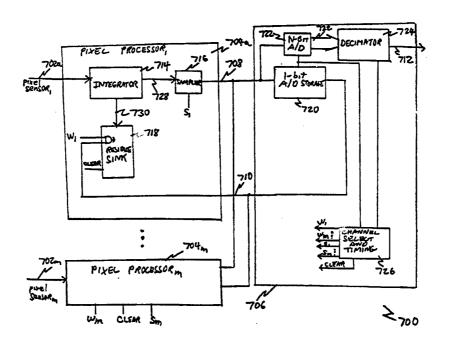
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(54) Title: METHOD AND APPARATUS FOR MULTIPLEXED OVERSAMPLED ANALOG TO DIGITAL MODULATION



### (57) Abstract

Apparatus for time multiplexed oversampled analog to digital modulation (700) is provided. Embodiments generally include a plurality of energy collection elements (702a and 702m) coupled to individual pixel processors 04a and 704m) preferably mounted proximate to each energy collection element. Each pixel processor shares a common block of conversion logic (720, 722 724) to form a plurality of integration loops to process the signal generated by each energy collection element. Specific embodiments are shown using CCD, CID, FET and charge well technologies.

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# INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER								
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According to	US CL :341/143 according to International Patent Classification (IPC) or to both national classification and IPC							
B. FIELDS SEARCHED								
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Electronic d	ata base consulted during the international search (nam	e of data base and, where practicable	search terms used)					
C. DOCUMENTS CONSIDERED TO BE RELEVANT								
Category*	Citation of document, with indication, where app	Relevant to claim No.						
Α	US 4,704,600 A (UCHIMURA et al	7						
Α	US 4,353,057 A (Bernet et al) 05 (	1						
Α	US 5,142,286 A (Ribner et al) 25 /	1						
Α	US 5,030,953 A (Chiang) 09 JULY	1						
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