

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
27 September 2007 (27.09.2007)

PCT

(10) International Publication Number
WO 2007/109040 A2

(51) International Patent Classification: **Not classified**

(21) International Application Number:
PCT/US2007/006395

(22) International Filing Date: 14 March 2007 (14.03.2007)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/782,458 15 March 2006 (15.03.2006) US

(71) Applicant (for all designated States except US): **INTER-DIGITAL TECHNOLOGY CORPORATION** [US/US];
3411 Silverside Road, Concord Plaza, Suite 105, Hagley Building, Wilmington, DE 19810 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **PIETRASKI, Philip, J.** [US/US]; 7 Talbot Place, Huntington Station, NY 11746 (US).

(74) Agent: **BALLARINI, Robert, J.**; Volpe and Koenig, P.C., United Plaza, Suite 1600, 30 South 17th Street, Philadelphia, PA 19103 (US).

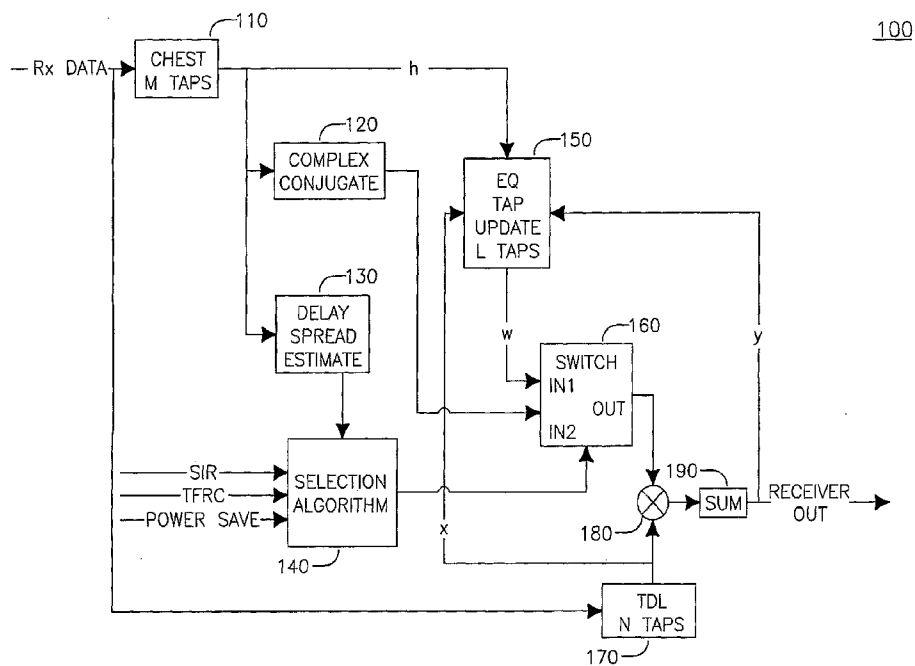
(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:
— without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND APPARATUS FOR SWITCHING OPERATING MODES OF A RECEIVER



(57) Abstract: In a wireless communication system, a method and apparatus switches operating modes of a receiver receiving data and operating in a first mode. The method comprises determining whether a criteria is met to switch the operating mode of the receiver. The operating mode of the receiver is switched from the first mode to a second mode if the criteria is met.

WO 2007/109040 A2

[0010] BRIEF DESCRIPTION OF THE DRAWINGS

[0011] A more detailed understanding of the invention may be had from the following description of a preferred embodiment, given by way of example and to be understood in conjunction with the accompanying drawings wherein:

[0012] Figure 1 is a functional block diagram of a receiver in accordance with the present invention; and

[0013] Figure 2 is a flow diagram of a method for switching operating modes of the receiver of Figure 1.

[0014] DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0015] When referred to hereafter, the terminology "wireless transmit/receive unit (WTRU)" includes but is not limited to a user equipment (UE), a mobile station, a fixed or mobile subscriber unit, a pager, a cellular telephone, a personal digital assistant (PDA), a computer, or any other type of user device capable of operating in a wireless environment. When referred to hereafter, the terminology "base station" includes but is not limited to a Node-B, a site controller, an access point (AP), or any other type of interfacing device capable of operating in a wireless environment.

[0016] Figure 1 is a functional block diagram of a receiver 100 in accordance with the present invention. The receiver 100 includes a channel estimate (CHEST) block 110, having M taps, a complex conjugate block 120, a delay spread estimate block 130, a selection block 140 (or selection algorithm block), an equalization (EQ) tap update block 150, having L taps, a switch 160, having inputs (designated In1 and In2) and an output, a tapped delay line (TDL) block 170 having L taps, a multiplier 180, and a summation (SUM) block 190.

[0017] In the present example, the CHEST block 110 receives the input data sequence (Rx Data) and performs an operation on it to produce a vector "M" elements long that is an estimate of the channel impulse response (CIR), a channel estimate. There are various methods of performing a channel estimate including, but not limited to, linear techniques such as correlations with a known pilot sequence, non-linear techniques such as squelching small path estimates to

reduce noise or decision directed methods, and adaptive techniques such as normalized least mean square (NLMS) or recursive least square (RLS) based channel estimates. The CHEST block 110 outputs a signal (h) to the EQ tap update block 150, the complex conjugate block 120 and delay spread estimate block 130.

[0018] The complex conjugate block 120 receives a vector of complex numbers as an input, and outputs a vector with each element being the complex conjugate of the corresponding input. The complex conjugate of the CIR may be used to realize a matched filter (MF) method and may be somewhat similar to that of a RAKE receiver, depending on the detail of the channel estimate. For example, if the elements of the channel estimate that do not correspond to a channel path location are squelched, the MF is very similar to a RAKE in performance. The complex conjugate block 120 outputs an MF signal to input In2 of the switch 160.

[0019] The delay spread estimate block 130 receives the channel estimate M length vector and computes a delay spread estimate. In one example, the delay spread may be calculated in accordance with the following equation:

$$delay_spread = \sqrt{\sum_{m=1}^M d_m |p_m|^2}; \quad \text{Equation (1)}$$

where d_m is the delay corresponding to the m^{th} element of the channel estimation vector and p_m is the value of the m^{th} element in the channel estimation vector. The delay spread estimate block 130 outputs its signal to the selection block 140.

[0020] The selection block 140 receives available data to determine if it is appropriate or acceptable to switch the receiver to the lower power MF receive. Such information may include, but is not limited to, Signal to Interference Ratio (SIR), the estimated channel delay spread received from the delay spread estimate block 130, the aggressiveness of the transmission being received (computable from the transport format resource combination (TFRC) input, or from the data rate attempted), the energy left in the receiver's battery (power save input), and/or indication from the network that it is permitted to switch.

The selection block 140 outputs a signal to the switch 160. The switch 160 also receives the signal (w) in the input In1 from the EQ tap update block 150.

[0021] The equalizer tap update block 150 generates the taps for the equalizer that are used when the receiver 100 operates as an advanced receiver. The output (w) is a vector of L elements. The inner product of the generated taps, w , and the TDL produce the equalizer output.

[0022] The switch block 160 receives either the equalizer taps or the conjugate of the channel estimate and forwards it on to an input of the multiplier 180. The multiplier 180 takes two vectors as inputs and computes the element-wise product to produce a third output vector. The elements of the output vector from 180 are all added up together in the SUM block 190 to produce either the equalizer or matched filter output, y , depending on which vector the switch 160 selected. In the case of adaptive equalizers, the output is fed back to the tap generation block. Where the MF is used and squelching was done in the channel estimate, the multiplications with zero elements may not need to be performed.

[0023] The TDL block 170 is preferably a shift register that receives the input data (Rx Data) and provides an output comprising a vector where the N elements of the vector are the last M values of the Rx Data signal. The TDL block 170 outputs a signal to the multiplier 180 and a signal (x) to the EQ tap update block 150. The multiplier 180 outputs a signal to the SUM block 190. The SUM block 190 outputs the receiver output signal (y), which is also a feedback to the EQ tap update block 150.

[0024] Figure 2 is a flow diagram of a method 200 for switching operating modes of the receiver 100 of Figure 1. In step 210, the receiver 100 is operating in equalizer mode. The receiver 100 then determines whether a criteria for switching modes is met (step 220). These may include when conditions do not permit the performance advantage of the equalizer mode, when conditions may indicate a preference for the MF operation, or when higher performance is not required. Under those circumstances, it may be beneficial to switch from equalizer operation to MF operation to save power or possibly to improve receiver performance.

[0025] For example, if a high data rate is requested, the SIR is high, the delay spread is low, and the battery life of the receiver 100 is good, then the criteria for switching modes is considered to not have been met and the receiver 100 continues to operate in equalizer mode (step 210). However, if for example, the battery life of the receiver 100 is low and/or the channel quality is excessive for the required data rate, then the receiver 100 switches to operating in MF mode (step 230). In this example, the decision to switch to the MF mode is made to save power. However, the decision to switch modes to the MF mode may also be made for performance reasons as described above, for example if the delay spread indicated that the channel consisted of essentially a single path.

[0026] When the receiver 100 is operating in the MF mode, all the computation in the EQ tap update block 150 may be stopped. This saves a considerable fraction of the receiver power. The CHEST block 110 may be enlarged to cover a larger window of M taps than the equalizer window of length L taps so that larger delay spread channel may be accommodated in the MF mode. The enlargement may either be permanent and only enabled in the MF mode, or may be dynamic if resources are available from the EQ tap update block 150 and reassigned to the CHEST block 110, without increasing gate-count complexity. The number of used taps in the TDL block 170, N , will either be set to L or M depending on the mode. The output of the receiver 100 may then be expressed as follows:

$$y(n) = \bar{v}_n^T \cdot \bar{x}(n), \tag{Equation (2)}$$

where

$$\bar{v}_n = \begin{cases} \bar{w}_n & \text{if EQ mode} \\ \bar{h}_n^* & \text{otherwise} \end{cases}.$$

[0027] The EQ tap update block 150 only runs when in the equalizer mode and computes the weights to be used when generating the output. The update equation is as follows:

$$\bar{w}_{n+1} = \alpha \cdot \bar{w}_n + \bar{\Delta}_n; \tag{Equation (3)}$$

where

$$\bar{\Delta}_n = \frac{\mu}{2 \cdot \|\bar{x}(n)\|^2} \cdot (\bar{h}^*(n) - 2 \cdot \bar{x}^*(n) \cdot y(n)).$$

[0028] For equalizer structures that make use of a channel estimate (for example, channel estimation enhanced NLMS (CENLMS) or non-adaptive block equalizers), the mode switching can be done with little or no increase in design complexity. Although the aspects of the present invention have been described in terms of the CENLMS, they are also applicable to other compatible structures.

[0029] Although the features and elements of the present invention are described in the preferred embodiments in particular combinations, each feature or element can be used alone without the other features and elements of the preferred embodiments or in various combinations with or without other features and elements of the present invention. The methods or flow charts provided in the present invention may be implemented in a computer program, software, or firmware tangibly embodied in a computer-readable storage medium for execution by a general purpose computer or a processor. Examples of computer-readable storage mediums include a read only memory (ROM), a random access memory (RAM), a register, cache memory, semiconductor memory devices, magnetic media such as internal hard disks and removable disks, magneto-optical media, and optical media such as CD-ROM disks, and digital versatile disks (DVDs).

[0030] Suitable processors include, by way of example, a general purpose processor, a special purpose processor, a conventional processor, a digital signal processor (DSP), a plurality of microprocessors, one or more microprocessors in association with a DSP core, a controller, a microcontroller, Application Specific Integrated Circuits (ASICs), Field Programmable Gate Arrays (FPGAs) circuits, any other type of integrated circuit (IC), and/or a state machine.

[0031] A processor in association with software may be used to implement a radio frequency transceiver for use in a wireless transmit receive unit (WTRU), user equipment (UE), terminal, base station, radio network controller (RNC), or any host computer. The WTRU may be used in conjunction with modules, implemented in hardware and/or software, such as a camera, a video camera

module, a videophone, a speakerphone, a vibration device, a speaker, a microphone, a television transceiver, a hands free headset, a keyboard, a Bluetooth® module, a frequency modulated (FM) radio unit, a liquid crystal display (LCD) display unit, an organic light-emitting diode (OLED) display unit, a digital music player, a media player, a video game player module, an Internet browser, and/or any wireless local area network (WLAN) module.

[0032] Embodiments

1. In a wireless communication system, a method for switching operating modes of a receiver receiving data, operating in a first mode and configured to produce an output.

2. The method of embodiment 1, further comprising determining whether a criteria is met to switch an operating mode of a receiver.

3. A method as in any preceding embodiment, further comprising switching an operating mode of a receiver from a first mode to a second mode if a criteria is met.

4. A method as in any preceding embodiment wherein a first and second operating modes include an equalizer mode and/or a matched filter (MF) mode.

5. A method as in any preceding embodiment wherein a criteria for switching an operating mode of a receiver includes performance related criteria.

6. A method as in any preceding embodiment wherein a criteria for switching an operating mode of a receiver includes a signal to interference ratio (SIR).

7. A method as in any preceding embodiment wherein a criteria for switching an operating mode of a receiver includes a delay spread estimation.

8. A method as in any preceding embodiment, further comprising switching an operating mode of a receiver from a first mode to a second mode if a delay spread estimation indicates that a transmission channel consists of a single path.

9. A method as in any preceding embodiment wherein a criteria for switching an operating mode of a receiver includes a battery power level of the receiver.

10. A method as in any preceding embodiment, further comprising switching an operating mode of a receiver from a first mode to a second mode if a battery power of the receiver is lower than a pre-defined value.

11. A method as in any preceding embodiment, further comprising performing a channel estimation on a receive data using M number of taps.

12. A method as in any preceding embodiment, further comprising performing equalization on received data using a tap update of L number of taps.

13. A method as in any preceding embodiment wherein a receiver is operating in a second mode, further comprising stopping equalization of received data.

14. A method as in any preceding embodiment wherein a receiver is operating in a second mode, further comprising enlarging M number of taps.

15. A method as in any preceding embodiment wherein an enlargement of a number of M taps is permanent.

16. A method as in any of embodiments 1-14 wherein an enlargement of a number of M taps is dynamic.

17. A method as in any preceding embodiment, further comprising reassigning equalization resources to channel estimation resources.

18. A method as in any preceding embodiment, further comprising setting a number N of tapped delay line (TDL) taps based upon an operating mode of a receiver.

19. A method as in any preceding embodiment wherein a number N is set to an L number of taps when a receiver is operating in an equalization mode.

20. A method as in any preceding embodiment wherein a number N is set to an M number of taps when a receiver is operating in an MF mode.

21. A method as in any preceding embodiment, further comprising weighting an output of a receiver.

22. A receiver configured to perform a method as in any preceding embodiment.

23. The receiver of embodiment 22, further comprising a channel estimation block.

24. A receiver as in any of embodiments 22-23 wherein a channel estimation block includes a window length of M taps.

25. A receiver as in any of embodiments 22-24, further comprising an equalization block.

26. A receiver as in any of embodiments 22-25 wherein an equalization block includes a window of L taps and is configured to generate an equalized signal.

27. A receiver as in any of embodiments 22-26, further comprising a selection block.

28. A receiver as in any of embodiments 22-27 wherein a selection block is configured to determine whether a criteria is met to switch from a first operating mode of a receiver to a second operating mode.

29. A receiver as in any of embodiments 22-28, further comprising a switch.

30. A receiver as in any of embodiments 22-29 wherein a switch is configured to receive an equalized signal from a complex conjugation block, and an input from the selection block.

31. A receiver as in any of embodiments 22-30 wherein a switch is configured to output either an equalized signal or an MF output based upon an input from a selection block.

32. A receiver as in any of embodiments 22-31 wherein a selection block is configured to receive and SIR input, a transport format resource combination (TFRC) input, a delay spread estimate, and/or a power save input.

33. A receiver as in any of embodiments 22-32 wherein a selection block is configured to signal to a switch to switch from a first mode of operation to a second mode of operation.

34. A receiver as in any of embodiments 22-33 wherein a first mode of operation is an equalization mode and a second mode of operation is an MF mode.

*

*

*

CLAIMS

What is claimed is:

1. In a wireless communication system, a method for switching operating modes of a receiver receiving data, operating in a first mode and configured to produce an output, the method comprising:

(a) determining whether a criteria is met to switch the operating mode of the receiver; and

(b) switching the operating mode of the receiver from the first mode to a second mode if the criteria is met; and

wherein the first and second operating modes are selected from the group consisting of an equalizer mode and a matched filter (MF) mode.

2. The method of claim 1 wherein the criteria for switching the operating mode of the receiver includes performance related criteria.

3. The method of claim 1 wherein the criteria for switching the operating mode of the receiver includes the signal to interference ratio (SIR).

4. The method of claim 1 wherein the criteria for switching the operating mode of the receiver includes a delay spread estimation.

5. The method of claim 4 wherein step (b) is performed if the delay spread estimation indicates that the transmission channel consists of a single path.

6. The method of claim 1 wherein the criteria for switching the operating mode of the receiver includes the battery power level of the receiver.

7. The method of claim 6 wherein step (b) is performed if the battery power is lower than a pre-defined value.

8. The method of claim 1 wherein the first mode is the equalizer mode and the second mode is the MF mode.

9. The method of claim 8, further comprising:

(c) performing a channel estimation on the received data using M number of taps.

10. The method of claim 9, further comprising:

(d) performing equalization on the received data using a tap update of L number of taps.

11. The method of claim 10 wherein the receiver is operating in the second mode, further comprising:

(e) stopping the equalization of the received data.

12. The method of claim 11, further comprising:

(f) enlarging the M number of taps.

13. The method of claim 12 wherein the enlargement of the number of M taps is permanent.

14. The method of claim 12 wherein the enlargement of the number of M taps is dynamic.

15. The method of claim 14, further comprising reassigning equalization resources to channel estimation resources.

16. The method of claim 10, further comprising:

(e) setting a number N of tapped delay line (TDL) taps based upon the operating mode of the receiver.

17. The method of claim 16 wherein the number N is set to the L number of taps when the receiver is operating in the equalizer mode.

18. The method of claim 16 wherein the number N is set to the M number of taps when the receiver is operating in the MF mode.

19. The method of claim 1, further comprising:

(c) weighting the output of the receiver.

20. In a wireless communication system, a receiver comprising:
a channel estimation block, said channel estimation block including a window length of M taps;

a complex conjugation block, said complex conjugation block configured to generate a matched filter (MF) output;

an equalization block, said equalization block including a window of L taps and configured to generate an equalized signal;

a selection block configured to determine whether a criteria is met to switch from a first operating mode of the receiver to a second operating mode; and

a switch, configured to receive the equalized signal from equalization block, the MF output from the complex conjugation block, and an input from the selection block, wherein the switch is configured to output either the equalized signal or the MF output based upon the input from the selection block.

21. The receiver of claim 20 wherein the selection block is configured to receive a signal to noise ratio (SNR) input, a transport format resource combination (TFRC) input, a delay spread estimate, and a power save input.

22. The receiver of claim 21 wherein the selection block is further configured to signal to the switch to switch from the first mode of operation to the second mode of operation.

23. The receiver of claim 20 wherein the first mode of operation is an equalization mode and the second mode of operation is a matched filter (MF) mode.

100

1/2

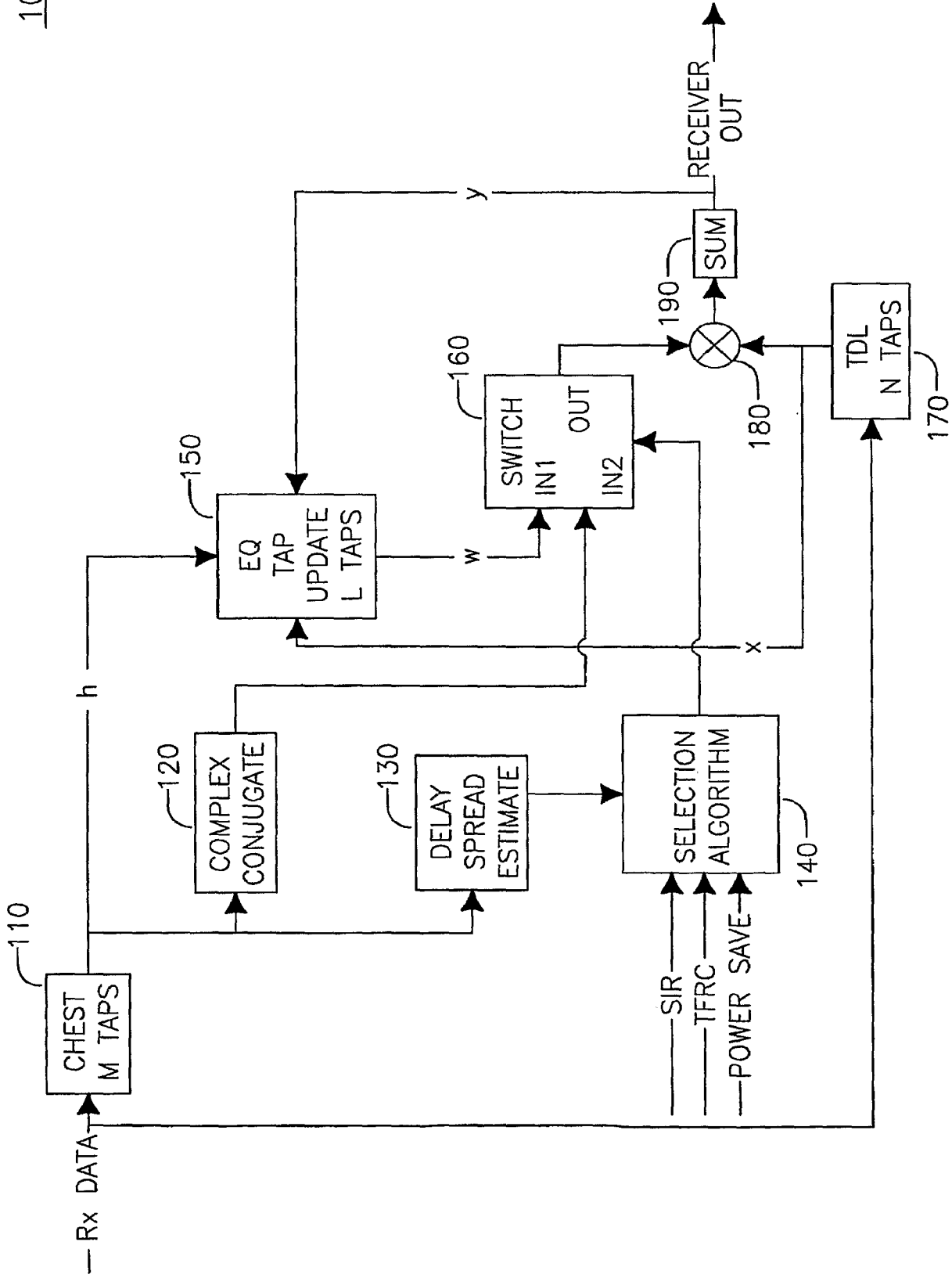


FIG. 1

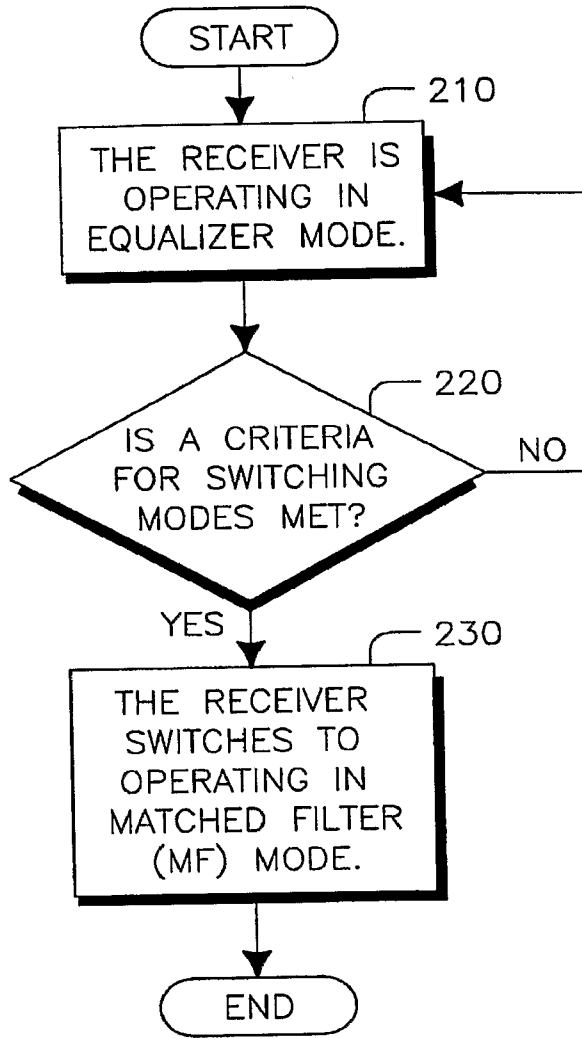


FIG.2