(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) **World Intellectual Property Organization**
International Bureau

(10) **International Publication Number**
WO 2021/158482 A1

(43) **International Publication Date**
12 August 2021 (12.08.2021)

(51) **International Patent Classification:**
H01L 21/311 (2006.01) ***H01L 21/02*** (2006.01)

(21) **International Application Number:**
PCT/US2021/016076

(22) **International Filing Date:**
01 February 2021 (01.02.2021)

(25) **Filing Language:** English

(26) **Publication Language:** English

(30) **Priority Data:**
62/969,567 03 February 2020 (03.02.2020) US

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(81) **Designated States** *(unless otherwise indicated, for every kind of national protection available)*: AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, IT, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, WS, ZA, ZM, ZW.

(84) **Designated States** *(unless otherwise indicated, for every kind of regional protection available)*: ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK,

(54) **Title:** METHOD FOR USING ULTRA-THIN ETCH STOP LAYERS IN SELECTIVE ATOMIC LAYER ETCHING

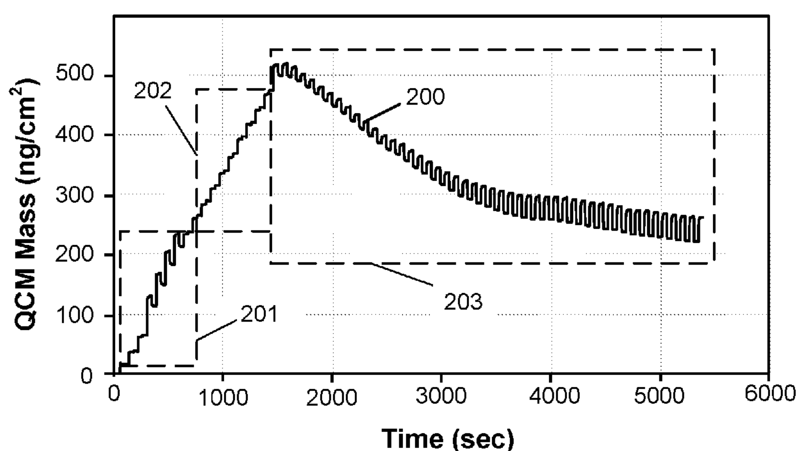

FIG. 2

(57) **Abstract:** Method for selective etching of materials using an ultrathin etch stop layer (ESL), where the ESL is effective at a thickness as small as approximately one monolayer using atomic layer etching (ALE). A substrate processing method includes depositing a first film on a substrate, depositing a second film on the first film, and selectively etching the second film relative to the first film using an ALE process, where the etching self-terminates at an interface of the second film and the first film.

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[Continued on next page]

EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— *with international search report (Art. 21(3))*

METHOD FOR USING ULTRA-THIN ETCH STOP LAYERS IN SELECTIVE ATOMIC LAYER ETCHING

CROSS-REFERENCE TO RELATED APPLICATIONS

5 **[0001]** This application claims priority to U.S. Provisional Patent Application No. 62/969,567, entitled, "METHOD FOR USING ULTRA-THIN ETCH STOP LAYERS IN SELECTIVE ATOMIC LAYER ETCHING," filed February 3, 2020; the disclosure of which is expressly incorporated herein, in its entirety, by reference.

10

FIELD OF INVENTION

[0002] The present invention relates to the field of semiconductor manufacturing and semiconductor devices, and more particularly, to a method of using ultra-thin inorganic etch stop layers in semiconductor processing.

15

BACKGROUND OF THE INVENTION

[0003] In the semiconductor and related industries, the fabrication of nanostructures and nanopatterns has resulted in demand for achieving near-atomic level accuracy and selectivity in depositing and etching different materials. Examples include metal filling of fine interconnect features, and formation of ultra-thin gate dielectrics and ultra-thin channels used
20 in field-effect transistors and other nanodevices below the 10 nm scale. Atomic layer deposition (ALD) and atomic layer etching (ALE) processes can define the atomic layer growth and removal required for advanced semiconductor fabrication, producing ultrasmooth thin films based on deposit/etch-back methods and conformal etching in high-aspect-ratio structures.

25

SUMMARY OF THE INVENTION

[0004] Methods for selective etching of materials using an ultrathin etch stop layer (ESL) is described, where the ESL is effective at a thickness as small as approximately one monolayer when using an ALE process.

30 **[0005]** According to one embodiment, a substrate processing method includes depositing a first film on a substrate, depositing a second film on the first film, and selectively etching the

second film relative to the first film using an ALE process, where the etching self-terminates at an interface of the second film and the first film.

[0006] According to another embodiment, a substrate processing method includes providing a substrate containing a first film on a substrate and a second film on the first film,
5 initiating etching of the second film using an ALE process that selectively etches the second film relative to the first film, and removing the second film using the ALE process, where the etching self-terminates at an interface of the second film and the first film. The method further includes, following the removing, etching the first film using an additional ALE process, where the ALE process includes alternating gaseous exposures of a first reactant and
10 a second reactant, and the additional ALE process includes alternating gaseous exposures of a third reactant and a fourth reactant, and where the ALE process and the additional ALE process are performed without plasma excitation of the first reactant, the second reactant, the third reactant, and the fourth reactant. According to one embodiment, the first film has a uniform thickness of approximately one monolayer.

15 **[0007]** According to another embodiment, a substrate processing method includes depositing a ZrO_2 film on a substrate, depositing a Al_2O_3 film on the ZrO_2 film, initiating etching of the Al_2O_3 film using a thermal ALE process that selectively etches the Al_2O_3 film relative to the ZrO_2 film, and removing the Al_2O_3 film using the thermal ALE process, wherein the etching self-terminates at an interface of the Al_2O_3 film and the ZrO_2 film. According to one
20 embodiment, the ZrO_2 film has a uniform thickness of approximately one monolayer. According to one embodiment, the thermal ALE process includes alternating gaseous exposures of HF and $Al(CH_3)_3$. According to one embodiment, the method further includes, following the removing, etching the ZrO_2 film using an additional thermal ALE process that includes alternating gaseous exposures of HF and $Al(CH_3)_2Cl$.

25

DETAILED DESCRIPTION OF THE DRAWINGS

[0008] In the accompanying drawings:

[0009] FIGS. 1A – 1E schematically show a method of processing a layer structure according to an embodiment of the invention;

30 **[0010]** FIG. 2 shows a substrate mass change traced with a quartz crystal microbalance (QCM) during deposition/etch processes according to an embodiment of the invention;

[0011] FIG. 3 shows a substrate mass change traced with a QCM during deposition/etch processes according to embodiment of the invention;

[0012] FIG. 4 shows etch rate measured by QCM according to an embodiment of the invention;

[0013] FIG. 5 shows a substrate mass change traced with a QCM during an ALE process according to embodiment of the invention; and

5 **[0014]** FIG. 6 shows in tabular form examples of combinations of etch reactants and materials that may be used for selective ALE according to embodiments of the invention.

DETAILED DESCRIPTION OF SEVERAL EMBODIMENTS

[0015] In fabrication of semiconductor devices, an ESL is used in material stacks to stop an
10 etch process at an interface of different materials or to protect an underlying material from etching. Embodiments of the invention describe the use of an ESL that may be only one monolayer (atomic layer) thick and may be deposited and later removed in-situ in one or more process chambers. The methods described herein can provide significant reduction in processing time and materials usage in semiconductor device manufacturing, and allow
15 deposition/etch processes in nano-sized spaces and 3D features. Further, the methods can reduce problems associated with stress buildup during integration of multi-stacks of materials in semiconductor devices.

[0016] According to one embodiment, a method is described for selective etching of materials using an ultrathin ESL, where the ESL is effective in ALE processing at a thickness
20 as small as approximately one monolayer. ALE is an etching technique for removing thin layers of material using sequential and self-limiting reactions. Thermal ALE, that is performed in the absence of plasma excitation, provides isotropic atomic-level etch control using sequential thermally driven reaction steps that are self-saturating and self-terminating. Thermal ALE etch mechanisms can include fluorination and ligand-exchange, conversion-
25 etch, and oxidation and fluorination reactions. The etching accuracy can reach atomic-scale dimensions, and a large area of uniform substrate etching can be achieved. Examples of substrates that may be processed using the embodiments of the invention include thin wafers of a semiconductor material (e.g., Si) that are conventionally found in semiconductor manufacturing and can have diameter of 100mm, 200mm, 300mm, or larger. However, other
30 types of substrates may be used, for examples substrates for making solar panels.

[0017] FIGS. 1A – 1E schematically show a method of processing a layer structure according to an embodiment of the invention. As schematically shown in FIG. 1A, the method includes providing a substrate 1 containing a base material 100 (e.g., a Si wafer), and a bottom film

102 on the base material 100. Although not shown in FIG. 1A, the substrate 1 may contain one or more additional films and materials and one or more simple or advanced patterned features.

[0018] In FIG. 1B, the method further includes depositing a first film 104 over the bottom film 102. According to embodiments of the invention, the first film 104 may serve as an ESL. In one example, the first film 104 is a dielectric film. In some examples, the first film 102 can include a metal oxide film with a general formula M_xO_y, where x and y are integers. Examples include ZrO_2 and Al_2O_3. In one example, the first film 104 can include ZrO_2 that may be uniformly deposited on the base material 100 using ALD processing. However, the first film 102 is not limited to metal oxides and may include or consist of other materials, for example oxides, nitrides, oxynitrides, and other materials found in semiconductor devices.

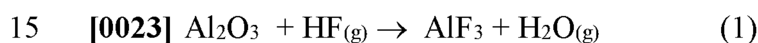
[0019] In FIG. 1C, the method further includes depositing a second film 106 on the first film 104, where the second film 106 contains a different material than the first film 104. According to embodiments of the invention, the first film 104 may be used to stop a subsequent etch process at an interface of the second film 106 and the first film 104 or to protect the first film 102 from etching. In one example, the second film 106 is a dielectric film. In some examples, the second film 106 can include a metal oxide film with a general formula M_xO_y, where x and y are integers. Examples include ZrO_2, HfO_2, and Al_2O_3. In one example, the second film 106 can include Al_2O_3 that may be uniformly deposited on the first film 104 using ALD processing. However, the second film 106 is not limited to metal oxides and may include or consist of other materials, for example oxides, nitrides, oxynitrides, and other materials found in semiconductor devices.

[0020] The method further includes initiating etching of the second film 106 using an ALE process (e.g., a thermal ALE process) that selectively etches the second film 106 relative to the first film 104. The ALE process removes the second film 106 until the etching self-terminates at the interface of the second film 106 and the first film 104 due to the selective etching characteristics of the ALE process. FIG. 1D schematically shows the substrate 1 when the second film 106 has been removed from the substrate 1. Thereafter, according to one embodiment, the first film 104 may be removed from the substrate 1, for example using an additional ALE process. This is schematically shown in FIG. 1D.

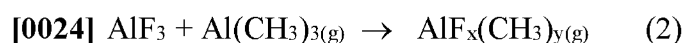
[0021] FIG. 2 shows a substrate mass change traced with a quartz crystal microbalance (QCM) during deposition/etch processes according to an embodiment of the invention. The mass trace 200 shows substrate mass gain/loss in ng/cm^2 on a QCM as a function of time,

where mass gain and mass loss correspond to deposition and etch processes, respectively. The film structure included a bottom Al_2O_3 film, a ZrO_2 film on the bottom Al_2O_3 film, and a top Al_2O_3 film on the ZrO_2 film. The mass trace 200 is divided into three sections, where the first section 201 shows mass gain during ALD of the ZrO_2 film having a monolayer thickness on the bottom Al_2O_3 film, second section 202 shows mass gain during ALD of the top Al_2O_3 film on the ZrO_2 film, and third section 203 shows mass loss during etching and removal of the top Al_2O_3 film using an ALE process. The ALD of the ZrO_2 film was performed using alternating gaseous exposures of zirconium tetrachloride ($ZrCl_4$) and water (H_2O), and the ALD of the top Al_2O_3 film was performed using alternating gas exposures of trimethyl aluminum ($Al(CH_3)_3$) and H_2O. The ALE of the top Al_2O_3 film used alternating gas exposures of hydrogen fluoride (HF) and $Al(CH_3)_3$, where each ALD cycle included Al_2O_3 surface fluorination using a HF exposure, followed by exposure to $Al(CH_3)_3$, which resulted in etching of the fluorinated surface layer (i.e., AlF_3) through a ligand exchange reaction.

[0022] Unbalanced ALE reactions for etching of the top Al_2O_3 film include:



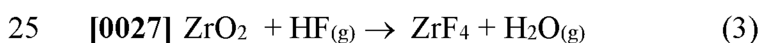
[0023] $Al_2O_3 + HF_{(g)} \rightarrow AlF_3 + H_2O_{(g)}$ (1)



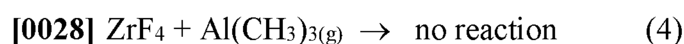
[0024] $AlF_3 + Al(CH_3)_{3(g)} \rightarrow AlF_x(CH_3)_{y(g)}$ (2)

[0025] The etching of the top Al_2O_3 film proceeds until the top Al_2O_3 film is fully removed and then the ALE process self-terminates at the interface of the top Al_2O_3 film and the ZrO_2 film. The ALE process self-terminates because the ZrO_2 film is highly resistant to etching by the alternating gases exposures of HF and $Al(CH_3)_3$. Although the ZrO_2 film undergoes fluorination upon reaction with HF to form ZrF_4, the ligand exchange reaction with $Al(CH_3)_3$ is thermodynamically unfavorable under the ALE conditions and this disrupts and stops the etching process.

[0026] Unbalanced ALE reactions for the ZrO_2 film include:



[0027] $ZrO_2 + HF_{(g)} \rightarrow ZrF_4 + H_2O_{(g)}$ (3)



[0028] $ZrF_4 + Al(CH_3)_{3(g)} \rightarrow$ no reaction (4)

[0029] The etch resistance of the ZrO_2 film is clearly shown in section 203 of FIG. 2, where, during removal of the top Al_2O_3 film, the measured mass trace 200 asymptotically approaches the mass of the ZrO_2 film after a large number of ALE cycles. Although fluorination of ZrO_2 is observed as a mass gain in each ALE cycle, following the subsequent exposure of the fluorinated surface to $Al(CH_3)_{3(g)}$, no net change in mass is observed, indicating a passive surface toward an exchange reaction. Thus, the etch process stops on the ZrO_2 film after fully etching and removing the top Al_2O_3 film, thereby demonstrating that

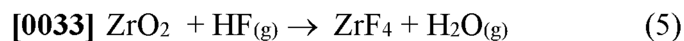
the ZrO_2 film, although having only a monolayer thickness, acts as an ESL to effectively protect the underlying material (i.e., the bottom Al_2O_3 film) from etching. From a thermodynamic point of view, the etch blocking ability of the ZrO_2 film as an ESL can in theory be infinite as the ligand exchange reaction is thermodynamically unfavorable under
5 the ALE conditions. This allows an ultra-thin ESL with a monolayer thickness to effectively block the ALE process by using a proper material as an ESL.

[0030] FIG. 3 shows substrate mass change traced with a QCM during deposition/etch processes according to embodiment of the invention. The trace 300 shows mass gain during ALD of a ZrO_2 film using alternating gas exposures of $ZrCl_4$ and H_2O, and mass change
10 during subsequent ALE processing of the ZrO_2 film using alternating gas exposures of HF and $Al(CH_3)_3$. The robustness of the ZrO_2 film as an ESL is clearly demonstrated and shows a 100% blocking efficiency of the ZrF_4 surface of the ZrO_2 film, even after 100 cycles of the ESL process.

[0031] FIG. 4 shows etch rate measured by QCM according to embodiment of the invention.
15 The etch rate of an Al_2O_3 film in an ALE process as a function of different amounts of ZrO_2 pre-deposited on the Al_2O_3 film is shown in the figure. The ZrO_2 was deposited by ALD using alternating gas exposures of $Al(CH_3)_3$ and H_2O, and the ALE process was performed using alternating gas exposures of HF and $Al(CH_3)_3$. The experimental data in solid circles 400 shows that increasing amount of ZrO_2 deposited on the Al_2O_3 film resulted in reduced
20 amount of etching of the underlying Al_2O_3 film. Particularly, about 200 ng of ZrO_2, which corresponds to approximately one monolayer of ZrO_2 deposited on the Al_2O_3 film, reduced the Al_2O_3 etch rate to approximately zero value. Increasing the thickness of the ZrO_2 film to above a monolayer thickness did not affect the etch rate, since the ZrO_2 already fully covered the Al_2O_3 film. The effective etch stopping at a thickness of only approximately one
25 monolayer of ZrO_2 is in agreement with the unfavorable thermodynamics of the etch reaction, where Al_2O_3 surface reaction sites are passivated with ZrO_2. Further, the effective etch blocking of ZrO_2 at a thickness of approximately one monolayer shows that the first monolayer of ZrO_2 uniformly covers the Al_2O_3 film and that the $ZrCl_4$ precursor is more reactive towards exposed Al_2O_3 surface sites than the ZrO_2 covering the Al_2O_3 film.

30 **[0032]** FIG. 5 shows a substrate mass change traced with a QCM during an ALE process according to embodiment of the invention. Although a ZrO_2 film is not etched by thermal ALE processing that etches a Al_2O_3 film using alternating gas exposures of HF and $Al(CH_3)_3$, the ZrO_2 film may be etched and removed by replacing one or more of the gaseous etch

reactants in the ALE processing. In FIG. 5, a ZrO_2 film was etched, as shown in trace 500, by thermal ALE processing using alternating gas exposures of HF and dimethyl aluminum chloride (DMAC, $Al(CH_3)_2Cl$). Replacing $Al(CH_3)_3$ with $Al(CH_3)_2Cl$ renders the ligand exchange reaction thermodynamically favorable and thereby enables etching of the ZrO_2 film according the following unbalanced ALE reactions:



[0033] $ZrO_2 + HF_{(g)} \rightarrow ZrF_4 + H_2O_{(g)}$ (5)

[0034] $ZrF_4 + Al(CH_3)_2Cl_{(g)} \rightarrow ZrF_xCl_{y(g)}$ (6)

[0035] The etching of the ZrO_2 film is illustrated by the stepwise mass loss in the QCM trace.

[0036] FIG. 6 shows in tabular form examples of combinations of etch reactants and materials that may be used for selective ALE according to embodiments of the invention. The listed combinations are based on experimental and thermodynamic information. In one example illustrated in FIG. 6, a ZrO_2 film may be used as an ESL for thermal ALE processing of Al_2O_3 and HfO_2 films using alternating gaseous exposures of HF and $Al(CH_3)_3$. Thereafter, if desired, the ZrO_2 film may be removed using alternating gaseous exposures of HF and $Al(CH_3)_2Cl$, for example. In another example, an Al_2O_3 film may be used as an ESL for thermal ALE processing of ZrO_2 and HfO_2 films using alternating gaseous exposures of HF and $SiCl_4$. Thereafter, if desired, the Al_2O_3 film may be removed using alternating gaseous exposures of HF and $Al(CH_3)_3$, for example.

[0037] According to some embodiments, the ALD processing, the ALE processing, or both, may be performed at a substrate temperature between about 100°C and about 400°C, between about 200°C and about 400°C, or between about 200°C and about 300°C. In one example, the ALD processing, the ALE processing, or both, may be performed at a substrate temperature between about 250°C and about 280°C.

[0038] In some examples, the ALD processing and the ALE processing may be performed at the same substrate temperature or at approximately the same substrate temperature. Those skilled in the art will readily appreciate that this allows for high substrate throughput when performing both the ALD processing and the ALE processing in the same process chamber, and when using different process chambers for the ALD processing and the ALE processing.

[0039] In some examples, two or more of the ALD processing, the ALE processing, and the additional ALE processing may be performed at that same substrate temperature or at approximately the same substrate temperature. For example, the ALE processing and the additional ALE processing may be performed at the same substrate temperature or at approximately the same substrate temperature.

[0040] A plurality of embodiments for a method for selective etching of materials using an ultrathin etch stop layer (ESL) have been described. The foregoing description of the embodiments of the invention has been presented for the purposes of illustration and description. It is not intended to be exhaustive or to limit the invention to the precise forms
5 disclosed. This description and the claims following include terms that are used for descriptive purposes only and are not to be construed as limiting. Persons skilled in the relevant art can appreciate that many modifications and variations are possible in light of the above teaching. It is therefore intended that the scope of the invention be limited not by this detailed description, but rather by the claims appended hereto.

10

What is claimed is:

1. A substrate processing method, comprising:
   depositing a first film on a substrate;
   depositing a second film on the first film; and
   selectively etching the second film relative to the first film using an atomic layer etching (ALE) process, wherein the etching self-terminates at an interface of the second film and the first film.

2. The method of claim 1, wherein the ALE process includes alternating gaseous exposures of a first reactant and a second reactant.

3. The method of claim 2, wherein the ALE process includes a thermal ALE process that is performed without plasma excitation of the first reactant and the second reactant.

4. The method of claim 1, wherein the first and second films are dielectric films.

5. The method of claim 1, wherein the first and second films include different metal oxide films that are selected from the group consisting of Al_2O_3, ZrO_2, and HfO_2.

6. The method of claim 1, wherein the second film includes an Al_2O_3 film.

7. The method of claim 6, wherein the Al_2O_3 film is deposited using alternating gas exposures of $Al(CH_3)_3$ and H_2O in an atomic layer deposition (ALD) process.

8. The method of claim 1, wherein the ALE process includes alternating gaseous exposures of 1) HF and 2) $Sn(acac)_2$, $Al(CH_3)_3$, $Al(CH_3)_2Cl$, $SiCl_4$, or $TiCl_4$.

9. The method of claim 1, wherein the first film includes a ZrO_2 film.

10. The method of claim 9, wherein the ZrO_2 film has a uniform thickness of approximately one monolayer.

11. The method of claim 9, wherein the ZrO_2 film is deposited using alternating gas exposures of $ZrCl_4$ and H_2O in an atomic layer deposition (ALD) process.

12. The method of claim 1, further comprising:

following the removing, etching the first film using an additional ALE process.

13. The method of claim 12, wherein the ALE process includes alternating gaseous exposures of a first reactant and a second reactant, and the additional ALE process includes alternating gaseous exposures of the first reactant and a third reactant that is different than the second reactant.

14. The method of claim 13, wherein the ALE process and the additional ALE process are performed without plasma excitation of the first reactant, the second reactant, and the third reactant.

15. The method of claim 13, wherein the first film includes a ZrO_2 film, the second film includes an Al_2O_3 film, the first reactant includes HF, the second reactant includes $Al(CH_3)_3$, and the third reactant includes $Al(CH_3)_2Cl$.

16. A substrate processing method, comprising:

providing a substrate containing a first film on a substrate and a second film on the first film;

initiating etching of the second film using a thermal atomic layer etching (ALE) process that selectively etches the second film relative to the first film;

removing the second film using the ALE process, wherein the etching self-terminates at an interface of the second film and the first film; and

following the removing, etching the first film using an additional ALE process, wherein the ALE process includes alternating gaseous exposures of a first reactant and a second reactant, and the additional ALE process includes alternating gaseous exposures of the first reactant and a third reactant that is different than the second reactant, and wherein the ALE process and the additional ALE process are performed without plasma excitation of the first reactant, the second reactant, and the third reactant.

17. A substrate processing method, comprising:

depositing a ZrO_2 film on a substrate;

depositing a Al_2O_3 film on the ZrO_2 film;

initiating etching of the Al_2O_3 film using a thermal atomic layer etching (ALE) process that selectively etches the Al_2O_3 film relative to the ZrO_2 film; and

removing the Al_2O_3 film using the thermal ALE process, wherein the etching self-terminates at an interface of the Al_2O_3 film and the ZrO_2 film.

18. The method of claim 17, wherein the thermal ALE process includes alternating gaseous exposures of HF and $Al(CH_3)_3$.

19. The method of claim 17, wherein ZrO_2 film has a uniform thickness of approximately one monolayer.

20. The method of claim 17, further comprising:

following the removing, etching the ZrO_2 film using an additional thermal ALE process that includes alternating gaseous exposures of HF and $Al(CH_3)_2Cl$.

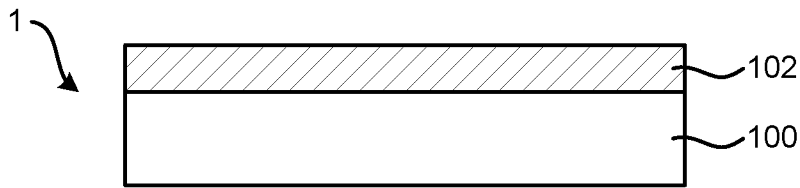


FIG. 1A

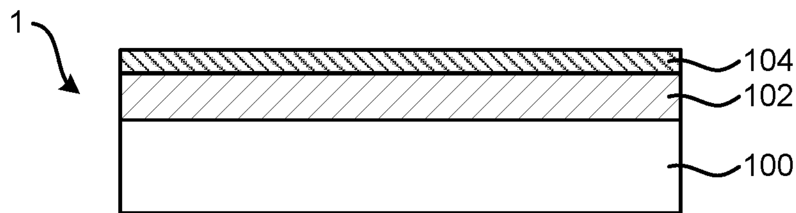


FIG. 1B

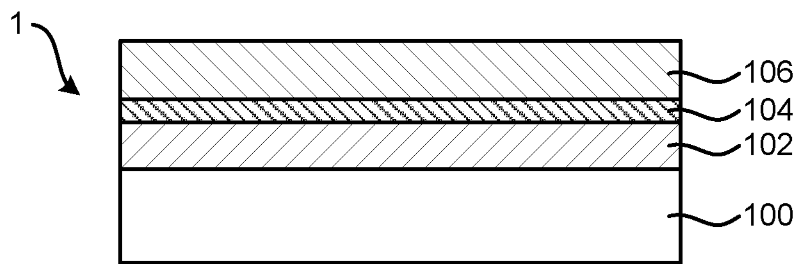


FIG. 1C

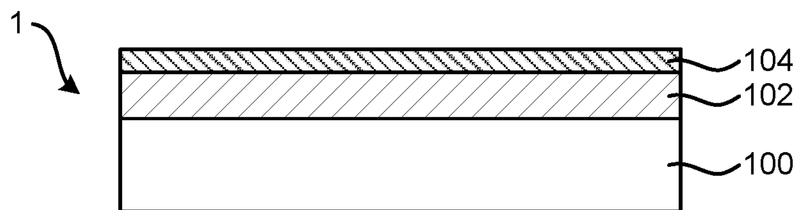


FIG. 1D

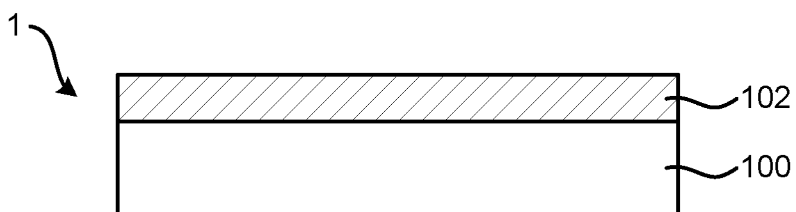


FIG. 1E

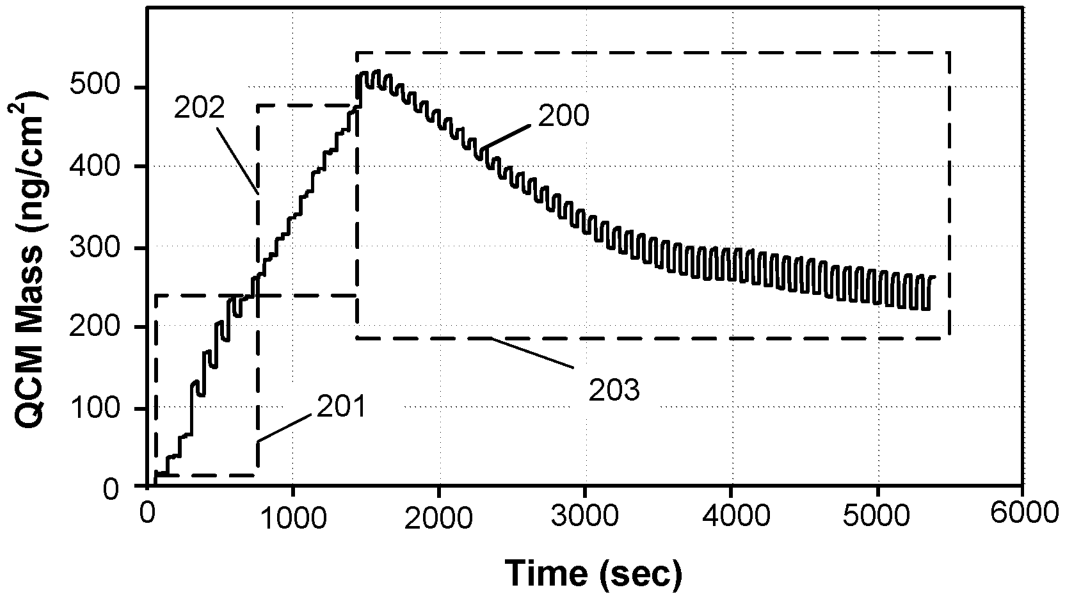


FIG. 2

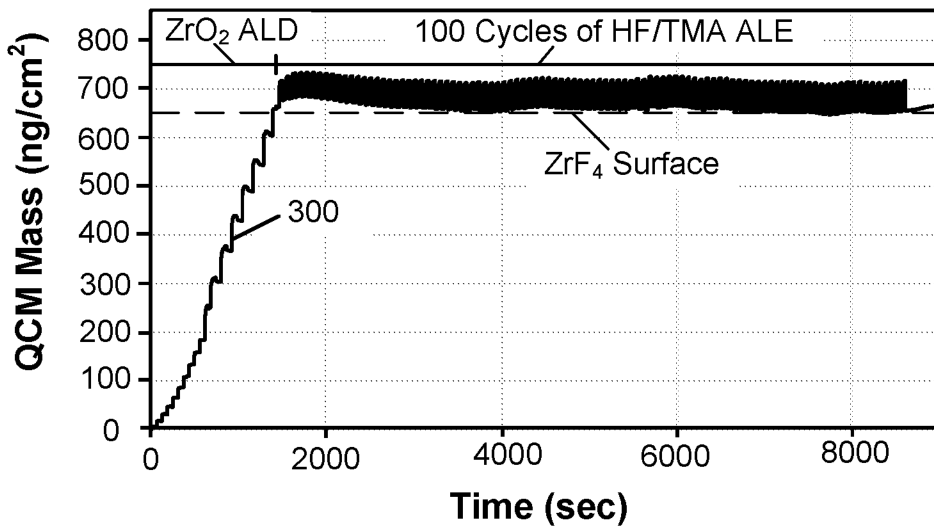


FIG. 3

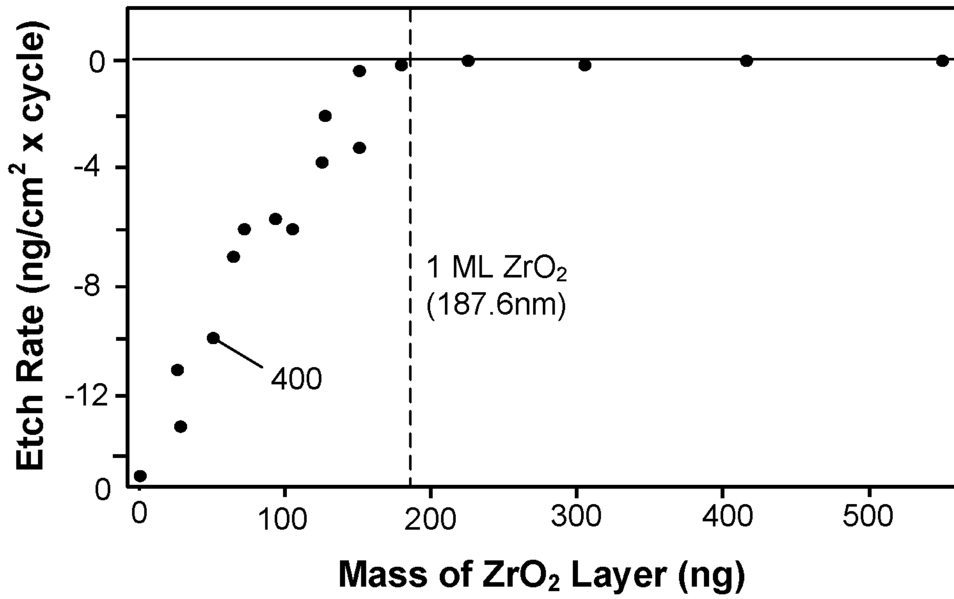


FIG. 4

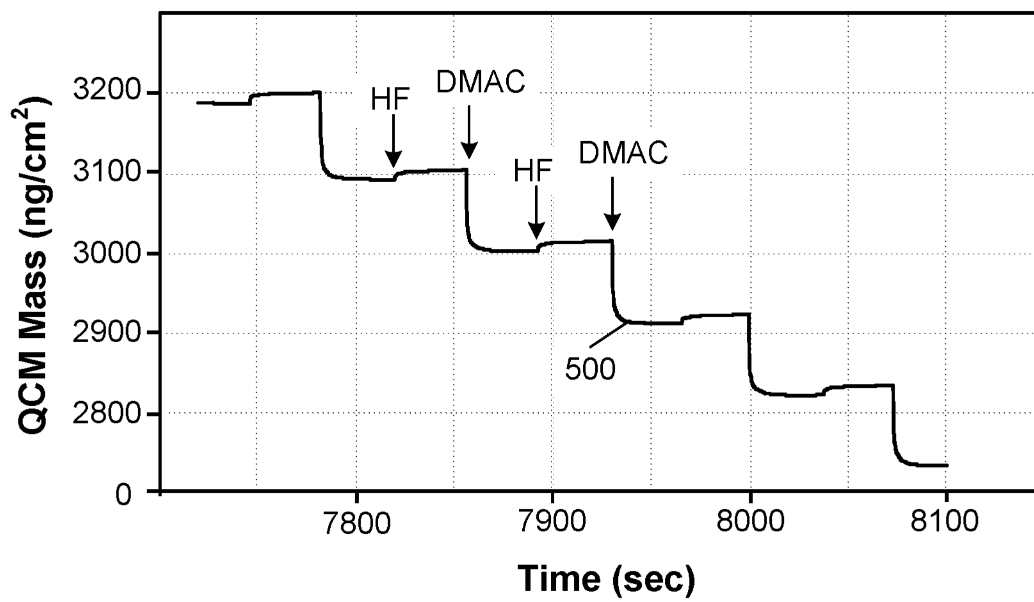


FIG. 5

Etch Reactants	Materials Etched	Materials Not Etched
HF/Sn(acac)$_2$	Al_2O_3, ZrO_2, HfO_2	
HF/Al(CH$_3$)$_3$	Al_2O_3, HfO_2	ZrO_2
HF/Al(CH$_3$)$_2$Cl	Al_2O_3, ZrO_2, HfO_2	
HF/$SiCl_4$	ZrO_2, HfO_2	Al_2O_3
HF/$TiCl_4$	ZrO_2, HfO_2	Al_2O_3

FIG. 6

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2021/016076

A. CLASSIFICATION OF SUBJECT MATTER

H01L 21/311(2006.01)i; **H01L 21/02**(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/311(2006.01); H01L 21/02(2006.01); H01L 21/033(2006.01); H01L 21/3065(2006.01); H01L 21/3105(2006.01); H01L 21/3213(2006.01)

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models
Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: etch stop, atomic layer etching, ZrO2, Al2O3

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2019-190453 A1 (INTEL CORPORATION et al.) 03 October 2019 (2019-10-03) page 3, line 33 - page 6, line 34, claims 1-7 and figures 1-2	1-20
A	US 9793135 B1 (ASM IP HOLDING B.V. et al.) 17 October 2017 (2017-10-17) column 10, line 60 - column 11, line 37 and figure 3	1-20
A	US 10283353 B2 (ASM IP HOLDING B.V. et al.) 07 May 2019 (2019-05-07) figure 7	1-20
A	US 2018-0240667 A1 (LAM RESEARCH CORPORATION) 23 August 2018 (2018-08-23) paragraphs [0045]-[0048]	1-20
A	KR 10-2018-0067667 A (L`AIR LIQUIDE, SOCIETE ANONYME POUR L`ETUDE ET L'EXPLOITATION DES PROCEDES GEORGES CLAUDE) 20 June 2018 (2018-06-20) the entire document	1-20

☐ Further documents are listed in the continuation of Box C.   ☑ See patent family annex.

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- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
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Date of the actual completion of the international search	Date of mailing of the international search report
21 May 2021	**21 May 2021**

Name and mailing address of the ISA/KR

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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/US2021/016076

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