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- (54) **Title:** METHOD FOR DEPOSITING A THIN FILM ELECTRODE AND THIN FILM STACK

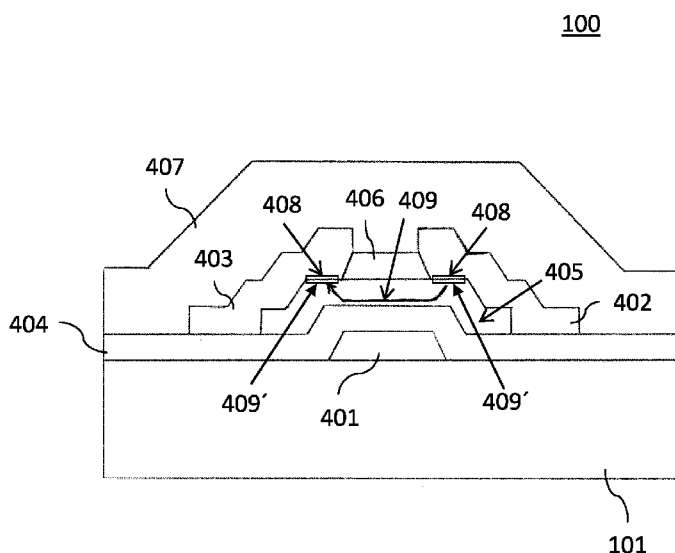


FIG. 3

(57) **Abstract:** A method for depositing at least one thin-film electrode (402, 403) onto a transparent conductive oxide film (405) is provided. At first, the transparent conductive oxide film (405) is deposited onto a substrate (101) to be processed. Then, the substrate (101) and the transparent conductive oxide film (405) are subjected to a processing environment containing a processing gas (207) acting as a donor material or an acceptor material with respect to the transparent conductive oxide film (405). The at least one thin-film electrode (402, 403) is deposited onto at least portions of the transparent conductive oxide film (405). A partial pressure of the processing gas (207) acting as the donor material or the acceptor material with respect to the transparent conductive oxide film (405) is varied while depositing the at least one thin-film electrode (402, 403) onto at least portions of the transparent conductive oxide film (405). Thus, a modified transparent conductive oxide film (410) having reduced interface resistance (408) and bulk resistance (409') can be obtained.

METHOD FOR DEPOSITING A THIN FILM ELECTRODE AND THIN FILM STACK

TECHNICAL FIELD OF THE INVENTION

[0001] Embodiments of the present invention relate to a sputter deposition system for carrying out process steps for manufacturing transparent oxide thin-film transistors. In particular, embodiments of the present invention relate to a deposition apparatus adapted for depositing a thin-film electrode onto a transparent conductive oxide film suited for thin-film transistors. Furthermore, embodiments of the present invention relate to a thin-film stack adapted for use in a thin-film transistor. Moreover, embodiments of the present invention relate to a method for depositing a thin-film electrode onto a transparent conductive oxide film.

BACKGROUND OF THE INVENTION

[0002] Thin-film transistors (TFT) are widely used, e.g., as switching elements in display devices. Thin-film transistors may thus control a gray level in display technology and may drive a variety of display devices. In a conventional display device, a semiconductor element for driving each pixel is formed of a thin-film transistor having at least one active layer. Thin-film stacks for use in TFTs may include transparent conductive oxide (TCO) films that are deposited onto substrates. TCO films have wide applications, as these TCO films may be used for electronic devices such as liquid crystal displays, touch panel devices, photovoltaic cells, and transparent thin-film transistors. As thin-film transistors are of increasing importance in many electronic devices, it is an issue to improve their electric properties and device performances.

SUMMARY OF THE INVENTION

[0003] In light of the above, a method for depositing a thin-film electrode onto a transparent conductive oxide film in accordance with independent claim 1 is provided. Furthermore, a

deposition apparatus adapted for depositing a thin-film electrode onto a transparent conductive oxide film in accordance with independent claim 7 is provided. In addition to that, a thin-film stack adapted for use in a thin-film transistor in accordance with independent claim 10 is provided.

[0004] According to one embodiment, a method for depositing a thin film electrode onto a transparent conductive oxide film is provided, the method including depositing the transparent conductive oxide film onto a substrate; subjecting the substrate and the transparent conductive oxide film to a processing environment containing a processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film; and depositing the thin film electrode onto at least portions of the transparent conductive oxide film, wherein a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film is varied while depositing the thin film electrode onto at least portions of the transparent conductive oxide film.

[0005] According to a further embodiment, a deposition apparatus adapted for depositing a thin film electrode onto a transparent conductive oxide film is provided, the deposition apparatus including a substrate carrier adapted for holding a substrate to be processed, a sputtering device adapted for sputtering the transparent conductive oxide film onto the substrate, and for sputtering at least one thin film electrode onto at least portions of the transparent conductive oxide film, and a gas flow controller adapted for controlling a composition of a processing environment of the substrate and the transparent conductive oxide film, the processing environment containing a processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film, wherein a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film is varied while depositing the thin film electrode onto at least portions of the transparent conductive oxide film.

[0006] According to yet a further embodiment, a thin film stack adapted for use in a thin film transistor is provided, the thin film stack including a substrate, a transparent conductive oxide film deposited onto the substrate, a precursor layer deposited onto the transparent conductive oxide film, and a thin film electrode deposited onto at least portions of the transparent conductive oxide film.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] So that the manner in which the above recited features of the present invention can be understood in detail, a more particular description of the invention, briefly summarized above, may be had by reference to embodiments. The accompanying drawings relate to embodiments of the invention and are described in the following:

- Figure 1 is a schematic block diagram of a sputter deposition system in accordance with a typical embodiment;
- Figure 2 is a side sectional view of a thin-film stack adapted for use in a thin-film transistor, according to another typical embodiment;
- Figure 3 is a side sectional view of a thin-film stack shown in Fig. 2, wherein the occurrence of interface and bulk resistances is shown;
- Figure 4 is a side sectional view of a thin-film stack for illustrating gas incorporation into a transparent conductive oxide film, according to yet another typical embodiment; and
- Figure 5 is a flowchart illustrating a method for depositing a thin-film electrode for use in a thin-film transistor onto a transparent conductive film.

BRIEF DESCRIPTION OF THE EMBODIMENTS

[0008] Reference will now be made in detail to the various embodiments of the invention, one or more examples of which are illustrated in the figures. Within the following description of the drawings, the same reference numbers refer to the same components. Generally, only the differences with respect to individual embodiments are described. Each example is provided by way of explanation of the invention and is not meant as a limitation of the invention. For example, features illustrated or described as part of one embodiment can be used on or in

conjunction with other embodiments to yield yet a further embodiment. It is intended that the present invention includes such modifications and variations.

[0009] Embodiments described herein refer inter alia to a method for depositing a thin-film electrode onto a transparent conductive film. The transparent conductive oxide film is deposited over a transparent substrate, such as a glass substrate, and is then subjected to a processing environment which contains a processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film. When a thin-film electrode is deposited onto at least portions of the transparent conductive oxide film, a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film may be varied. Thus, a thin-film stack is obtained which may be used in a thin-film transistor.

[0010] The thin-film stack may include a substrate, a transparent conductive oxide film deposited onto the substrate, a precursor layer deposited onto the transparent conductive oxide film and a thin-film electrode deposited onto at least portions of the transparent conductive oxide film. In order to manufacture the thin-film stack for use in a thin-film transistor, a deposition apparatus may be provided which is adapted for depositing the thin-film electrode onto the transparent conductive oxide film. The deposition apparatus includes a substrate carrier which is adapted for holding the substrate to be processed.

[0011] A sputtering device is adapted for sputtering the transparent conductive oxide film over the substrate and for sputtering at least one thin-film electrode onto at least portions of the transparent conductive oxide film. A gas flow controller is adapted for controlling a composition of a processing environment of the substrate and the transparent conductive oxide film. The processing environment may contain a processing gas which acts as a donor material or an acceptor material with respect to the transparent conductive oxide film.

[0012] A partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film may be varied while depositing the thin-film electrode onto at least portions of the transparent conductive oxide film. In order to provide good electrical properties of electronic thin-film devices, a reduced contact resistance between the transparent conductive oxide film and the at least one thin-film electrode must, therefore, be considered. In particular, a contact resistance between a semiconductor layer of the

thin-film resistor and the respective source/drain layer coated onto it may be reduced. Thin-film transistor devices may thus exhibit an improved efficiency.

[0013] Fig. 1 is a schematic block diagram of a sputter deposition system 200 in accordance with a typical embodiment. As shown in Fig. 1, the sputter deposition system 200 includes a sputtering reactor 205 where a variety of thin films may be deposited over a substrate 101, and a gas flow controller 300. In the sputtering reactor 205, a substrate carrier 102 is arranged which is adapted to hold the substrate 101 to be processed.

[0014] The sputtering reactor 205 may further include a heating device 103 which is arranged adjacent to the substrate carrier 102 in order to heat up the substrate carrier 102 and the substrate 101 to a desired temperature. A sputtering target 201 providing deposition material 204 for forming the thin films onto the substrate 101 is arranged in the interior of the sputtering reactor 205. Sputtering gas 202, which is introduced into the sputtering reactor 205 via a sputtering gas inlet 206, sputters deposition material 204 off the sputtering target 201.

[0015] The sputtering system included in the sputtering reactor 205 may be formed of, but is not restricted to, a DC sputtering system, a RF sputtering system, or a magnetron sputtering system. The sputtering reactor 205 is connected to a vacuum pump 203 which may provide a desired internal pressure in the sputtering reactor 205. The sputtering gas 202 and a donor gas or an acceptor gas 207 are introduced into the sputtering reactor 205 via the sputtering gas inlet 206.

[0016] The sputtering gas 202 and the donor gas or acceptor gas 207 are provided by a gas flow controller 300. The gas flow controller 300 includes, amongst other things, a sputtering gas source 303 and a gas source 304 providing the donor gas or the acceptor gas 207. The sputtering gas source 303 and the gas source 304 are each connected via pipes to a gas mixer 302 which provides a mixture of sputtering gas 202 and donor or acceptor gas 207. It is noted here that the content of donor gas or the acceptor gas 207 in the sputtering gas 202 may be varied.

[0017] A gas valve 301 is included in the gas flow controller 300 in order to provide a desired gas flow of sputtering gas 202 and donor gas or acceptor gas 207 into the sputtering reactor 205. The sputtering device is adapted for sputtering at least one transparent conductive oxide film (TCO film) above the substrate 101 and for sputtering at least one thin-film electrode (described

herein below with respect to Figs. 2 and 3) onto at least portions of the transparent conductive oxide film.

[0018] The gas flow controller 300 provides a control of a composition of a processing environment surrounding the substrate 101 and the transparent conductive oxide film in the interior of the sputtering reactor 205. The processing environment contains the donor gas or acceptor gas 207, which acts as a donor material or an acceptor material with respect to the transparent conductive oxide film.

[0019] The partial pressure of the processing gas which acts as the donor material or the acceptor material with respect to the transparent conductive oxide film, i.e., the donor gas or acceptor gas 207 may be varied with respect to the pressure of the sputtering gas 202, while the thin-film electrode is deposited onto at least portions of the transparent conductive oxide film (see also Figs. 2, 3, and 4). It is noted here that the partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film, i.e., the donor gas or the acceptor gas 207, may be expressed in terms of a flow ratio of gases which are introduced into the sputtering reactor 205. The flow ratio of the donor gas or the acceptor gas 207 with respect to the sputtering gas 202 may be varied in a range from 0.5 % to 60 %, and typically amounts to approximately 10 %.

[0020] It is noted here, albeit not described in detail, that at least one deposition parameter may be controlled during the thin-film deposition process. The at least one deposition parameter may be selected from the group consisting of power applied at the sputtering cathodes (sputtering targets 201), a pressure of the sputtering gas 202, a composition of the deposition gas mixture, a substrate temperature, a deposition time and any combination thereof.

[0021] The processing environment may include a gas composition which is selected from the group consisting of Ar, H₂, Ar/ H₂, NH₃, Ar/ NH₃, N₂, Ar/ N₂ and any combination thereof. The sputtering process may be provided by magnetron sputtering, RF-sputtering, or DC-sputtering and by reactive sputtering, or non-reactive sputtering. According to a typical embodiment which can be combined with other embodiments described herein, the sputtering deposition system 205 may be provided as a magnetron sputtering unit adapted for sputtering metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.

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[0022] Fig. 2 is a side sectional view of a thin-film stack 100 including a number of layers on a substrate 101 to be processed. The thin-film stack 100, as shown in Fig. 2, may be part of a thin-film transistor in an inverted-staggered structure. The substrate 101 may be provided as a glass substrate. Onto the substrate 101, a gate contact 401 is deposited. The gate contact 401 and the remaining portion of the substrate surface are coated by a gate dielectric layer 404. Onto at least portions of the gate dielectric layer 404, a transparent conductive oxide film 405 (TCO film, TCO layer) is deposited. The transparent conductive oxide film 405 may include an n-type material selected from the group consisting of ZnO, IGZO, ITO, In₂O₃, SnO₂, CdO, and any combination thereof or a p-type material selected from the group consisting of Cu₂O, CuAlO₂, Cu₂SrO₂, CuGaO₂, and any combination thereof.

[0023] The coexistence of electrical conductivity and optical transparency in these materials may further depend on nature, number, and atomic arrangements of metal cations in crystalline or amorphous oxide structures, on the resident morphology, and on the presence of intrinsic or intentionally introduced defects.

[0024] Thin-film electrodes, such as a drain contact 402 and a source contact 403 are deposited onto at least portions of the transparent conductive oxide film 405. Between the source contact 403 and the drain contact 402, an etch stop layer 406 is located. For protection of the entire thin-film structure, a passivation layer 407 is deposited onto the entire thin-film structure.

[0025] According to another typical embodiment, which can be combined with other embodiments described herein, a precursor layer may be deposited onto the transparent conductive oxide film prior to depositing the thin-film electrodes 403, 402. The precursor layer may include sputtered metals selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof. The thin-film electrodes, e.g., the drain contact 402 and the source contact 403 may include a material which is provided in form of sputtered metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.

[0026] Fig. 3 is another side sectional view of thin-film stack 100 according to a typical embodiment, wherein Fig. 3 exhibits the existence of the combination of a bulk resistance 409/409' and interface resistances 408. It is noted here, albeit not shown in the drawings, that the precursor layer is deposited prior to depositing the thin-film electrodes 403, 402, i.e., the source contact 403 and the drain contact 402.

[0027] According to a typical embodiment, which can be combined with other embodiments described herein, the precursor layer may have a thickness in a range from one atomic layer up to 100 nm, and typically has a thickness of approximately 10 nm.

[0028] The bulk resistance 409' below the drain/source electrodes indicated by two arrows in Fig. 3, may be reduced by providing a precursor layer which contains hydrogen. The hydrogen content may result in a carrier concentration of an n-type transparent conductive oxide film 405 of approximately 10^{19} atoms/cm³. Moreover, the bulk resistance 409' below the drain/source electrodes indicated by two arrows in Fig. 3, may be reduced by providing a precursor layer which contains nitrogen. The nitrogen content may result in a carrier concentration of a p-type transparent conductive oxide film 405 of approximately 10^{19} atoms/cm³. The entire resistance, e.g., the sum of the interface resistances 408 and the bulk resistance 409/409' is according to the following equation:

$$R_C = R_{\text{interface}} + R_{\text{bulk}} = R_{408} + R_{409} + R_{409'} \quad (1)$$

[0029] Fig. 4 is another side sectional view of a thin-film stack 100 according to yet a further embodiment which can be combined with other embodiments described herein. Fig. 4 shows the situation before depositing a thin-film electrode, e.g. the drain contact 402 and/or the source contact 403, onto the transparent conductive oxide film 405. Due to the combination of donor gas or the acceptor gas 207 and sputtering gas 202, which has been described herein above with respect to Fig. 1, at least a part of the donor gas or the acceptor gas 207 may be incorporated in a precursor layer prior to depositing the thin-film electrodes.

[0030] In Fig. 4, the substrate 101 to be processed is shown with layers of the gate contact 401, the gate dielectric layer 404 above the gate contact 401 and the remaining portions of the surface of the substrate 101 to be processed, a transparent conductive oxide film 410 which is modified by the incorporation of donor gas or acceptor gas 411 onto at least portions of the gate dielectric layer 404, and the etch stop layer 406 atop the modified transparent conductive oxide film 410. It is noted here that the portion of the transparent conductive oxide film 405 below the etch stop layer 406 is not modified by the donor gas or the acceptor gas 207.

[0031] It is noted here that the term "modified transparent conductive oxide film" indicates that a diffusion of donor gas or the acceptor gas 207 into the transparent conductive oxide film has

occurred such that the bulk resistance 409' is reduced. The donor gas or the acceptor gas 207 which is contained in the processing environment in the sputtering reactor 205 (see Fig. 1) may act as a donor material or an acceptor material with respect to the transparent conductive oxide film 410.

[0032] As a donor material, hydrogen may be provided. Hydrogen gas, which is included as a donor gas 207 in the sputtering gas 202 introduced into the sputtering reactor 205 by means of the sputtering gas inlet 206, in addition to decreasing the bulk resistance 409', also decreases the interface resistance 408 of the interfaces between the transparent conductive oxide film 410 and the thin-film electrodes 403, 404 (not shown in Fig. 4). Thus, hydrogen gas is added to the sputtering process in order to generate a first few atomic layers of source/drain material with hydrogen content, wherein the remaining thin-film electrode is deposited without hydrogen in the sputter process gas until a set point value for the layer thickness has been reached.

[0033] As described above, the transparent conductive oxide film 405 may include n-type material selected from the group consisting of ZnO, IGZO, ITO, In_2O_3 , SnO_2 , CdO , Cu_2O , and any combination thereof. As an alternative, the transparent conductive oxide film 405 may include p-type material selected from the group consisting of Cu_2O , CuAlO_2 , Cu_2SrO_2 , CuGaO_2 , and any combination thereof. Particularly for the example, where Cu_2O is used as a p-type TCO, which can be used as a TFT active layer material, the above described processes can be applied by utilizing an nitrogen containing processing gases as acceptor gases, such as N_2 , N_2O or combinations thereof. Nitrogen is an effective p-type dopant for Cu_2O . Thus, providing a precursor layer creating acceptor states in the undoped Cu_2O due to the nitrogen containing gas, can also improve the contact resistance.

[0034] Thus, the source/drain electrodes are not directly deposited onto the active channel island, but a precursor layer is provided between the transparent conductive oxide film 410 and the thin-film electrodes. Thus, hydrogen may act as a donor in transparent conductive oxide films 410 e.g. made from ZnO, IGZO, ITO, In_2O_3 , SnO_2 , CdO and any combination thereof, wherein a hydrogen incorporation during the sputtering process has an influence on the overall resistance according to equation (1) given herein above. Furthermore, nitrogen may act as an acceptor in transparent conductive oxide films 410 e.g. made from Cu_2O , CuAlO_2 , Cu_2SrO_2 , CuGaO_2 , and

any combination thereof, wherein a nitrogen incorporation during the sputtering process has an influence on the overall resistance according to equation (1) given herein above.

[0035] In this way, the overall resistance, i.e. the sum of the interface resistances 408 and the bulk resistance 409/409' may be reduced. Both the interface resistance 408 and the bulk resistance 409' may be reduced by sputtering metallization material in a hydrogen-containing atmosphere or in a nitrogen-containing atmosphere. The thin precursor layer may be deposited by static reactive magnetron sputtering of pure source/drain metal, such as Mo, Ti, MoTi, Al, Cu, or any combination thereof, wherein the deposition of the thin precursor layer is performed after a definition of the etch stop layer 406. An appropriate atmosphere for this process may be an Ar/H₂ or an Ar/NH₃ atmosphere for providing donor material with respect to n-type TCO films, or an N₂, Ar/N₂, Ar/N₂O atmosphere for providing acceptor material with respect to p-type TCO films. Thus, hydrogen or nitrogen present in the sputtering atmosphere may reduce the interface resistances 408 between the transparent conductive oxide film 410 and the source/drain metallization which is sputtered directly above the precursor film, e.g., in a pure argon atmosphere.

[0036] In addition to that, the in situ H₂ diffusion allows reduction of the bulk resistance 409'. The extent of a reduction of the overall resistance R_C according to equation (1) given above may be provided by controlling different deposition parameters of the precursor film, such as power at the sputtering cathodes, pressure of the sputtering gas, composition of the argon/hydrogen or argon/nitrogen mixture, deposition time, substrate temperature, etc.

[0037] Fig. 5 is a flowchart illustrating a method for depositing a thin-film electrode onto a transparent conductive oxide film. The process starts at block 501. Then, a transparent conductive oxide film 405, 410 is deposited onto a substrate 101 (block 502). In block 503, the substrate and the transparent conductive oxide film deposited onto the substrate 101 are subjected to a processing environment containing a processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film 405.

[0038] Then, the thin-film electrode, e.g., the source contact 403 and the drain contact 402 are deposited onto at least portions of the transparent conductive oxide film 405. During deposition of the thin-film electrodes, a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film may be varied, as

indicated by block 505. This variation of the partial pressure of the donor gas 207 is performed while depositing the thin-film electrodes 402, 403 onto at least portions of the transparent conductive oxide film 405. Thus, a modified transparent conductive oxide film 410 (see Fig. 4) is obtained which incorporates the donor material, e.g., hydrogen or the acceptor material, e.g. nitrogen. The procedure is ended at block 506.

In light of the above, a plurality of embodiments has been described. For example, according to one embodiment, a method for depositing a thin film electrode onto a transparent conductive oxide film is provided, the method including depositing the transparent conductive oxide film onto a substrate; subjecting the substrate and the transparent conductive oxide film to a processing environment containing a processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film; and depositing the thin film electrode onto at least portions of the transparent conductive oxide film, wherein a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film is varied while depositing the thin film electrode onto at least portions of the transparent conductive oxide film. The processing environment may include a gas composition which is selected from the group consisting of Ar, H₂, Ar/ H₂, NH₃, Ar/ NH₃, and any combination thereof or from the group consisting of: Ar, N₂, Ar/ N₂, and any combination thereof. According to yet further embodiments, which can be combined with any of the other embodiments and modifications above, the method may further include depositing a precursor layer onto the transparent conductive oxide film prior to depositing the thin film electrode. In addition to that, the precursor layer may be deposited by sputtering metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof. According to yet further additional or alternative modifications at least one deposition parameter is controlled during the thin film deposition process, wherein the at least one deposition parameter is selected from the group consisting of power applied at sputtering cathodes, pressure of sputtering gas, composition of deposition gas mixture, substrate temperature, and any combination thereof. According to an optional modification thereof, the partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive film is varied. A flow ratio of the donor gas with respect to the sputtering gas may be varied in a range from 0.5 % to 60 %, and typically amounts to approximately 10 %. According to another embodiment a deposition apparatus adapted for depositing a thin film electrode onto a transparent

conductive oxide film is provided, the deposition apparatus including a substrate carrier adapted for holding a substrate to be processed, a sputtering device adapted for sputtering the transparent conductive oxide film onto the substrate and for sputtering at least one thin film electrode onto at least portions of the transparent conductive oxide film, and a gas flow controller adapted for controlling a composition of a processing environment of the substrate and the transparent conductive oxide film, the processing environment containing a processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film, wherein a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film is varied while depositing the thin film electrode onto at least portions of the transparent conductive oxide film. According to yet further additional or alternative modifications the deposition apparatus may include a heating device adapted for heating the substrate to be processed. According to yet further embodiments, which can be combined with any of the other embodiments and modifications above, the sputtering device is provided as a magnetron sputtering unit adapted for sputtering metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof. According to yet another embodiment, a thin film stack adapted for use in a thin film transistor is provided, the thin film stack including a substrate, a transparent conductive oxide film deposited onto the substrate, a precursor layer deposited onto the transparent conductive oxide film, and a thin film electrode deposited onto at least portions of the transparent conductive oxide film. According to yet further embodiments, which can be combined with any of the other embodiments and modifications above, the transparent conductive oxide film may include an n-type material selected from the group consisting of ZnO, IGZO, ITO, In_2O_3 , SnO_2 , CdO, and any combination thereof or a p-type material selected from the group consisting of Cu_2O , CuAlO_2 , Cu_2SrO_2 , CuGaO_2 , and any combination thereof. According to an optional modification thereof, the precursor layer includes sputtered metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof. Furthermore, the precursor layer may have thickness which is in a range from one atomic layer to 100 nm, and typically has a thickness of approximately 10 nm. According to yet further embodiments, which can be combined with any of the other embodiments and modifications above, a carrier concentration of one portion of the transparent conductive oxide film is in a range from 10^{17} atoms/cm³ to 10^{20} atoms/cm³ and typically amounts to approximately 10^{19} atoms/cm³. A carrier concentration of another portion of the transparent conductive oxide film can be in a range from 10^{16} atoms/cm³ to 10^{19} atoms/cm³ and typically amounts to approximately 10^{18}

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atoms/cm³. According to yet further additional or alternative modifications, the thin film electrode may include sputtered metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.

[0039] While the foregoing is directed to embodiments of the invention, other and further embodiments of the invention may be devised without departing from the basic scope thereof, and the scope thereof is determined by the claims that follow.

CLAIMS

1. A method for depositing a thin film electrode onto a transparent conductive oxide film, the method comprising:

depositing the transparent conductive oxide film onto a substrate;

subjecting the substrate and the transparent conductive oxide film to a processing environment containing a sputtering gas and a processing gas, the processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film; and

depositing the thin film electrode onto at least portions of the transparent conductive oxide film, wherein

a partial pressure of the processing gas acting as the donor material or the acceptor material with respect to the transparent conductive oxide film is varied with respect to the pressure of the sputtering gas while depositing the thin film electrode onto at least portions of the transparent conductive oxide film.
2. The method in accordance with claim 1, wherein the processing environment comprises a gas composition which is selected from the group consisting of Ar, H₂, Ar/H₂, NH₃, Ar/NH₃, and any combination thereof or wherein the processing environment comprises a gas composition which is selected from the group consisting of: Ar, N₂, Ar/ N₂, Ar/N₂O and any combination thereof
3. The method in accordance with claim 1 or 2, further comprising depositing a precursor layer onto the transparent conductive oxide film prior to depositing the thin film electrode.

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4. The method in accordance with claim 3, wherein the precursor layer is deposited by sputtering a metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.
5. The method in accordance with any one of the preceding claims, wherein at least one deposition parameter is controlled during the thin film deposition process, wherein the at least one deposition parameter is selected from the group consisting of power applied at sputtering cathodes, pressure of sputtering gas, composition of deposition gas mixture, substrate temperature, and any combination thereof.
6. The method in accordance with any one of the preceding claims, wherein a flow ratio of the processing gas acting as the donor material or the acceptor material with respect to the sputtering gas is varied in a range from 0.5 % to 60 %, and typically amounts to approximately 10 %.
7. A deposition apparatus adapted for depositing a thin film electrode onto a transparent conductive oxide film, the deposition apparatus comprising:
 - a substrate carrier adapted for holding a substrate to be processed;
 - a sputtering device adapted for sputtering the transparent conductive oxide film onto the substrate and for sputtering at least one thin film electrode onto at least portions of the transparent conductive oxide film; and
 - a gas flow controller having a gas mixer and being adapted for providing a desired gas flow of sputtering gas and donor gas or acceptor gas into the sputtering device and for controlling a composition of a processing environment of the substrate and the transparent conductive oxide film, the processing environment containing the sputtering gas and a processing gas, the processing gas acting as a donor material or an acceptor material with respect to the transparent conductive oxide film, wherein

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the gas flow controller is further adapted for varying a partial pressure of the processing gas acting as the donor material or an acceptor material with respect to the transparent conductive oxide film, with respect to the pressure of the sputtering gas while depositing the thin film electrode onto at least portions of the transparent conductive oxide film.

8. The deposition apparatus in accordance with claim 7, further comprising a heating device adapted for heating the substrate to be processed.
9. The deposition apparatus in accordance with claim 7 or 8, wherein the sputtering device is provided as a magnetron sputtering unit adapted for sputtering metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.
10. A thin film stack adapted for use in a thin film transistor, the thin film stack comprising:
 - a substrate;
 - a transparent conductive oxide film deposited onto the substrate;
 - a precursor layer which contains a donor material or an acceptor material with respect to the transparent conductive oxide film and which is deposited onto the transparent conductive oxide film; and
 - a thin film electrode deposited onto at least portions of the transparent conductive oxide film.
11. The thin film stack in accordance with claim 10, wherein the transparent conductive oxide film comprises an n-type material selected from the group consisting of ZnO, IGZO, ITO, In₂O₃, SnO₂, CdO, and any combination thereof or a p-type material selected from the group consisting of Cu₂O, CuAlO₂, Cu₂SrO₂, CuGaO₂, and any combination thereof.

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12. The thin film stack in accordance with claim 10 or 11, wherein the precursor layer comprises sputtered metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.
13. The thin film stack in accordance with any one of the claims 10 to 12, wherein the precursor layer has a thickness which is in a range from one atomic layer to 100 nm, and typically has a thickness of approximately 10 nm.
14. The thin film stack in accordance with any one of the claims 10 to 13, wherein a carrier concentration of one portion of the transparent conductive oxide film is in a range from 10^{18} atoms/cm³ to 10^{20} atoms/cm³ and typically amounts to approximately 10^{19} atoms/cm³.
15. The thin film stack in accordance with any one of the claims 10 to 14, wherein the thin film electrode comprises sputtered metal selected from the group consisting of Mo, Ti, MoTi, Al, Cu, and any combination thereof.

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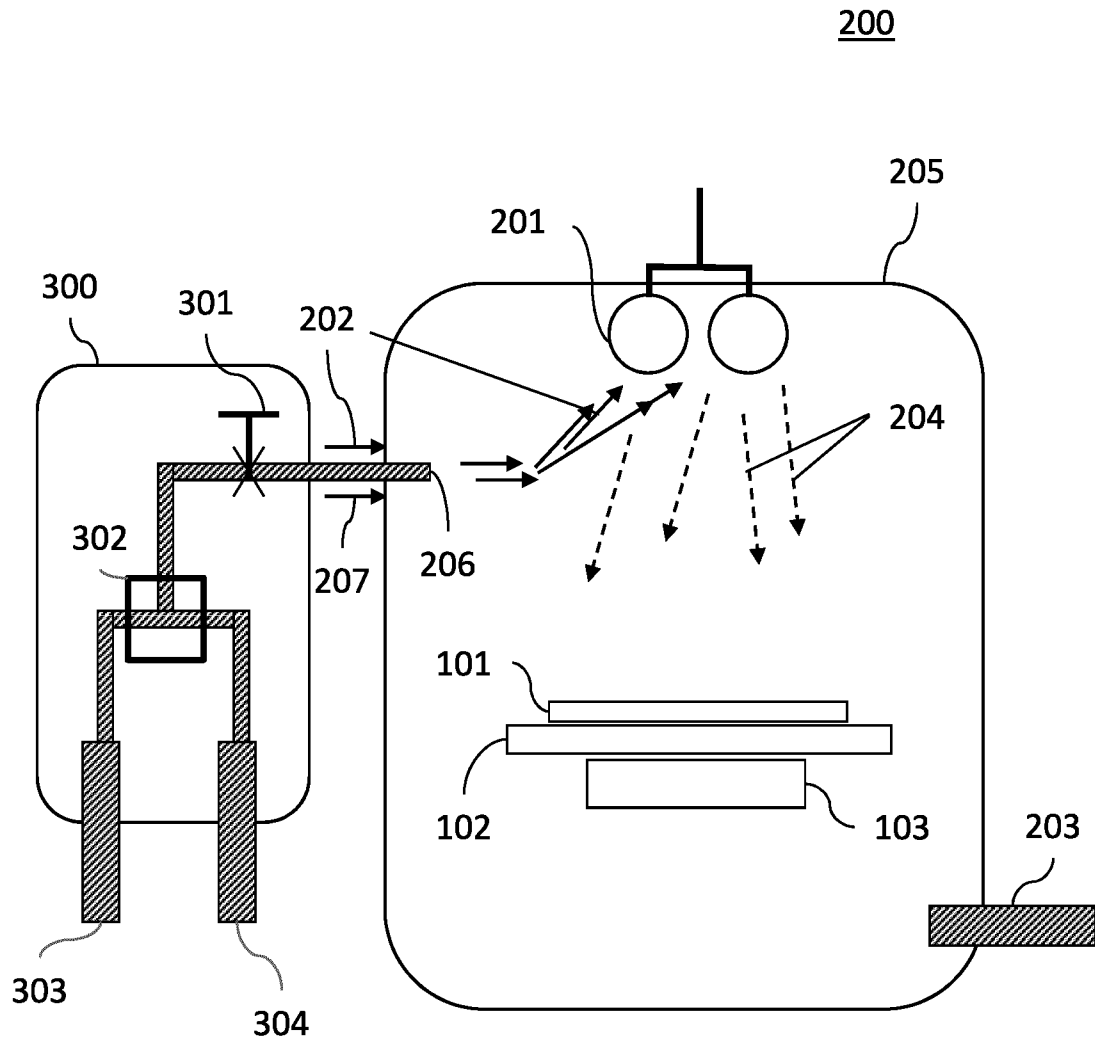


FIG. 1

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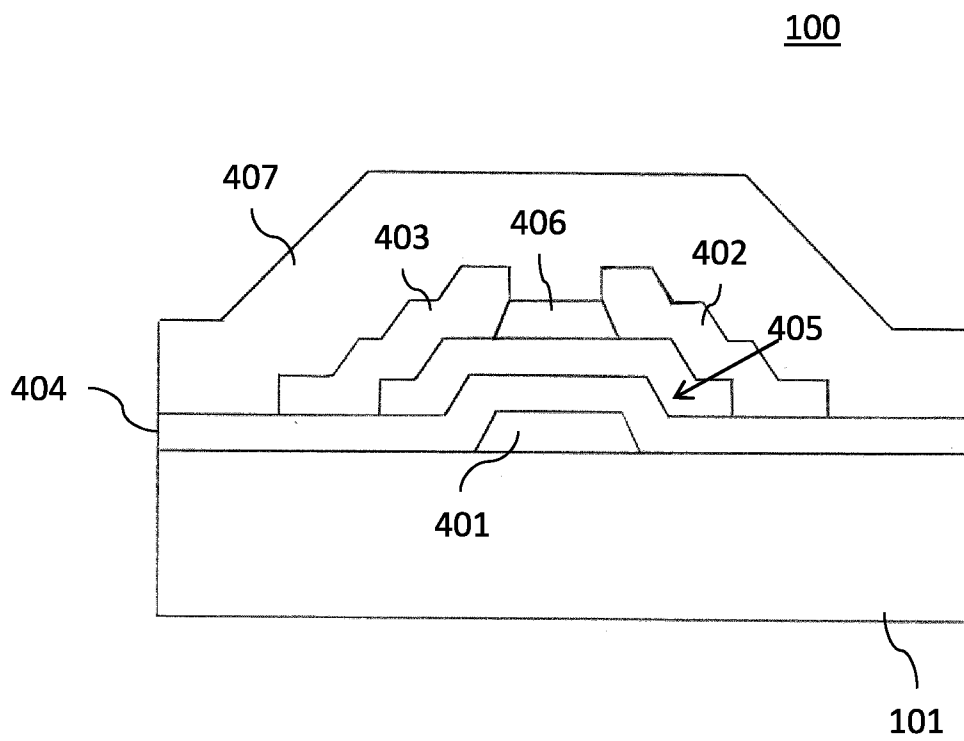


FIG. 2

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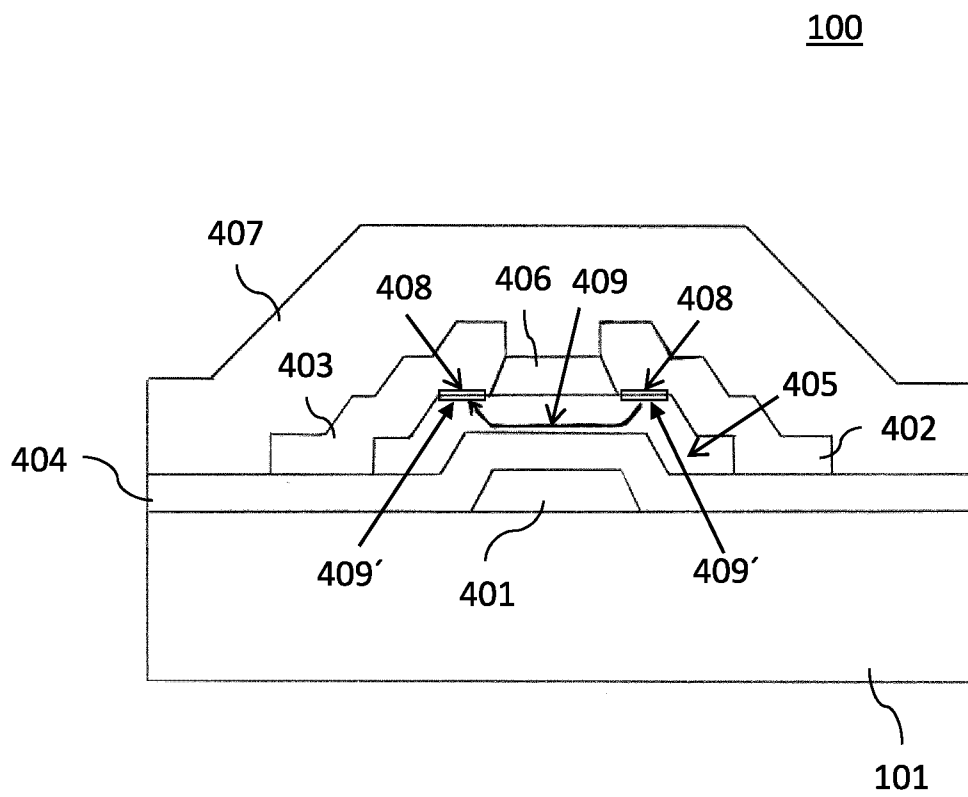


FIG. 3

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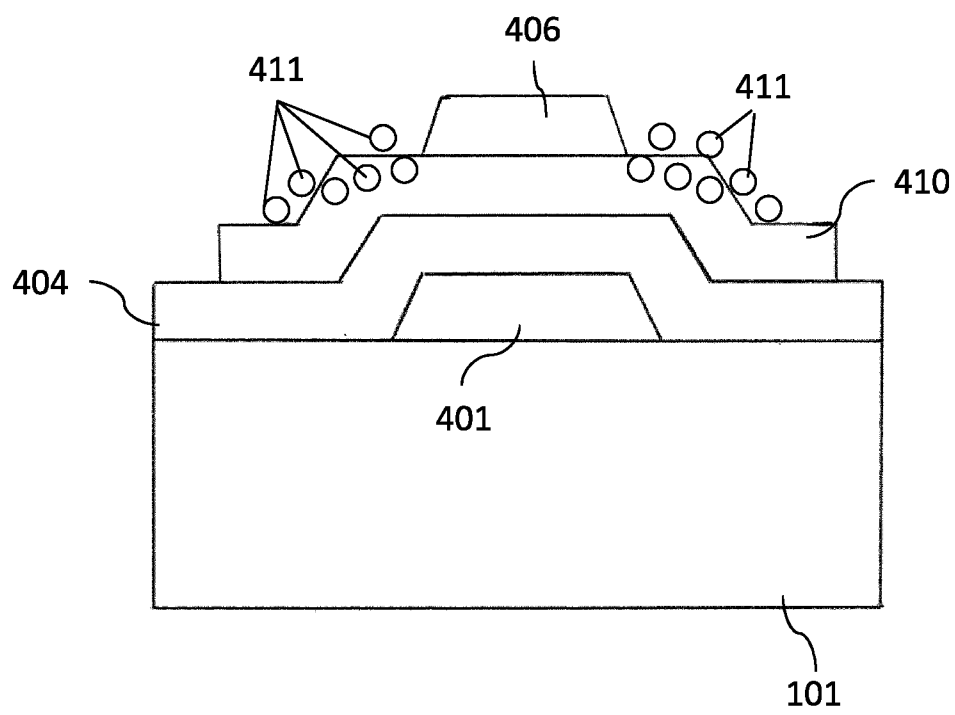


FIG. 4

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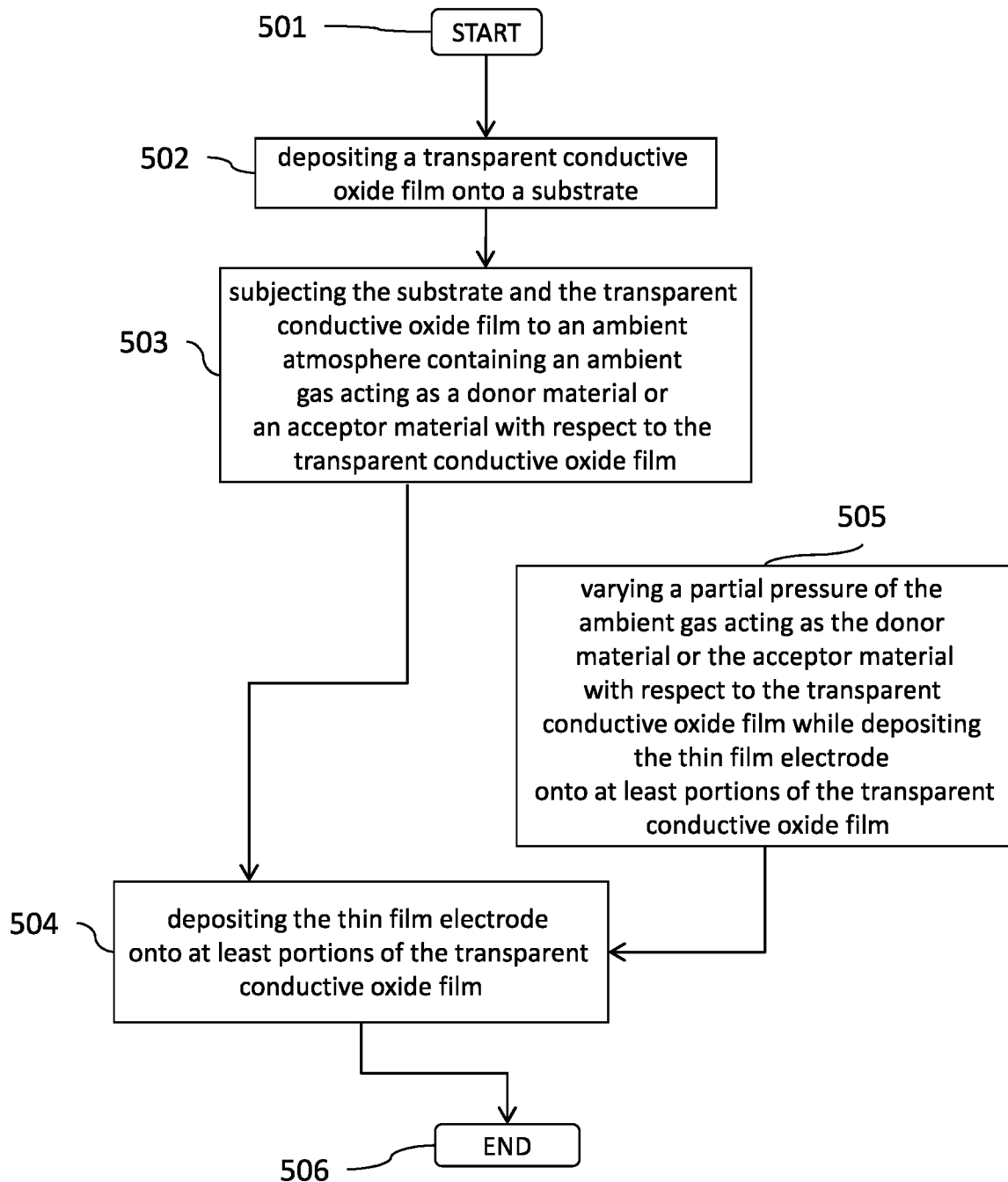


FIG. 5

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2011/068191

A. CLASSIFICATION OF SUBJECT MATTER

INV. H01L29/45 H01L29/786
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2010/244031 A1 (AKIMOTO KENGO [JP] ET AL) 30 September 2010 (2010-09-30) paragraphs [0094] - [0098], [0132], [0133], [0140]; figure 3A -----	1-6, 10-15
X	US 2009/065771 A1 (IWASAKI TATSUYA [JP] ET AL) 12 March 2009 (2009-03-12) paragraphs [0067], [0081], [0087], [0110], [0111]; claims 1,2; figure 4D ----- -/--	7-9



Further documents are listed in the continuation of Box C.



See patent family annex.

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Date of the actual completion of the international search

10 November 2011

Date of mailing of the international search report

18/11/2011

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Juhl, Andreas

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2011/068191

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>WOO-SEOK CHEONG ET AL: "Metal-Doped Oxide Electrodes for Transparent Thin-Film Transistors Fabricated by Direct Co-Sputtering Method", JAPANESE JOURNAL OF APPLIED PHYSICS, JAPAN SOCIETY OF APPLIED PHYSICS, JP, vol. 48, no. 4, 1 April 2009 (2009-04-01), pages 4C089-1, XP001554509, ISSN: 0021-4922, DOI: DOI:10.1143/JJAP.48.04C089 [retrieved on 2009-04-20] the whole document</p> <p>-----</p>	1-15

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2011/068191

Patent document cited in search report	Publication date	Patent family member(s)	Publication date	
US 2010244031	A1	30-09-2010	CN 101859798 A	13-10-2010
			JP 2010258423 A	11-11-2010
			KR 20100109395 A	08-10-2010
			TW 201104864 A	01-02-2011
			US 2010244031 A1	30-09-2010

US 2009065771	A1	12-03-2009	AT 527693 T	15-10-2011
			BR PI0709583 A2	19-07-2011
			CN 101401213 A	01-04-2009
			EP 1984954 A1	29-10-2008
			JP 2007250983 A	27-09-2007
			KR 20080114802 A	31-12-2008
			RU 2008141166 A	27-04-2010
			US 2009065771 A1	12-03-2009
			US 2011256684 A1	20-10-2011
			WO 2007119386 A1	25-10-2007
