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(54) **Title:** METHODS FOR CONTROLLING HOST MEMORY ACCESS WITH MEMORY DEVICES AND SYSTEMS

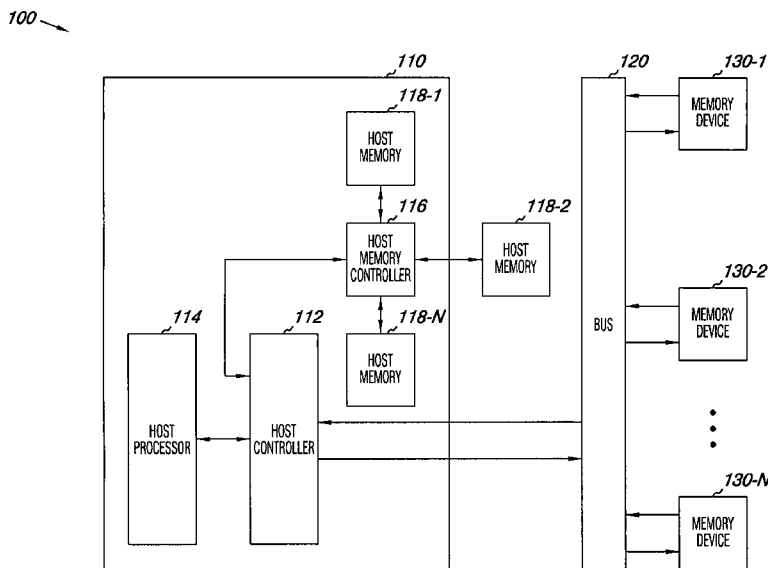


Fig. 1

(57) **Abstract:** The present disclosure includes methods for controlling host memory access with a memory device, systems, host controllers and memory devices. One embodiment for controlling host memory access with a memory device includes receiving at least one command from a host and controlling execution of the at least one command with the memory device.

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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2010/001521**A. CLASSIFICATION OF SUBJECT MATTER***G06F 13/16(2006.01)i, G06F 13/38(2006.01)i, G06F 12/00(2006.01)i*

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G06F 13/16; G06F 13/14

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models
Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: host, memory, header, command

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y A	EP 1782217 B1 (TREK 2000 INTERNATIONAL LTD.) 17 December 2008 See abstract; the whole claims	1, 17, 24, 35 2-3, 6-7, 10, 16-20 , 22-24, 28-29, 35-36 4-5, 8-9, 11-15, 21 , 25-27, 30-34, 37-39
Y A	US 2004-0122988 A1 (JONG SEOK HAN et al.) 24 June 2004 See abstract; the whole claims	2-3, 6-7, 10, 16-20 , 22-24, 28-29, 35-36 4-5, 8-9, 11-15, 21 , 25-27, 30-34, 37-39

 Further documents are listed in the continuation of Box C. See patent family annex.

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Information on patent family members

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