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SPRUSON & FERGUSON

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PATENT REQUEST: STANDARD PATENT

I/We, the Applicant(s)/Nominated Person(s) specified below, request I/We be granted a patent for the invention disclosed in the accompanying standard complete specification.

[70,71] Applicant(s)/Nominated Person(s):

Sony Corporation, of 7-35, Kitashinagawa 6-chome, Shinagawa-ku, Tokyo, JAPAN

[54] Invention Title:

Method and Apparatus for Recording and Reproducing Digital Video Signals

[72] Inventor(s):

Yuka Oikawa, Naofumi Yanagihara and Nobuaki Izumi

[74] Address for service in Australia:

Spruson & Ferguson, Patent Attorneys
Level 33 St Martins Tower
31 Market Street
Sydney New South Wales Australia (Code SF)

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By:

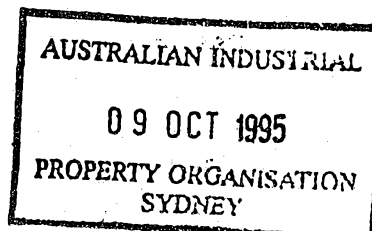
Registered Patent Attorney

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NOTICE OF ENTITLEMENT

I, Fraser Patison Old, of Spruson & Ferguson, St Martins Tower, 31 Market Street, Sydney, New South Wales 2000, Australia, being the patent attorney for the Applicant(s)/Nominated Person(s) in respect of Application No 30233/95 state the following:-

The Applicant(s)/Nominated Person(s) has/have entitlement from the actual inventor(s) as follows:-

The Applicant(s)/Nominated Person(s), is/are the assignee(s) of the actual inventor(s).

The Applicant(s)/Nominated Person(s) is/are the applicant(s) of the basic application(s) listed on the Patent Request. The basic application(s) listed on the Patent Request is/are the first application(s) made in a Convention Country in respect of the invention.

DATED this SIXTH day of OCTOBER 1995

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METHOD AND APPARATUS FOR RECORDING AND REPRODUCING DIGITAL VIDEO SIGNALS

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(71) Applicant(s)
SONY CORPORATION

(72) Inventor(s)
YUKA OIKAWA; NAOFUMI YANAGIHARA; NOBUAKI IZUMI

(74) Attorney or Agent
SPRUSON & FERGUSON, GPO Box 3898, SYDNEY NSW 2001

(56) Prior Art Documents
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(57) Claim

1. A method for reproducing digital video signals recorded on a recording medium by an inclined azimuth recording system, wherein the improvement resides in that

during varying speed reproduction, varying speed reproduction is carried out by setting a varying playback speed S so that $S = a(t \cdot m \pm 1)/n$

where n is the number of heads, a is a constant of 1 or 2, t is the number of tracks on which one-frame data of the digital video signals is recorded, and m is a constant which is an integer other than 0.

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COMPLETE SPECIFICATION

FOR A STANDARD PATENT

ORIGINAL

Name and Address
of Applicant:

Sony Corporation
7-35, Kitashinagawa 6-chome
Shinagawa-ku
Tokyo
JAPAN

Actual Inventor(s): Yuka Kiyama, Naofumi Yanagihara and Nobuaki Izumi

Address for Service:

Spruson & Ferguson, Patent Attorneys
Level 33 St Martins Tower, 31 Market Street
Sydney, New South Wales, 2000, Australia

Invention Title:

Method and Apparatus for Recording and Reproducing
Digital Video Signals

The following statement is a full description of this invention, including the best method of performing it known to me/us:-

TITLE OF THE INVENTION

Method and Apparatus for Recording and Reproducing Digital Video Signals

Cross-References to Related Patents

As applications pertinent to the present application, JP
5 Patent Applications Nos. 05-056576 (filing date, 1993.02.22), 05-
195533 (filing date, 1993.07.13), 05-287702 (filing date,
1993.10.22) and 06-142042 (filing date, 1994.06.23) have been
proposed by the present Assignee. US applications corresponding
to these application are now pending. Each of the above
10 applications is owned by the present Assignee and is hereby
incorporated by reference.

BACKGROUND OF THE INVENTION

1. Field of the Invention

This invention relates to a method and apparatus for
15 reproducing a recording medium having digital video signals
recorded thereon by an inclined azimuth recording system. More
particularly, it relates to a method and apparatus for
reproducing digital video signals by varying speed reproduction.

2. Descriptio of the Related Art

20 Recently, developments of a digital video tape recorder
(digital VTR) in which signals digitized from high-definition
analog picture signals or high definition digital picture signals
are encoded (compressed) using a high efficiency encoding system,
24 such as discrete cosine transform (DCT), and the resulting
encoded data is recorded on or reproduced from a magnetic tape

(video tape) by a rotary head, are proceeding briskly.

Such digital VTR employs the so-called inclined azimuth recording system in which the gap directions of two rotary heads recording and/or reproducing signals on neighboring tracks are 5 inclined relative to each other. The digital VTR records/reproduces one frame of encoded data on or from plural tracks on the video tape.

For repeatedly reproducing a given track of the above-described inclined azimuth recording system, an output is derived 10 only from the rotary head whose angle of inclination of the gap direction (azimuth angle) coincides with the direction of magnetization of the track. That is, since no output is derived from the rotary head whose azimuth angle is not coincident with the direction of magnetization of the track, signals of two 15 fields are alternately reproduced by having tracks with the coincident directions of magnetization traced by the two rotary heads by controlling the tape feed stop position.

There is also known such a digital VTR in which third and fourth rotary heads having azimuth angles opposite to those of 20 the above-mentioned two heads, referred to herein as first and second rotary heads, are mounted in the vicinity of these first and second heads, so that the tracks whose directions of magnetization are not coincident with the azimuth angles of the first and second heads are reproduced by the third and fourth 25 heads for producing perpetually stable playback signals.

For carrying out varying speed reproduction for the above-described digital VTR, as distinct from the normal speed reproduction in which the heads are caused to trace track patterns sequentially for reproducing track data recorded on the 5 video tape, the heads are caused to run obliquely relative to the patterns for reproducing track data recorded on the video tape.

That is, for varying speed reproduction, the time to tape displacement, which is 1:1 for normal speed reproduction, is set to 1:3 and 3:1 for triple-speed reproduction and for 1/3 slow reproduction, respectively.

For reproducing data by varying speed reproduction, more specifically, for triple-speed reproduction of a video tape, having one-frame data recorded on 20 tracks, namely tracks $A_1, B_1, A_2, B_2, A_3, B_3, \dots, A_{10}, B_{10}$, using double azimuth heads A and B, as shown in Fig.1, when the head A is moved from the track A_1 to track A_2 and the head B is moved from the track B_1 to track B_2 , for reproducing the first frame F1, data of regions D_{A1} and D_{A2} of the tracks A_1 and A_2 , whose directions of magnetization are coincident with the azimuth angles of the head A, and data of regions D_{B1} and D_{B2} of the tracks B_1 and B_2 , whose directions of magnetization are coincident with the azimuth angles of the head B, are reproduced.

As for the second and third frames F2 and F3, the heads A and B trace the tracks in the same way as those of the first frame F1. One-frame data is reproduced by the heads tracing the

tracks of the frames F1, F2 and F3 20 times.

The one-frame varying speed reproduced picture, obtained as described above, is an aggregation of data D_{F1} picked up from the first frame F1, data D_{F2} picked up from the second frame F2 and 5 data D_{F3} picked up from the third frame F3, as shown in Fig.2. These first to third frames are shown in Fig.1, and data D_{F1} , D_{F2} and D_{F3} , picked up from each of these frames, are referred to herein as macro-block data. That is, one picture is constituted by data of fractions of a number of different frames.

10 Thus the picture becomes ill-looking and unnatural depending upon the state of data mixing of fractions of plural different frames. That is, since neighboring macro-blocks cannot be constituted by data of temporally neighboring frames, a number of boundary lines due to temporal difference of the neighboring 15 macro-blocks appear on the varying speed reproduced picture.

SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide a method and apparatus for reproducing digital video signals in which lines of discontinuation caused by temporal difference of neighboring macro-blocks during varying speed reproduction may be rendered less obtrusive for improving the quality of the varying speed reproduced picture.

In one aspect, the present invention provides a method for reproducing digital video signals recorded on a recording medium by an inclined azimuth recording system, wherein, during varying

speed reproduction, varying speed reproduction is carried out by setting a varying playback speed S so that $S = a(t \cdot m \pm 1)/n$, where n is the number of heads, a is a constant of 1 or 2, t is the number of tracks on which one-frame data of the digital video signals is recorded, and m is a constant which is an integer other than 0.

The digital video signals read out with the varying playback speed are allocated and displayed at positions on a picture associated with the digital video signals.

10 In another aspect, the present invention provides an apparatus for reproducing digital video signals in which digital video signals recorded by a rotary head unit having n heads in accordance with an inclined azimuth recording system so that one frame data of the digital video signals are recorded on n tracks
15 of the rotary head unit are read out and in which a picture corresponding to the digital video signals thus read out is displayed on a screen by picture display processing means. The apparatus includes driving control means for achieving varying speed reproduction of the digital data during varying speed reproduction with a varying playback speed $S = a(t \cdot m \pm 1)/n$, where
20 a is a constant equal to 1 or 2 and m is a constant which is an integer other than 0.

25 A picture display processor allocates and displays digital video signals read out with the varying playback speed at positions on a picture associated with the digital video signals.

With the method and apparatus for reproducing the digital video signals, a varying playback speed S for varying speed reproduction is set so that $S = a(t \cdot m \pm 1)/n$ where n is the number of heads, a is a constant of 1 or 2, t is the number of 5 tracks on which one-frame data of the digital video signals is recorded, and m is a constant which is an integer other than 0. The digital video signals recorded by an inclined azimuth recording system are varying speed reproduced at the varying playback speed S . The picture display processor displays a 10 picture corresponding to the digital video signals read out at the varying speed playback speed. In this manner, non-continuous lines caused by temporal difference in neighboring macro-blocks during varying speed reproduction may be rendered less obtrusive for improving the picture quality of the varying speed reproduced 15 picture.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig.1 illustrates head tracing on a video tape during varying speed reproduction and picked-up data in the conventional method for reproducing digital video signals.

20 Fig.2 illustrates positions on the picture of data picked up during varying speed reproduction in accordance with the method shown in Fig.1.

Fig.3 is a block diagram showing an arrangement of an digital video signal reproducing apparatus for carrying out the 25 digital video signal reproducing method according to a first

embodiment of the present invention.

Fig.4 illustrates the constitution of two pairs of double azimuth rotary heads of a rotary head unit employed in the reproducing apparatus shown in Fig.3.

5 Fig.5 is a block diagram showing an arrangement of an encoder of the recording system of the reproducing apparatus shown in Fig.3.

Fig.6 illustrates positions of data recorded on the video tape by the above recording system.

10 Fig.7 shows the relation between the data positions on the picture and data positions on the video tape.

Fig.8 illustrates head tracing for a case in which continuous data can be picked up by the same head, with the varying reproducing speed being 5.25.

15 Fig.9 illustrates data picked up by first 20 head tracings for a case in which continuous data can be picked up by the same head of the rotary head unit, with the varying reproducing speed being 5.25.

Fig.10 illustrates data picked up by the next 20 head tracings for a case in which continuous data can be picked up by the same head of the rotary head unit, with the varying reproducing speed being 5.25.

Fig.11 shows an arrangement of two head pairs displaced by 180° from each other.

25 Fig.12 illustrates head tracing for a case in which

continuous data can be picked up by the opposing heads of the rotary head unit, with the varying reproducing speed being 10.5.

Fig.13 illustrates data picked up by first 20 head tracings for a case in which continuous data can be picked up by the 5 opposing heads of the rotary head unit, with the varying reproducing speed being 10.5.

Fig.14 illustrates data picked up by the next 20 head tracings for a case in which continuous data can be picked up by the opposing heads of the rotary head unit, with the varying reproducing speed being 10.5.

Fig.15 illustrates the constitution of four pairs of double azimuth rotary heads employed as a rotary head unit in a reproducing apparatus for carrying out the digital video signal reproducing method according to a second embodiment of the present invention.

Fig.16 illustrates head tracing for a case in which continuous data can be picked up by the same head of the rotary head unit, with the varying reproducing speed being 2.625.

Fig.17 illustrates data picked up by first 20 head tracings for a case in which continuous data can be picked up by the same head, with the varying reproducing speed being 2.625.

Fig.18 illustrates data picked up by the next 20 head tracings for a case in which continuous data can be picked up by the same head, with the varying reproducing speed being 2.625.

Fig.19 illustrates head tracing for a case in which

continuous data can be picked up by the opposing heads of the rotary head unit, with the varying reproducing speed being 5.25.

Fig.20 illustrates data picked up by first 20 head tracings for a case in which continuous data can be picked up by the 5 opposing heads of the rotary head unit, with the varying reproducing speed being 5.25.

Fig.21 illustrates data picked up by the next 20 head tracings for a case in which continuous data can be picked up by the opposing heads of the rotary head unit, with the varying 10 reproducing speed being 5.25.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring to the drawings, preferred embodiments of the present invention will be explained in detail.

First, the method for reproducing digital video signals 15 according to a first embodiment of the present invention will be explained.

The method for reproducing digital video signals is carried out by a digital video tape recorder (digital VTR) having a reproducing apparatus for digital video signals constructed as shown in Fig.3.

The reproducing apparatus includes a rotary head unit 1, made up of two pairs of double azimuth heads, a playback amplifier 2 and a signal processor 4 for decoding encoded data reproduced from a magnetic tape (video tape) 3. The reproducing 25 apparatus also includes an entry unit 5 for setting e.g., a

varying speed reproducing mode, and a system controller 6 for controlling the digital VTR in its entirety in accordance with the mode as set by the entry unit 5. The reproducing apparatus also includes a driving controller 7 for running the video tape 5 3 at an optimum multiple speed in association with the head constitution during the varying speed reproducing mode under control by the system controller 6, and a picture display processor 8 for allocating data read out from the video tape 3 with the above optimum multiple speed to associated positions in 10 the picture for displaying the picture.

The rotary head unit 1 is comprised of two pairs of double azimuth heads, that is a first double azimuth head pair, made up of a head HA1 of an azimuth A and a head HB1 of an azimuth B, and a second double azimuth head pair, made up of a head HA2 of the 15 azimuth A and a head HB2 of the azimuth B, as shown in Fig.3. The head unit 1 is configured so that continuous data will be picked up by the same heads.

On the video tape 3, digital video signals are recorded by the inclined azimuth recording system.

Specifically, the recording system of the digital VTR has 20 an encoder shown in Fig.5. The digital video signals are recorded on the video tape 3 by the recording system in accordance with the inclined azimuth system.

That is, the encoder includes an orthogonal transform unit 25 10 for processing blocked picture data with discrete cosine

transform (DCT), a picture memory 11 and an activity detection circuit 12 for detecting the activity classes. The encoder also includes a quantization unit 13 for selecting a quantizer per five macro-blocks for carrying out quantization, and a variable length encoding circuit 14 for variable length encoding a quantization output.

The orthogonal transform unit 10 includes a blocking circuit 101 for dividing picture data into blocks each made up of an 8x8 array of pixels, and a shuffling circuit 102 for shuffling five macro-blocks. The orthogonal transform unit also includes a DCT circuit 103 and a motion detection circuit 104 for effecting motion detection on the macro-block basis.

The quantization unit 13 includes a Huffman table 131, which is a table for variable length encoding, and a data quantity estimation circuit 132 for calculating the post-quantization data quantity. The quantization unit also includes a quantization step selection circuit 133 for selecting the quantization step and a quantizer 134 for effecting quantization with the selected quantization step.

The video signals, converted into digital signals, are supplied to the above-described encoder.

The blocking circuit 101 divides input digital video signals into blocks each made up of an array of 8x8 pixels, and routes picture data of the respective blocks to the shuffling circuit 102.

The shuffling unit 102 collects five of the macro-blocks which are separated from one another on the picture to form a video segment which is supplied to the DCT circuit 103 and the motion detection circuit 104.

5 The motion detection circuit 104 detects the motion of the video segment from the shuffling circuit 102 and routes the detection information as to the 8 pixel by 8 pixel intra-frame mode or the 4 pixel by 4 pixel by 2 inter-field mode to the DCT circuit 103, while outputting the same information as the
10 subsidiary information to the reproducing apparatus shown in Fig.1.

The DCT circuit 103 performs two-dimensional DCT, with the block size of 8 pixels by 8 pixels, in terms of five macro-blocks, shuffled on the picture by the shuffling circuit 102, as
15 a unit. The DCT circuit 104 is responsive to the detection information from the motion detection circuit 104 to select and switch between intra-frame pixel values in the array of 8 pixels by 8 pixels and the inter-field prediction error values in the array of 4 pixels by 8 pixels by 2 in order to effect two-dimensional DCT. The DCT circuit 103 routes dc components of the
20 DCT coefficients to a dc component quantizer, not shown, while routing ac components of the DCT coefficients to the picture memory 11 and to the activity detection circuit 12. The dc component quantizer performs simple linear quantization on the dc components.

The picture memory 11 temporarily stores the ac components supplied thereto from the DCT circuit 103.

The activity detection circuit 12 detects the maximum value of the ac components from the DCT circuit 103 and classifies the
5 ac components depending upon the maximum value. The classification information is supplied to a data quantity estimation circuit 132, while being supplied as the subsidiary information to the reproducing apparatus shown in Fig.1.

The ac components temporarily stored in the picture memory
10 11 are outputted to the quantizer 134.

The data quantity estimation circuit 132 calculates a post-quantization data quantity based upon the classification information from the activity detection circuit 12 and the codes of the Huffman table 131. The results of calculation are
15 outputted to the quantization step selection circuit 133.

The quantization step selection circuit 133 selects the quantization step, depending upon the results of calculation from the data quantity estimating circuit 132, and outputs the number of the selected quantization step to the quantizer 134, while
20 outputting the same quantization step number as the subsidiary information to the reproducing apparatus shown in Fig.1.

The quantizer 134 quantizes the ac components from the picture memory 11 at the quantization step selected by the quantization step selection circuit 133, and outputs the
25 quantized ac components to the variable length encoding circuit

14.

The variable length encoding circuit 14 variable length encodes the ac components quantized by the quantizer 134, such as by Huffmann encoding, and outputs the variable length encoded ac components.

The dc components and the ac components, quantized and variable length encoded as described above, are deshuffled by a deshuffling circuit, not shown, so as to be re-arrayed in a sequence in which the macro-blocks become continuous on the picture. The re-arrayed macro-blocks are recorded at pre-set positions on the video tape 3 shown in Fig.3.

That is, since the digital picture data are processed in terms of five macroblocks, shuffled on the picture, as a unit, and deshuffled before being recorded on the video tape 3, data A, B, C, ...S, T on the video tape 3 shown in Fig.6 may be readily associated with data A, B, C, ...S, T on the picture. Therefore, when recording one-frame data on e.g., 20 tracks, the 20-track data thus recorded are associated with 20 transversely divided portions on the picture.

The motion detection information, activity detection information and the quantization step number, outputted from the recording system having the above-described encoder, are supplied to the signal processor 4 via the system controller 6 shown in Fig.1.

The signal processor 104 decodes and inverse discrete cosine

transforms the encoded data recorded by the recording system on the video tape 3, that is variable-length codes, in accordance with the motion detection information, activity detection information and the quantization step number from the recording system, for expanding the video signals, recorded in the compressed state, into the original base-band video signals.

The entry unit 5 sets one of the normal playback mode and the varying speed playback mode and outputs the set mode as a mode setting signal.

The system controller 6 control the entire system so that so that the operation will take place under the set mode.

For example, during the varying speed playback mode, the driving control unit 7 is controlled so that, by utilizing he tracking signals, such as automatic tracking control (ATF) signals or the like tracking signals, the phase information will be additionally employed in the speed control of the video tape 3. Thus the tracing of the rotary head 1 is perpetually maintained at the same relative position with the tracks on the video tape 3.

The drive control unit 7 is made up of a speed setting circuit 71 for setting the speed of the video tape 3 responsive to the mode under control of the system controller 6 and a driving circuit 72 for running the video tape 3 at a speed as set by the speed setting circuit 71 and for driving the rotary head.

During varying speed reproduction, the speed setting circuit

71 sets an optimum multiple speed of the video tape 3 depending upon the number heads of the rotary head unit 1. The optimum multiple speed is referred to herein as a varying playback speed S.

5 That is, the varying playback speed S is set to

$$S = a(t \cdot m \pm 1)/n \quad (1)$$

wherein n denotes the number of heads, a is a constant equal to 1 or 2, t is a number of tracks required for recording one-frame picture data and m is a constant which is an integer other than 10 0.

In the present embodiment, in which the number of heads n is 4, the number of tracks t is 20, the varying playback speed S is given by

$$\begin{aligned} S &= (20m \pm 1)/4 \\ 15 \quad &= a(5m \pm 0.25) \end{aligned} \quad (2)$$

where the plus sign "+" and the minus sign "-" \pm in the equation (i) correspond to the normal and reverse directions respectively.

If, in the above equation (2), the running direction is the normal direction, and the constants a and m are $a = 1$ and $m = 1$, that is if the varying playback speed $S = 5.25$, the driving circuit 72 runs the video tape 3 at the varying playback speed $S = 5.25$ as set by the speed setting circuit 71.

The data picked up by the tracing of the rotary head 1 is now explained.

25 During the tracing from 1-0 to 1-5 of the first rotary head,

the tracing 1-0 and the tracing 1-1 for the first frame F1 pick up data of an area a_1 of a track A_1 , data of an area a_2 of a track B_1 , data of an area a_3 of a track A_2 , data of an area a_4 of a track B_2 , data of an area a_5 of a track A_3 and data of an area a_6 of a track B_3 , for the first frame F1, as shown in Fig.8.

The tracing 1-2 and the tracing 1-3 for the second frame F2 pick up data of an area b_1 of the track A_2 , data of an area b_2 of the track B_2 , data of an area b_3 of the track A_3 and data of an area b_4 of the track B_3 , for the second frame F2.

On the other hand, the tracing 1-4 and the tracing 1-5 for the third frame F3 pick up data of an area c_1 of the track A_2 , data of an area c_2 of the track B_2 , data of an area c_3 of the track A_3 , data of an area c_4 of the track B_3 , data of an area c_5 of a track A_4 and data of an area c_6 of a track B_4 , for the third frame F3.

The data of the respective frames, picked up by the respective tracings, are continuous data. For example, data of the area b_1 of the second frame F2, picked up by the tracing 1-2, are continuous to data of the area a_3 of the first frame F1 picked up by the first tracing 1-0, while data of the area c_1 of the third frame F3, picked up by the tracing 1-4, are continuous to data of the area b_1 .

As for the tracing from 2-0 to 2-5 of the second head of the rotary head unit 1, similarly to the tracing of the first head, the picked-up data become continuous.

The data thus picked up from the respective frames are supplied to the picture display processor 8 via the signal processor 4.

The picture display processor 8 allocates the data picked up from the video tape 3 run by the driving controller 7 at the varying playback speed S to associated positions on the picture in order to display the picture.

For example, as shown in Fig.9, the picture display processor 8 displays a fractional picture corresponding to data of the area c_1 in a picture area 3 which is an area located at a position associated with the data of the area c_1 , while displaying a fractional picture corresponding to data of the area b_1 in a picture area 2 which is an area located at a position associated with the data of the area b_1 . Similarly, the picture display processor 8 displays a fractional picture corresponding to data of the area a_3 in a picture area 1 which is an area located at a position associated with the data of the area a_3 .

That is, the picture area 1 formed by data picked up from the first frame F_1 , the picture area 2 formed by data picked up from the second frame F_2 and the picture area 3 formed by data picked up from the third frame F_3 represent continuous fractional pictures.

If the first frame F_1 , second frame F_2 and the third frame F_3 are traced 20 times to produce one-frame data, and another one-frame data is produced by the next 20 times of tracing, the

fractional picture 8 formed by data picked up from the eighth frame, the fractional picture 9 formed by data picked up from the ninth frame and the fractional picture 10 formed by data picked up from the tenth frame become continuous fractional pictures, 5 by display processing by the picture display processor 8, as shown in Fig.8. The fractional pictures 8, 9 and 10 correspond to macro-blocks. That is, the neighboring macro-blocks are made up of data of temporally proximate frames.

The operation of the digital VTR having the reproducing 10 system for digital video signals as described above is now explained.

The normal playback mode or the varying speed playback mode is set at the entry unit 5, which then sends a corresponding mode setting signal to the system controller 6.

15 The system controller 6 is responsive to the mode setting signal from the entry unit 5 for controlling the operation of the entire system, such as the signal processor 4, driving controller 7 or the picture display processor 8.

The speed setting circuit 71 of the driving controller 7 20 operates, under control by the system controller 6, for setting the running speed of the video tape 3, driven by the driving circuit 72, to a varying playback speed S during the varying playback mode so that

$$S = a(t \cdot m \pm 1)/n$$

25 for the number of heads equal to n , the constant a equal to 1 or

2, the number of tracks required for recording one-frame picture t and the constant m equal to an integer other than zero.

The driving circuit 72 runs the video tape 3 at the varying playback speed S as set by the speed setting circuit 71.

5 The encoded data, recorded on the video tape 3, are read by the rotary head 1 so as to be supplied via the playback amplifier 2 to the signal processor 4.

The signal processor 4 demodulates data, read out by the rotary head 1, in accordance with a demodulating system 10 associated with the modulating system of the recording side of the digital VTR, and routes the demodulated data to the picture display processor 8.

The picture display processor 8 allocates the data demodulated by the signal processor 4 to the positions on the 15 picture which are associated with the demodulated data for displaying the picture.

By setting the running speed of the video tape 3 at the speed setting circuit 71 to the varying playback speed S shown by the equation (1) for varying speed reproduction so that the 20 neighboring macro-blocks will be constituted by data of temporally proximate frames, the boundary lines between neighboring macro-blocks become less noticeable so that a spontaneous varying speed playback picture is obtained.

Although two pairs of double azimuth heads shown in Fig.4 25 are used in the above-described embodiment, it is also possible

to use two diametrically opposing double azimuth head pairs, made up of A azimuth heads HA3, HA4 arranged in diametrically opposite positions and B azimuth heads HB3 and HB4 similarly arranged at diametrically opposite positions, as shown in Fig.11.

5 Although the rotary head unit 1 is so configured that continuous data will be picked up by the same head, it may also be configured so that continuous data will be picked up by the diametrically opposing heads.

10 More specifically, if, with the above equation (2), the running direction is the normal direction and the constants \underline{a} and \underline{m} are such that $a = 2$ and $m = 1$, that is the varying playback speed $S = 10.5$, the driving circuit 72 runs the video tape 3 at the varying playback speed $S = 10.5$ as set by the speed setting circuit 71.

15 In such case, as for the tracing from 1-0 to 1-3 of the first head of the rotary head unit 1, the tracing 1-0 and the tracing 1-1 for the first frame F1 pick up data of an area d_1 of the track A_1 , data of an area d_2 of the track B_1 , data of an area d_3 of the track A_2 , data of an area d_4 of the track B_2 , data of an area d_5 of the track A_3 , data of an area d_6 of the track B_3 , data of an area d_7 of the track A_4 , data of an area d_8 of the track B_4 , data of an area d_9 of a track A_5 and data of an area d_{10} of a track B_5 . As for the tracing 1-2 and the tracing 1-3 for the third frame F3, data of the areas f_1 to f_{10} are picked up in a similar manner.

25

As for the tracings 2-0 and 2-1 of the second head of the rotary head unit 1, data of the areas e_1 to e_{10} are picked up in a similar manner.

In this case, as in the case shown in Fig.8 in which 5 continuous data may be picked up by the same heads, data of the area e_3 of the second frame F2, picked up by the tracing 2-0, are continuous to data of the area d_5 of the first frame F1 picked up by the tracing 2-0, while data of the area f_3 of the third frame F3, picked up by the tracing 1-2, are continuous to data of the 10 area e_3 .

By display processing by the picture display processor 8, the fractional picture corresponding to data of the area f_3 is displayed in a picture area 3 positioned in association with the data of the area f_3 , as shown in Fig.13. Similarly, the 15 fractional picture corresponding to data of the area e_3 is displayed in a picture area 2 positioned in association with the data of the area e_3 , while the fractional picture corresponding to data of the area d_5 is displayed in a picture area 1 positioned in association with the data of the area d_5 . In this 20 manner, the above picture data are allocated to the picture positions associated with the picked up data for displaying the fractional pictures.

Thus, in this case, the picture area 1 formed by the data picked up from the first frame F1, the picture area 2 formed by the data picked up from the second frame F2 and the picture area 25

3 formed by the data picked up from the third frame F3 become continuous picture areas.

If the first frame F1, second frame F2 and the third frame F3 are traced 20 times to produce one-frame data, and another
5 one-frame data is produced by the next 20 times of tracing, the fractional picture 20 formed by data picked up from the 20th frame, the fractional picture 19 formed by data picked up from the 19th frame and the fractional picture 18 formed by data picked up from the 18th frame, ... become continuous fractional
10 pictures, by display processing by the picture display processor 8, as shown in Fig.14. That is, the neighboring macro-blocks are made up of data of temporally proximate frames.

The method for reproducing digital video signals according to the second embodiment of the present invention is explained.

15 The above-described method for reproducing digital video signals according to the first embodiment is carried out by the digital VTR having the playback system for digital video signals shown in Fig.3, in which the rotary head unit is made up of two pairs of double azimuth heads, with the number of heads being
20 four. The digital video signal reproducing method of the present second embodiment is carried out by the above-described digital VTR employing four pairs of the double azimuth heads, with the number of the heads being eight.

25 The parts specifying the same function as those of the first embodiment are depicted by the same numerals and the

corresponding description is omitted for simplicity.

Referring to Fig.15, the rotary head unit 1 is constituted by double azimuth heads (referred to herein as first heads), made up of an A azimuth head HA5 and a B azimuth head HB5, double
5 azimuth heads (referred to herein as second heads), made up of an A azimuth head HA6 and a B azimuth head HB6, double azimuth heads (referred to herein as third heads), made up of an A azimuth head HA7 and a B azimuth head HB7, and double azimuth heads (referred to herein as fourth heads), made up of an A
10 azimuth head HA8 and a B azimuth head HB8. The first heads and the second heads are arranged at diametrically opposite positions to each other, while the third heads and the fourth heads are similarly arranged at diametrically opposite positions to each other.

15 In this case, the running speed of the video tape 3 is set by the speed setting circuit 71 shown in Fig.3 to a varying playback speed S such that

$$\begin{aligned} S &= (20m \pm 1)/8 \\ &= a(2.5m \pm 0.125) \end{aligned} \quad (3)$$

20 If assumed that, with the rotary head unit 1, continuous data can be picked up by the same heads, the running direction is the normal direction and $a = 1$ and $m = 1$, that is the varying playback speed $S = 2.625$, the driving circuit 72 runs the video
25 tape 3 with the varying playback speed $S = 2.625$ as set by the speed setting unit 71.

Thus, as for the tracing from 1-0 to 1-5 of the first head of the rotary head unit 1, the tracing 1-0 and the tracing 1-1 for the first frame F1 pick up data of an area g_1 of track A_1 , data of an area g_2 of track B_1 , data of an area g_3 of track A_2 and
5 data of an area g_4 of track B_2 , for the first frame F1. The tracing 1-2 and the tracing 1-3 for the second frame F2 pick up data of an area h_1 of track A_2 and data of an area h_2 of track B_2 for the second frame F2.

On the other hand, the tracing 1-4 and the tracing 1-5 for
10 the third frame F3 pick up data of an area i_1 of track A_2 , data of an area i_2 of track B_2 , data of an area i_3 of track A_3 and data of an area i_4 of track B_3 , for the third frame F3.

As described above, data of the area h_1 for the second frame F2, picked up by the tracing 1-2, is continuous to data of the
15 area g_3 of the first frame F1 picked up by the tracing 1-0, while data of the area i_1 for the third frame F3, picked up by the tracing 1-4, is continuous to data of the area h_1 .

As for the tracing of the second to the fourth heads, similarly to the tracing of the first head, described above, the
20 data picked up become continuous data.

The data thus picked up from the respective frames are sent
via the signal processor 4 to the picture display processor 8.

The picture display processor 8 allocates data picked up from the video tape 3 run at the varying playback speed by the
25 driving controller 7 to picture positions associated with the

data for displaying the picture.

Referring to Fig.17, the picture display processor 8 displays data of the area i_1 at a picture area 3 positioned in association with the data of the area i_1 . The picture display processor 8 also displays data of the area h_1 at a picture area 2 positioned in association with the data of the area h_1 , while displaying data of the area g_3 at a picture area 1 positioned in association with the data of the area g_3 .

Thus the picture area 1 formed by the data picked up from the first frame F1, the picture area 2 formed by the data picked up from the second frame F2 and the picture area 3 formed by the data picked up from the third frame F3 become continuous picture areas.

If the first frame F1, second frame F2 and the third frame F3 are traced 20 times to produce one-frame data, and another one-frame data is produced by the next 20 tracings, the fractional picture 6 formed by data picked up from the sixth frame, the fractional picture 5 formed by data picked up from the fifth frame and the fractional picture 4 formed by data picked up from the fourth frame become continuous fractional pictures, by display processing by the picture display processor 8, as shown in Fig.18. That is, the neighboring macro-blocks are made up of data of temporally proximate frames.

By setting the running speed of the video tape 3 at the speed setting circuit 71 to the varying playback speed S shown

by the equation (1) for varying speed reproduction so that the neighboring macro-blocks will be constituted by data of temporally proximate frames, as in the above-described first embodiment, the boundary lines between neighboring macro-blocks become less noticeable so that a spontaneous varying speed playback picture is obtained.

Although the rotary head unit 1 is so configured that continuous data will be picked up by the same heads, it may also be configured so that continuous data will be picked up by the diametrically opposing heads.

More specifically, if, with the above equation (3), the running direction is the normal direction and the constants a and m are such that $a = 2$ and $m = 1$, that is the varying playback speed S is such that $S = 5.25$, the driving circuit 72 runs the video tape 3 at the varying playback speed $S = 5.25$ as set by the speed setting circuit 71.

In this case, as for the tracing from 1-0 to 1-3 of the first head, the tracing 1-0 and the tracing 1-1 for the first frame F1 pick up data of an area j_1 of track A_1 , data of an area j_2 of track B_1 , data of an area j_3 of track A_2 and data of an area j_4 of track B_2 , data of an area j_5 of track A_3 and data of an area j_6 of track B_3 , as shown in Fig.19. As for the tracing 1-2 and the tracing 1-3 of the third frame F-3, data of the areas l_1 to l_6 are picked up in a similar manner. As for the tracing 3-0 and the tracing 3-1 of the third head, data of the areas k_1 to k_4 are

picked up by the tracing 3-0 and the tracing 3-1 for the second frame F-2.

As for the tracing from 2-0 to 2-3 of the second head and the tracing 4-0 and the tracing 4-1 of the fourth head, data can be picked up in a similar manner.

In the present case, similarly to the case of Fig.16 in which continuous data can be picked up by the same heads, data of the area k_1 of the second frame F2 picked up by the tracing 3-0 is continuous to data of the area j_3 of the first frame F1 picked up by the tracing 1-0, while data of the area l_1 of the third frame F3 picked up by the tracing 1-2 is continuous to data of the area k_1 .

By display processing by the picture display processor 8, the fractional picture corresponding to data of the area l_1 is displayed in a picture area 3 positioned in association with the data of the area l_1 , as shown in Fig.20. Similarly, the fractional picture corresponding to data of the area k_1 is displayed in a picture area 2 positioned in association with the data of the area k_1 , while the fractional picture corresponding to data of the area j_3 is displayed in a picture area 1 positioned in association with the data of the area j_3 . In this manner, the picked up picture data are allocated to the picture positions associated with the picked up data for displaying the fractional pictures.

Thus, in this case, the picture area 1 formed by the data

picked up from the first frame F1, the picture area 2 formed by the data picked up from the second frame F2 and the picture area 3 formed by the data picked up from the third frame F3 become continuous picture areas.

- 5 If the first frame F1, second frame F2 and the third frame F3 are traced 20 times to produce one-frame data, and another one-frame data is produced by the next 20 times of tracing, the fractional picture 20 formed by data picked up from the 20th frame, the fractional picture 19 formed by data picked up from the 19th frame and the fractional picture 18 formed by data picked up from the 18th frame, ... become continuous fractional pictures, by display processing by the picture display processor 8, as shown in Fig.21. That is, the neighboring macro-blocks are made up of data of temporally proximate frames.

The claims defining the invention are as follows:

~~WHAT IS CLAIMED IS:~~

1. A method for reproducing digital video signals recorded on a recording medium by an inclined azimuth recording system, wherein the improvement resides in that

during varying speed reproduction, varying speed reproduction is carried out by setting a varying playback speed S so that $S = a(t \cdot m \pm 1)/n$

where n is the number of heads, a is a constant of 1 or 2, t is the number of tracks on which one-frame data of the digital video signals is recorded, and m is a constant which is an integer other than 0.

2. The method as claimed in claim 1, wherein the digital video signals read out with the varying playback speed are allocated and displayed at positions on a picture associated with the digital video signals.

3. The method as claimed in claim 1, wherein the varying playback speed is determined discretely.

4. The method as claimed in claim 1, wherein the combination of the plural heads reproducing the signals is switched depending upon the varying playback speed during the varying speed reproduction.

5. The method as claimed in claim 1, wherein the number of the plural heads is 4 or 8.

6. An apparatus for reproducing digital video signals in which digital video signals recorded by a rotary head unit having n

heads in accordance with an inclined azimuth recording system so that one frame data of the digital video signals are recorded on n tracks of the rotary head unit are read out and in which a picture corresponding to the digital video signals thus read out is displayed on a screen by picture display processing means, wherein the improvement comprises

driving control means for varying speed reproduction of the digital data during varying speed reproduction with a varying playback speed S such that $S = a(t \cdot m \pm 1)/n$, where a is a constant equal to 1 or 2 and m is a constant which is an integer other than 0.

7. The apparatus as claimed in claim 6, wherein said picture display processor allocate and display digital video signals read out with the varying playback speed at positions on a picture associated with the digital video signals.

8. The apparatus as claimed in claim 6, wherein the digital video signals read out with the varying playback speed are allocated and displayed at positions on a picture associated with the digital video signals.

9. The apparatus as claimed in claim 7, wherein the varying playback speed is determined discretely.

10. The apparatus as claimed in claim 9, wherein the combination of the plural heads reproducing the signals is switched depending upon the varying playback speed during the varying speed reproduction.

11. The apparatus as claimed in claim 6, wherein the number of the plural heads is 4 or 8.

DATED this TWENTY-FOURTH day of AUGUST 1995
Sony Corporation

Patent Attorneys for the Applicant
SPRUSON & FERGUSON

Method and Apparatus for Recording and Reproducing Digital
Video Signals

ABSTRACT

A method and an apparatus for reproducing digital video signals in
5 which every one-frame data of the digital video signals are recorded on n
tracks of a recording medium by an inclined azimuth recording system are
varying speed reproduced by a rotary head (1) having n heads with a
varying playback speeds such that $A = a(t \cdot m + 1)/n$, where a is a constant
equal to 1 or 2 and m is a constant which is an integer other than 0. A
10 picture corresponding to the digital video signals read out with the
varying playback speed (S) is displayed on the screen. In this manner,
non-continuous lines caused by temporal difference in neighboring
macro-blocks during varying speed reproduction may be rendered less
obtrusive for improving the picture quality of the varying speed
reproduced picture.

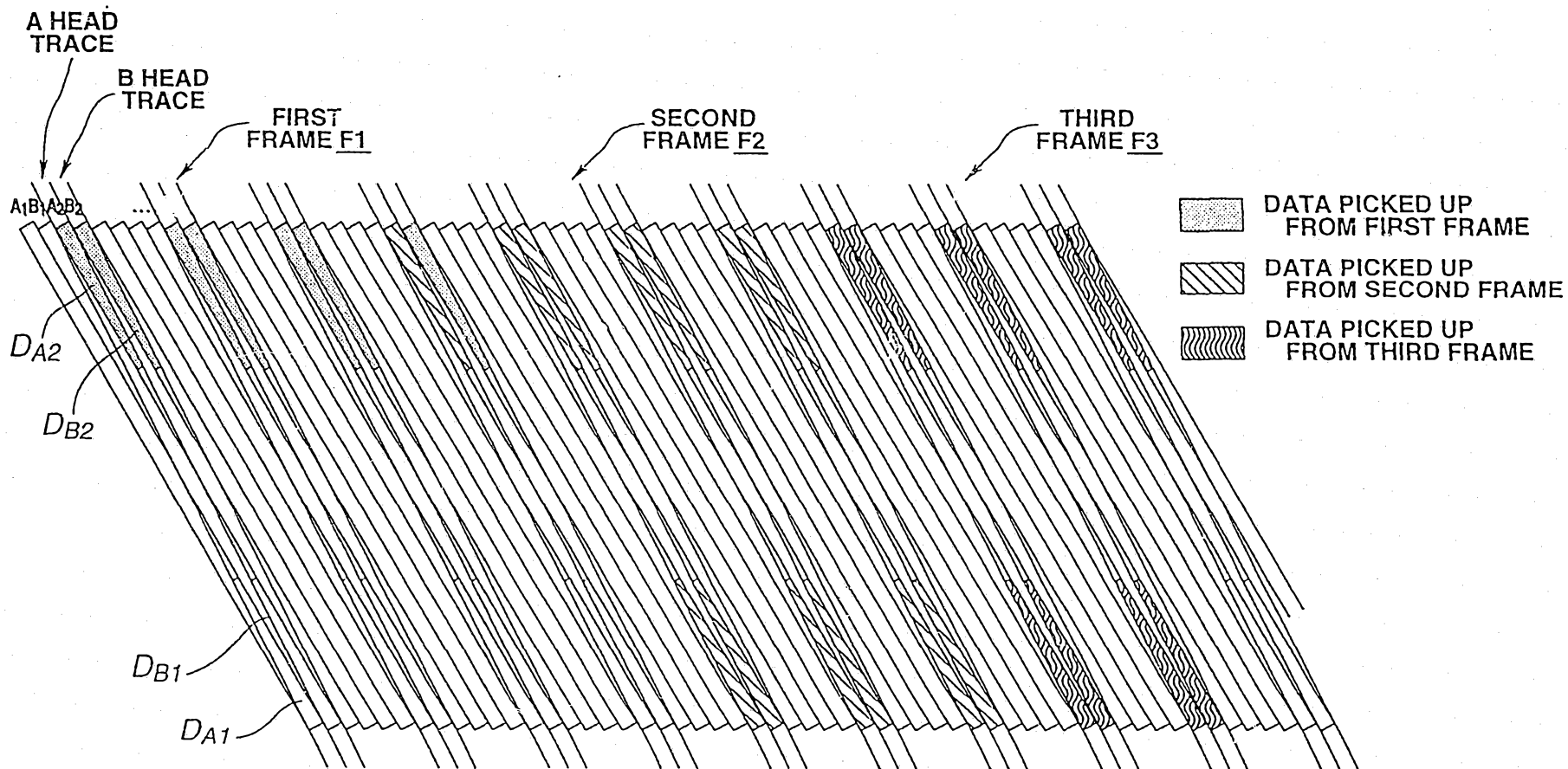


FIG.1

24 00 95 30233

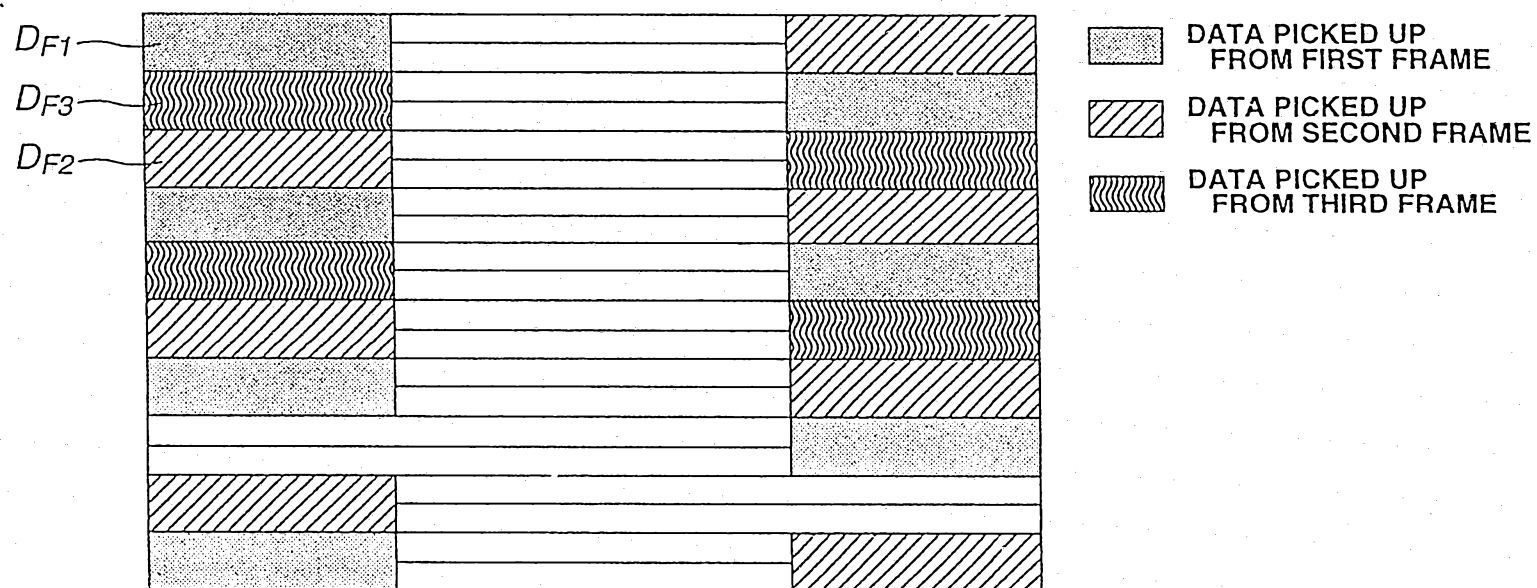


FIG.2

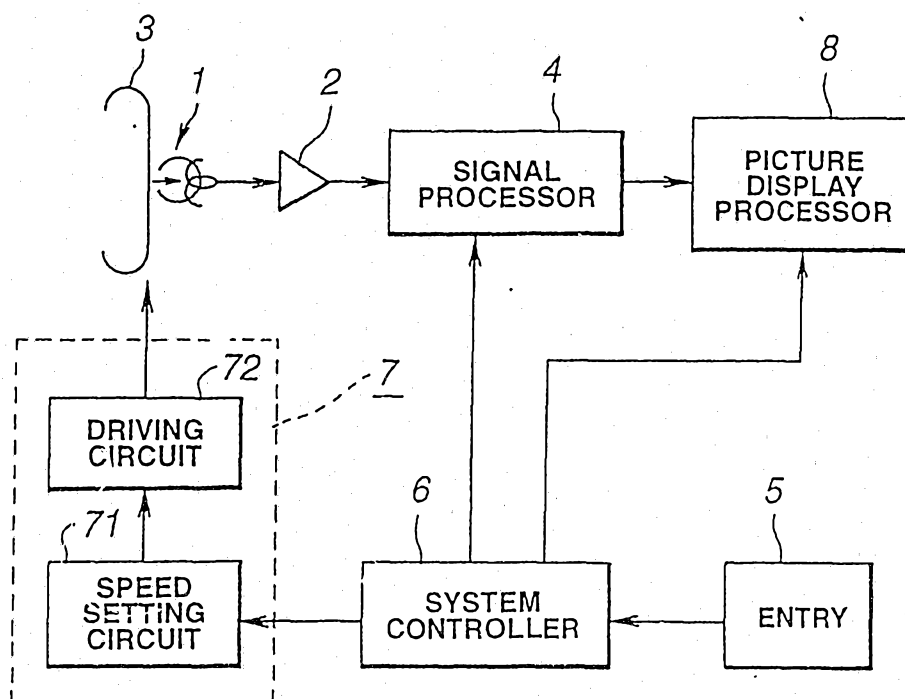


FIG.3

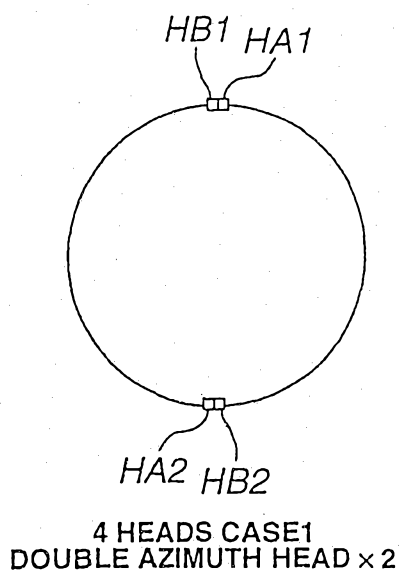


FIG.4

24 00 95 30233

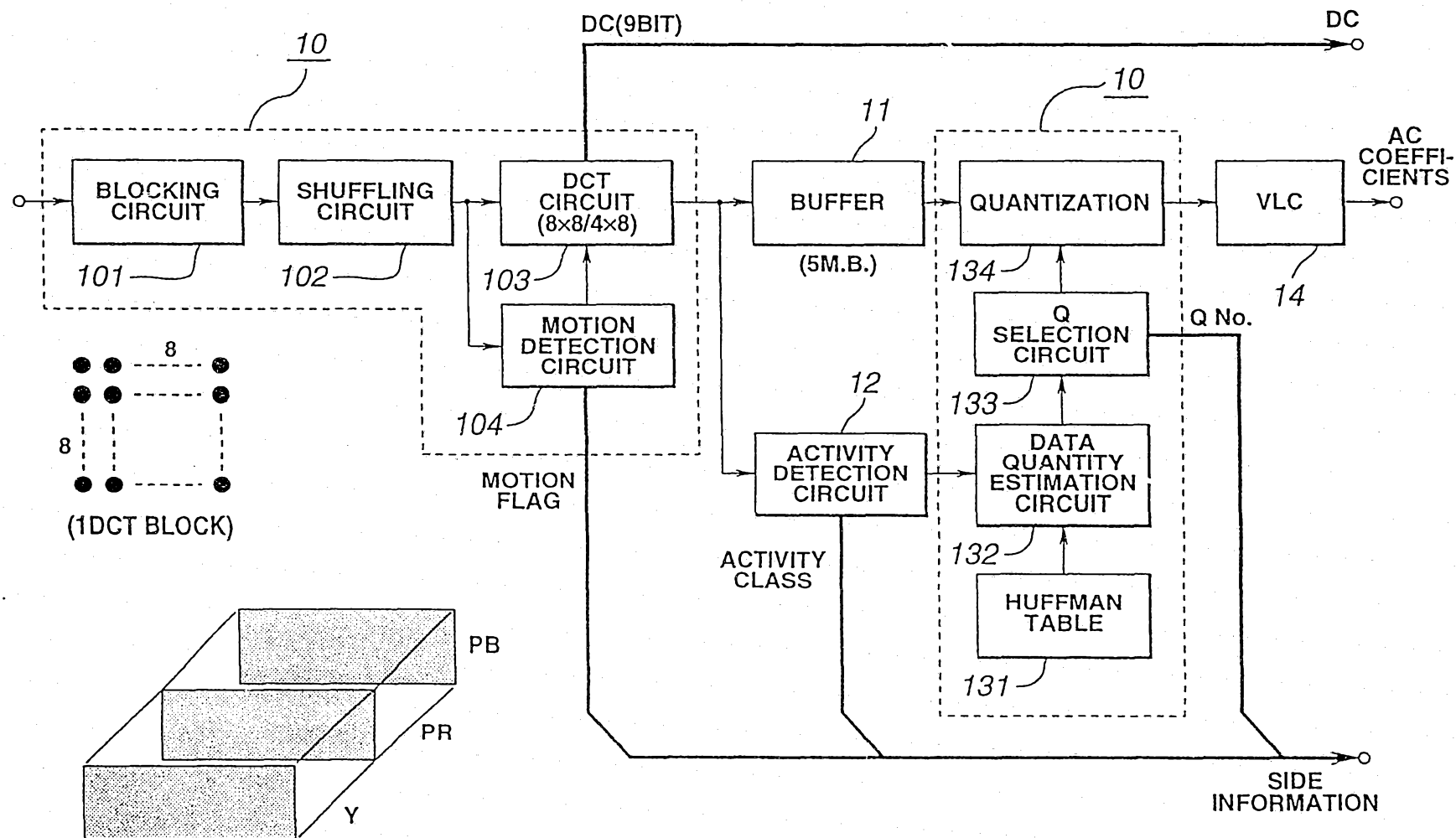


FIG.5

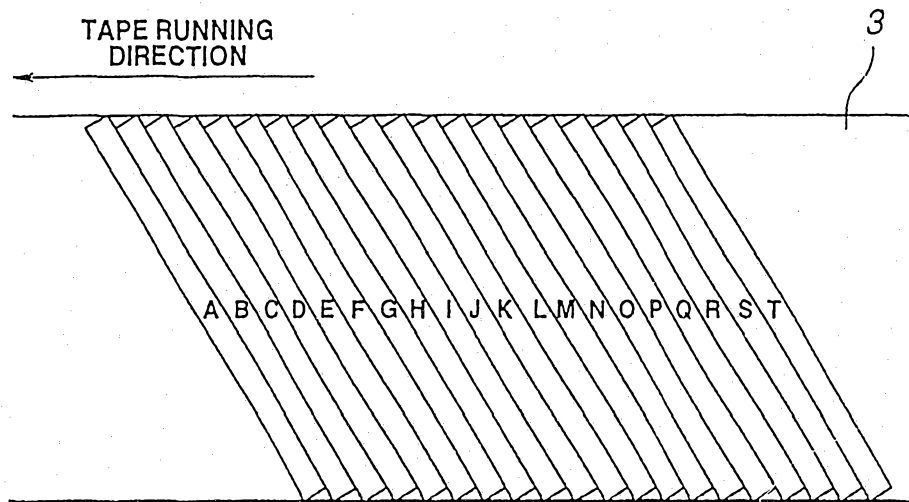


FIG.6

	A
	B
	C
	D
	E
	F
	G
	H
	I
	J
	K
	L
	M
	N
	O
	P
	Q
	R
	S
	T

FIG.7

24 00 95 30233

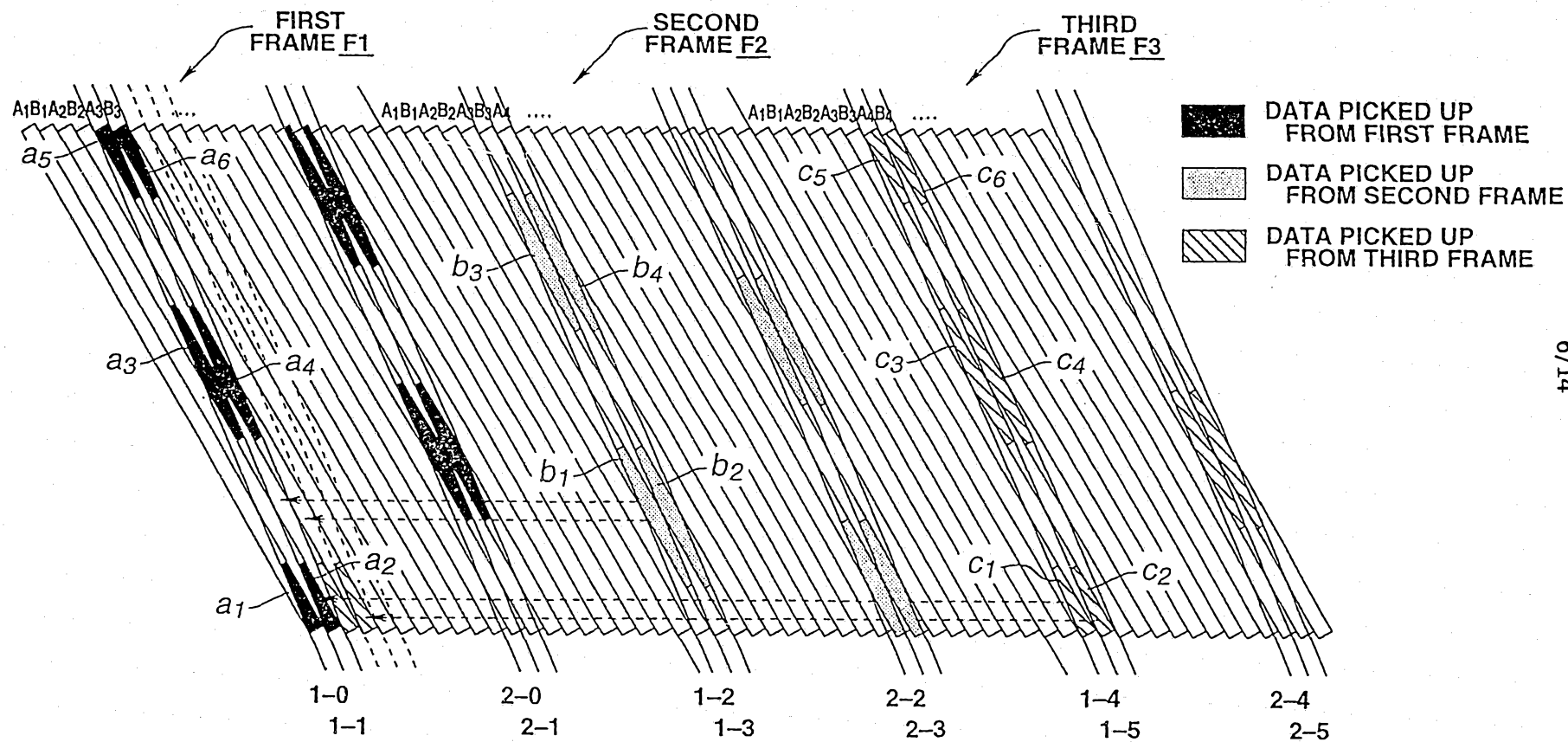


FIG.8

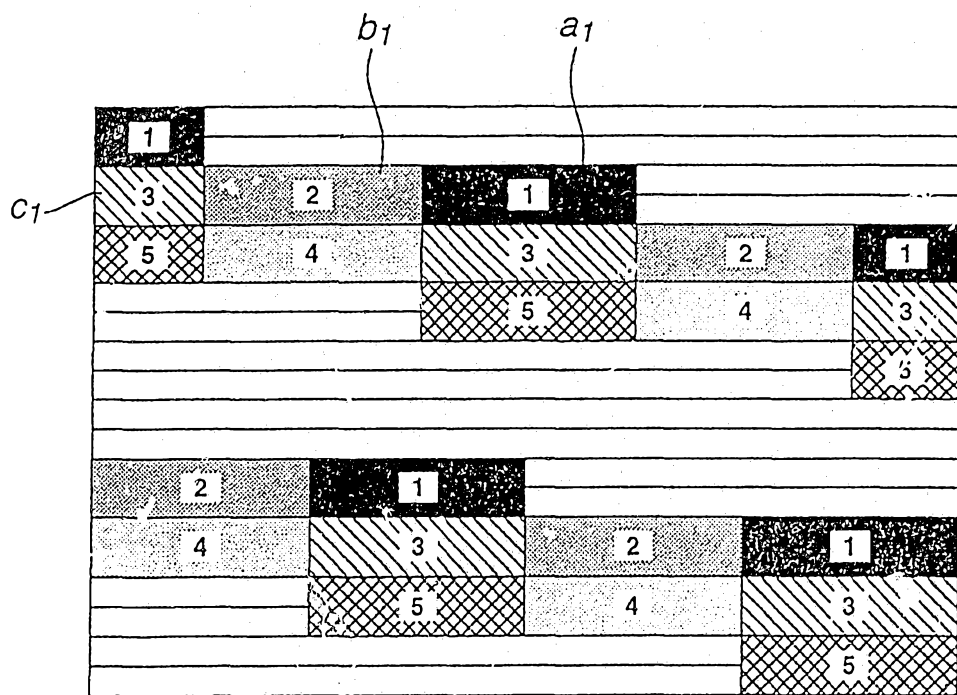


FIG. 9

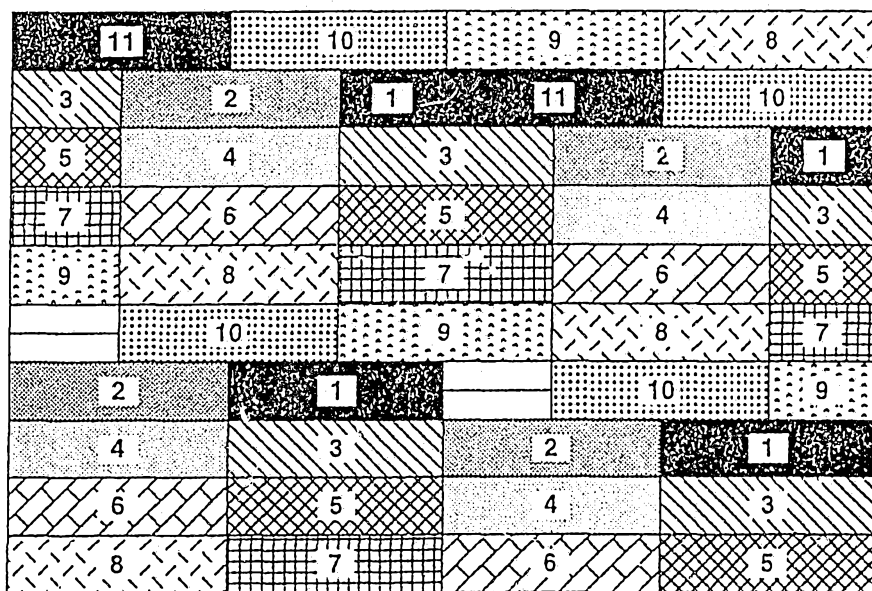
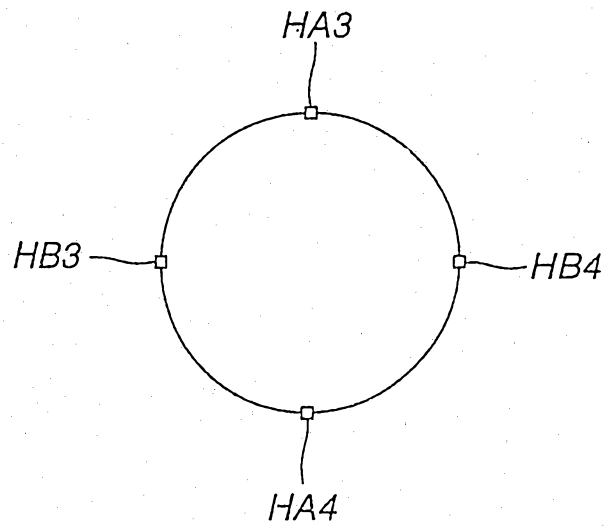
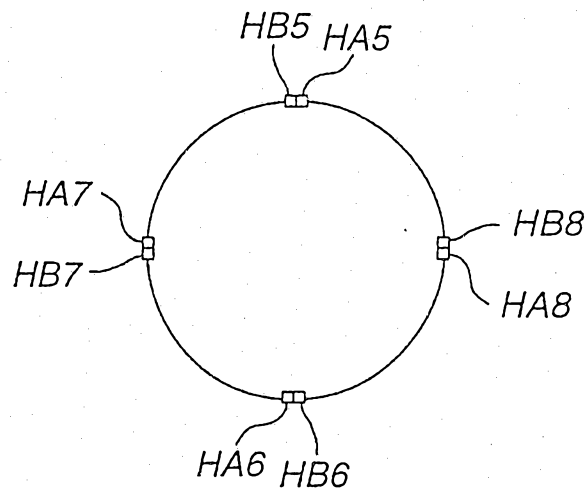


FIG. 10



4 HEADS CASE2

FIG.11



8 HEADS DOUBLE
AZIMUTH HEAD $\times 4$

FIG.15

24 08 95 30233

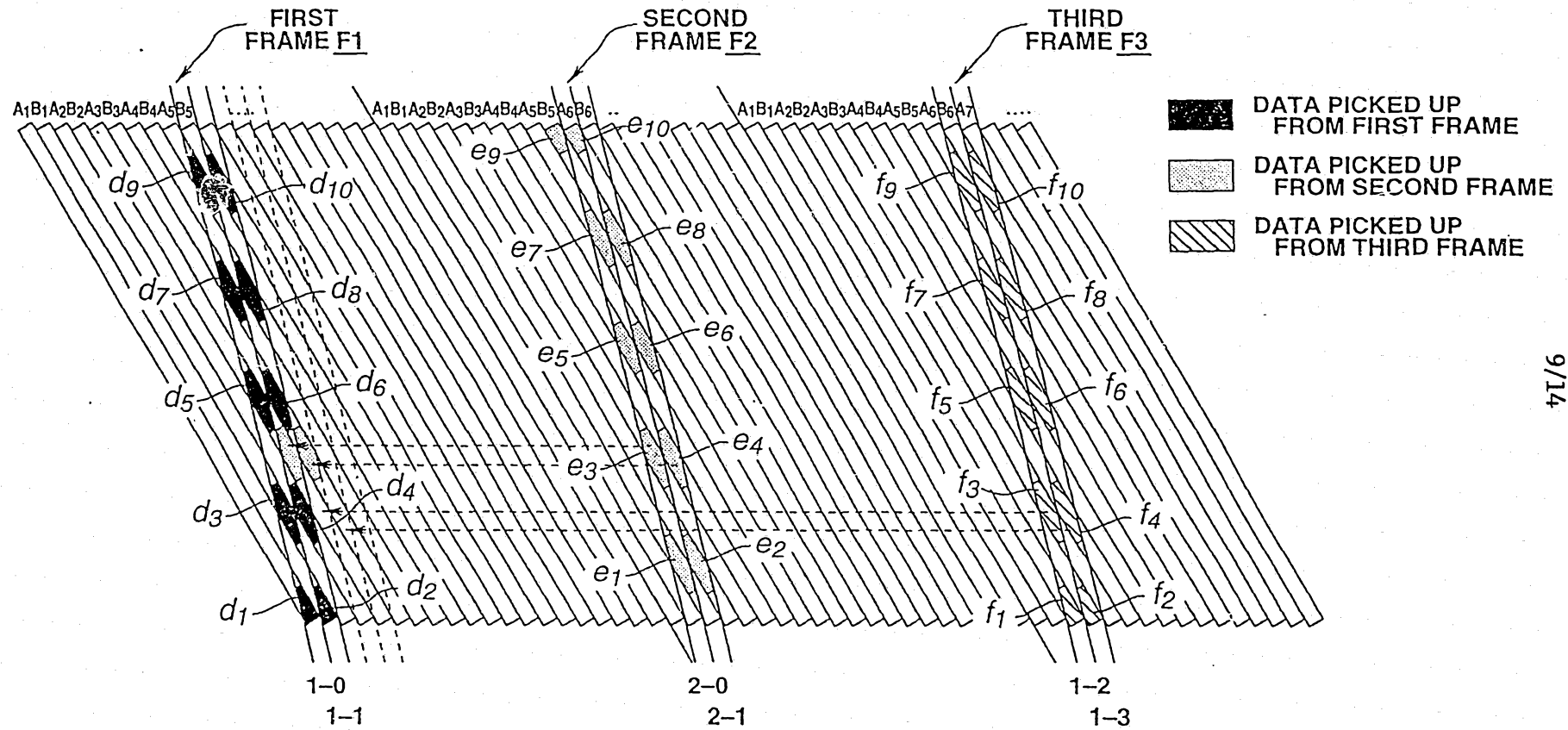


FIG.12

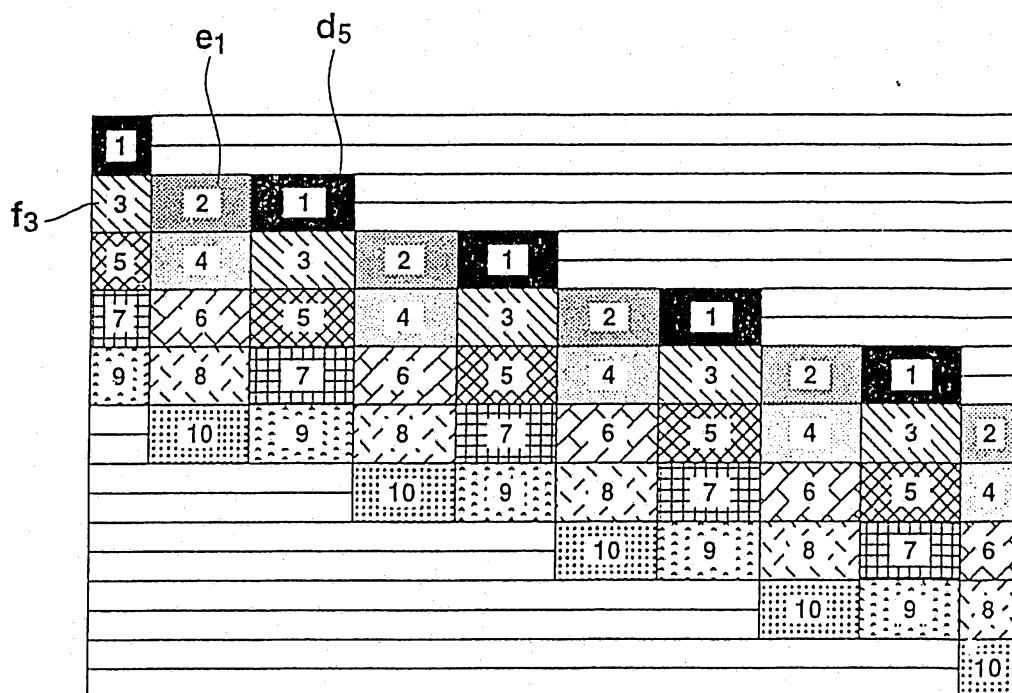


FIG. 13

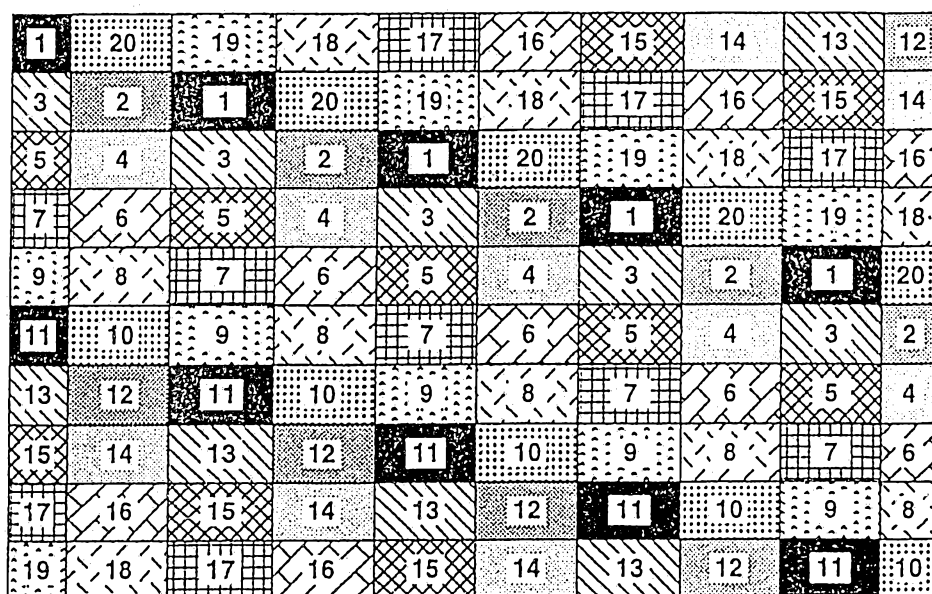


FIG. 14

24 08 95 30233

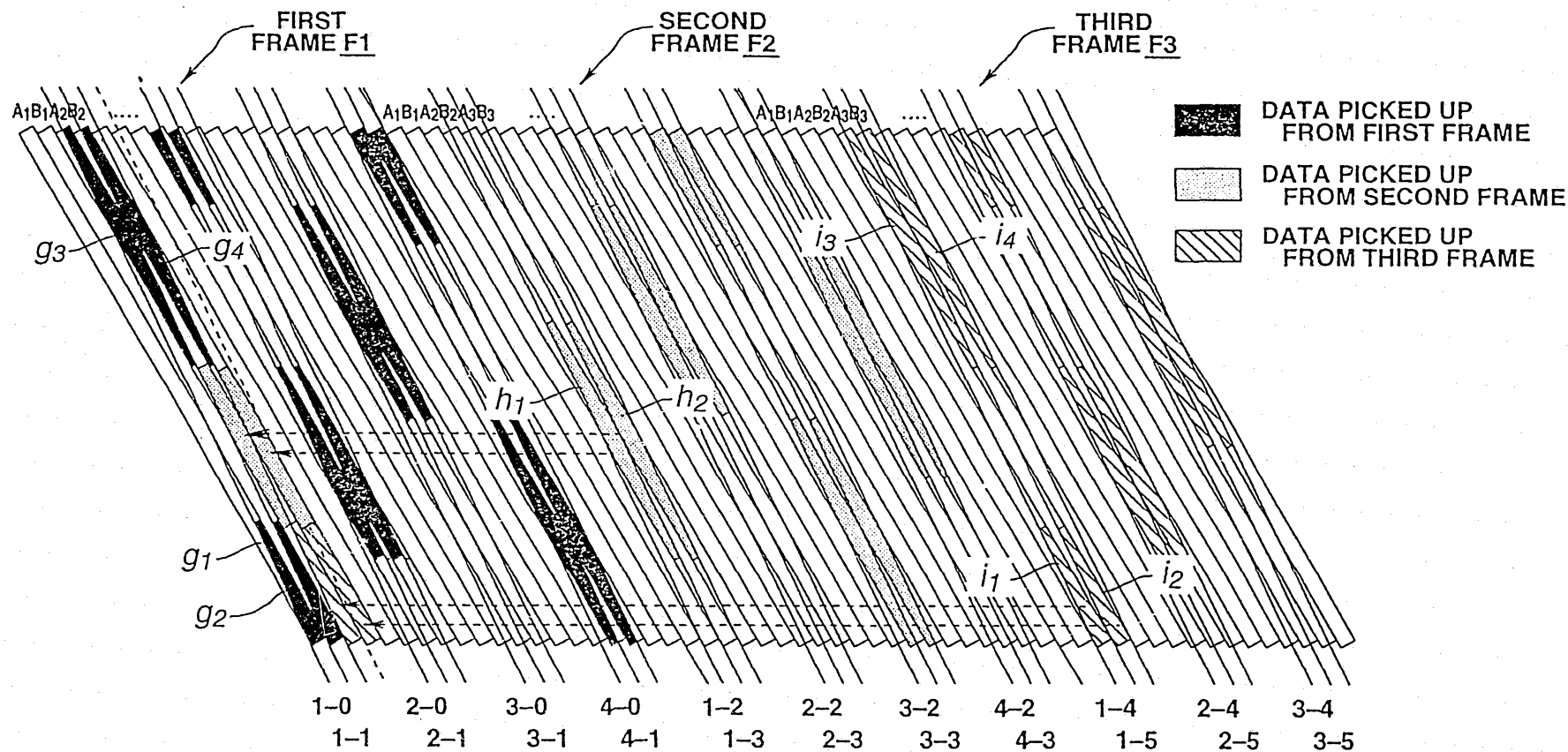


FIG.16

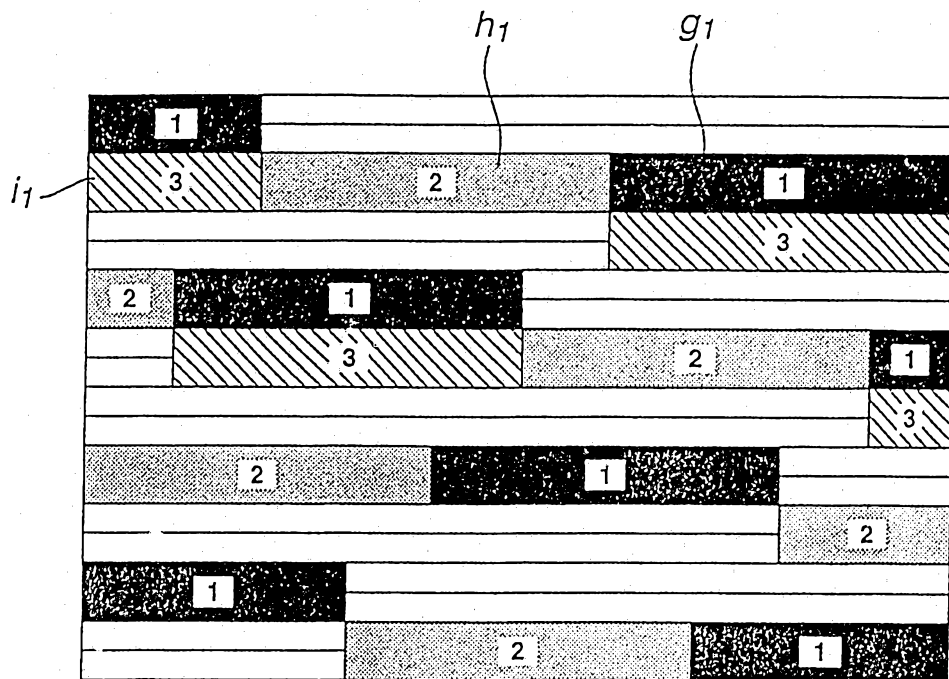


FIG.17

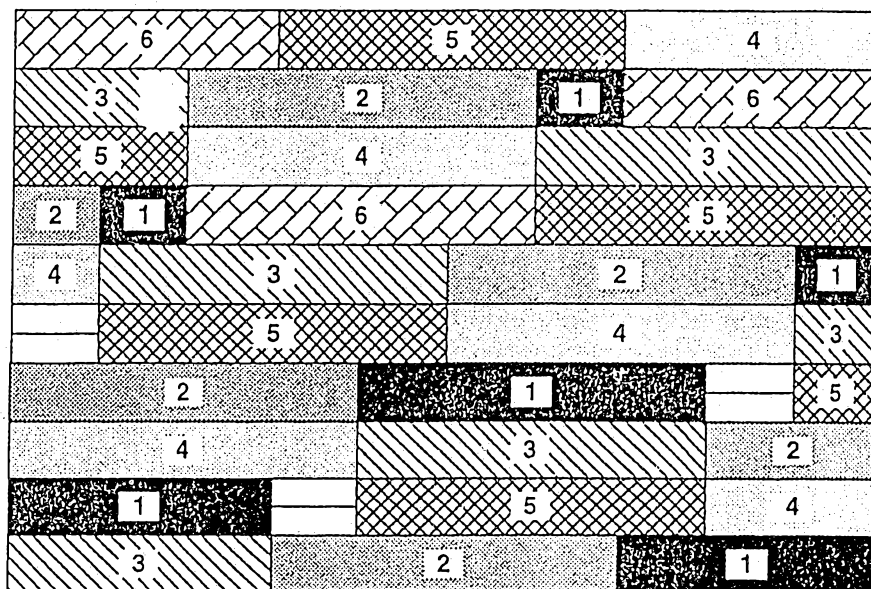
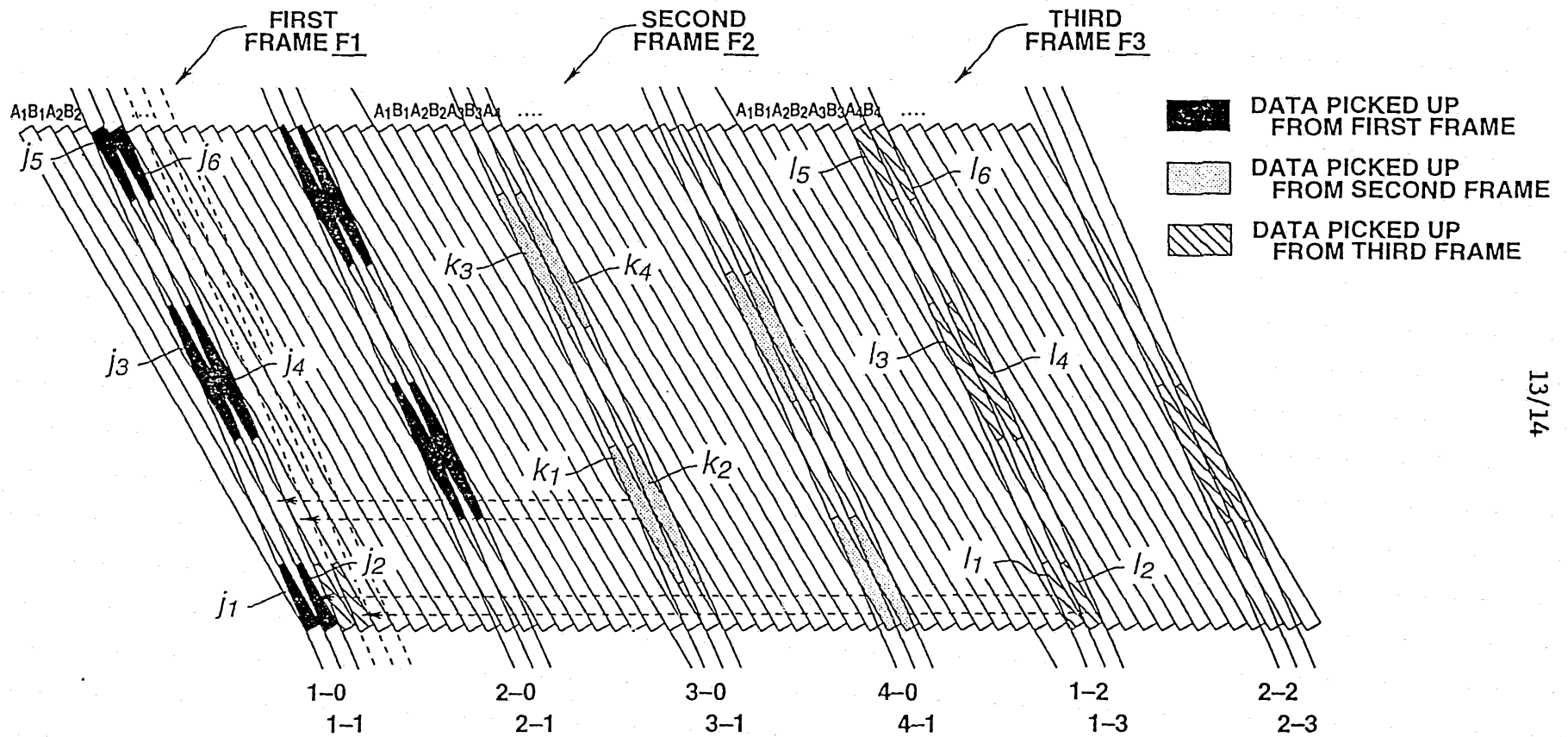


FIG.18

24 08 95 30233



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FIG.19

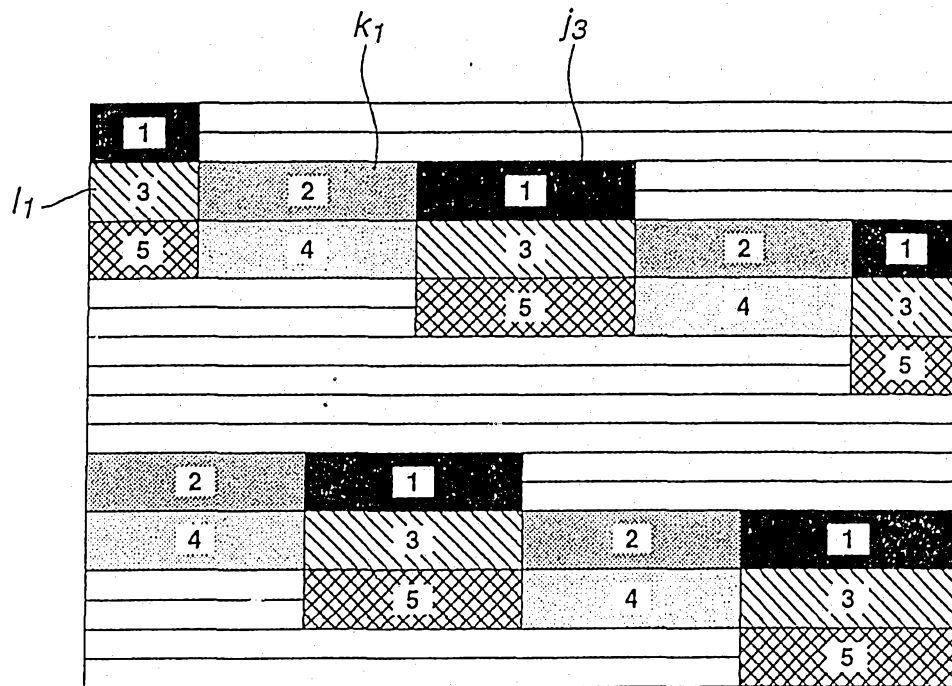


FIG. 20

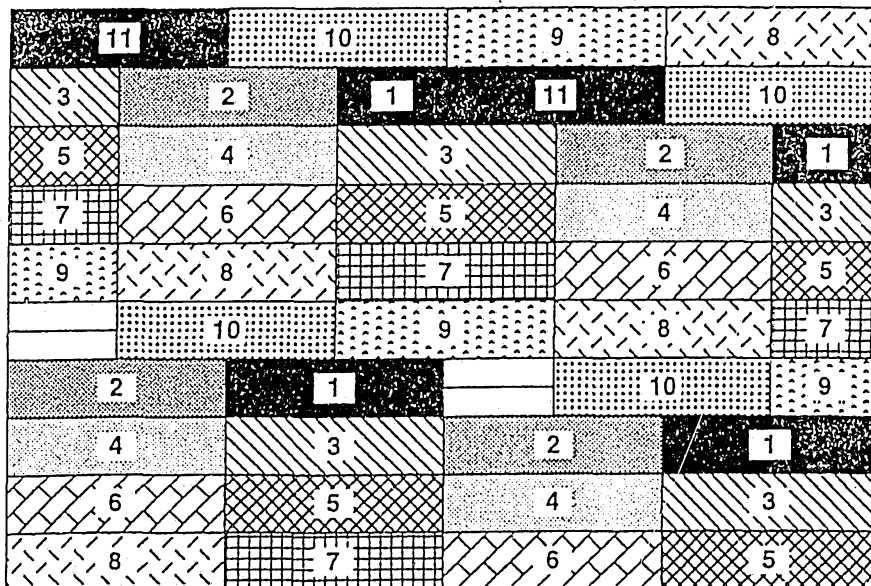


FIG. 21