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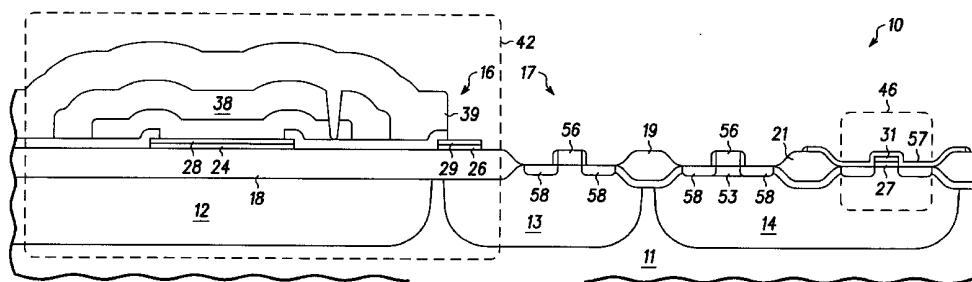
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(54) Title: INTEGRATED CMOS CAPACITIVE PRESSURE SENSOR



(57) Abstract: A capacitive pressure sensor (10) utilizes a diaphragm (38) that is formed along with forming gates (56,57) of active devices on the same semiconductor substrate (11).



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G01L9/12

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 012 336 A (W.P. EATON ET AL.) 11 January 2000 (2000-01-11)	1
Y	column 8, line 26 - line 40; figures 2F,2G	2-4
A		6,13
Y	----- KASTEN K ET AL: "CMOS-compatible capacitive high temperature pressure sensors" SENSORS AND ACTUATORS A, ELSEVIER SEQUOIA S.A., LAUSANNE, CH, vol. 85, no. 1-3, 25 August 2000 (2000-08-25), pages 147-152, XP004214462 ISSN: 0924-4247 page 148, left-hand column, paragraph 2; figure 2 -----	2-4

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Patent family members are listed in annex.

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 6012336	A	11-01-2000	US 5798283 A	25-08-1998
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			US 5963788 A	05-10-1999
