

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



WIPO | PCT

**(43) International Publication Date
31 July 2014 (31.07.2014)**

(10) International Publication Number

WO 2014/116479 A1

(51) **International Patent Classification:**
H04B 7/04 (2006.01) **H04B 7/06** (2006.01)

(21) **International Application Number:**
PCT/US2014/011609

(22) **International Filing Date:**
15 January 2014 (15.01.2014)

(25) **Filing Language:** English

(26) **Publication Language:** English

(30) **Priority Data:**
13/747,601 23 January 2013 (23.01.2013) US

(71) **Applicant:** **QUALCOMM INCORPORATED** [US/US];
c/o International IP Administration, 5775 Morehouse
Drive, San Diego, California 92121-1714 (US).

(72) **Inventors:** **JOSE, Jubin**; c/o QUALCOMM Incorporated,
5775 Morehouse Drive, San Diego, California 92121-1714
(US). **WU, Xinzhou**; c/o QUALCOMM Incorporated,
5775 Morehouse Drive, San Diego, California 92121-1714
(US). **SUBRAMANIAN, Sundar**; c/o QUALCOMM Incorporated,
5775 Morehouse Drive, San Diego, California 92121-1714
(US). **LI, Junyi**; c/o QUALCOMM Incorporated,
5775 Morehouse Drive, San Diego, California 92121-1714 (US).

(74) **Agents:** **HODGES, Jonas J.** et al.; Arent Fox LLP, 1717
K Street, NW, Washington, District of Columbia 20036
(US).

(81) **Designated States** (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) **Designated States** (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: OPPORTUNISTIC INTERFERENCE ALIGNMENT FOR MULTI-CELL MULTI-USER UPLINK

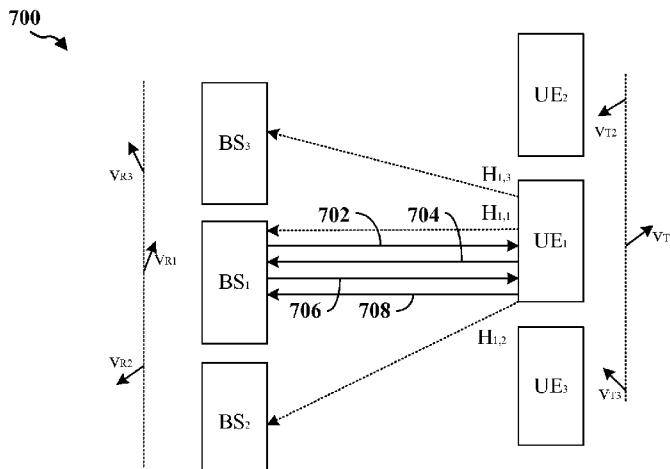


FIG. 7

(57) Abstract: A UE receives information indicating a receive direction vector for a serving BS and a set of receive direction vectors for at least one interfering BS. The UE determines a channel between the UE and the serving BS and a set of channels between the UE and each of the at least one interfering BS. The UE determines a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors. The UE determines an interference caused to the at least one interfering BS by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors. The UE transmits information indicating the interference to the serving BS.



Published:

— *with international search report (Art. 21(3))*

OPPORTUNISTIC INTERFERENCE ALIGNMENT FOR MULTI-CELL MULTI-USER UPLINK

CROSS-REFERENCE TO RELATED APPLICATION(S)

[0001] This international patent application claims the benefit of U.S. non-Provisional Application No. 13/747,601 entitled “OPPORTUNISTIC INTERFERENCE ALIGNMENT FOR MULTI-CELL MULTI-USER UPLINK” and filed on January 23, 2013 which is expressly incorporated by reference herein in its entirety.

BACKGROUND

Field

[0002] The present disclosure relates generally to communication systems, and more particularly, to opportunistic interference alignment for multi-cell multi-user uplink.

Background

[0003] Wireless communication systems are widely deployed to provide various telecommunication services such as telephony, video, data, messaging, and broadcasts. Typical wireless communication systems may employ multiple-access technologies capable of supporting communication with multiple users by sharing available system resources (e.g., bandwidth, transmit power). Examples of such multiple-access technologies include code division multiple access (CDMA) systems, time division multiple access (TDMA) systems, frequency division multiple access (FDMA) systems, orthogonal frequency division multiple access (OFDMA) systems, single-carrier frequency division multiple access (SC-FDMA) systems, and time division synchronous code division multiple access (TD-SCDMA) systems.

[0004] These multiple access technologies have been adopted in various telecommunication standards to provide a common protocol that enables different wireless devices to communicate on a municipal, national, regional, and even global level. An example of an emerging telecommunication standard is Long Term Evolution (LTE). LTE is a set of enhancements to the Universal Mobile Telecommunications System (UMTS) mobile standard promulgated by Third Generation Partnership Project (3GPP). It is designed to better support mobile

broadband Internet access by improving spectral efficiency, lowering costs, improving services, making use of new spectrum, and better integrating with other open standards using OFDMA on the downlink (DL), SC-FDMA on the uplink (UL), and multiple-input multiple-output (MIMO) antenna technology. However, as the demand for mobile broadband access continues to increase, there exists a need for further improvements in LTE technology. Preferably, these improvements should be applicable to other multi-access technologies and the telecommunication standards that employ these technologies.

SUMMARY

[0005] In an aspect of the disclosure, a method, a computer program product, and an apparatus are provided. The apparatus is a user equipment (UE). The UE receives information indicating a receive direction vector for a serving base station and a set of receive direction vectors for at least one interfering base station. The UE determines a channel between the UE and the serving base station. The UE determines a set of channels between the UE and each of the at least one interfering base station. The UE determines a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors. Each modulated symbol of the modulated symbols is mapped to a plurality of resource blocks of the set of resource blocks. The UE determines an interference caused to the at least one interfering base station by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors. The UE transmits information indicating the interference to the serving base station.

BRIEF DESCRIPTION OF THE DRAWINGS

- [0006] FIG. 1 is a diagram illustrating an example of a network architecture.
- [0007] FIG. 2 is a diagram illustrating an example of an access network.
- [0008] FIG. 3 is a diagram illustrating an example of a DL frame structure in LTE.
- [0009] FIG. 4 is a diagram illustrating an example of an UL frame structure in LTE.

[0010] FIG. 5 is a diagram illustrating an example of a radio protocol architecture for the user and control planes.

[0011] FIG. 6 is a diagram illustrating an example of an evolved Node B and user equipment in an access network.

[0012] FIG. 7 is a first diagram for illustrating exemplary methods.

[0013] FIG. 8 is a second diagram for illustrating exemplary methods.

[0014] FIG. 9 is a third diagram for illustrating exemplary methods.

[0015] FIG. 10A is a fourth diagram for illustrating exemplary methods.

[0016] FIG. 10B is a fifth diagram for illustrating exemplary methods.

[0017] FIG. 11 is a flow chart of a method of wireless communication.

[0018] FIG. 12 is a conceptual data flow diagram illustrating the data flow between different modules/means/components in an exemplary apparatus.

[0019] FIG. 13 is a diagram illustrating an example of a hardware implementation for an apparatus employing a processing system.

DETAILED DESCRIPTION

[0020] The detailed description set forth below in connection with the appended drawings is intended as a description of various configurations and is not intended to represent the only configurations in which the concepts described herein may be practiced. The detailed description includes specific details for the purpose of providing a thorough understanding of various concepts. However, it will be apparent to those skilled in the art that these concepts may be practiced without these specific details. In some instances, well known structures and components are shown in block diagram form in order to avoid obscuring such concepts.

[0021] Several aspects of telecommunication systems will now be presented with reference to various apparatus and methods. These apparatus and methods will be described in the following detailed description and illustrated in the accompanying drawings by various blocks, modules, components, circuits, steps, processes, algorithms, etc. (collectively referred to as “elements”). These elements may be implemented using electronic hardware, computer software, or any combination thereof. Whether such elements are implemented as hardware or software depends

upon the particular application and design constraints imposed on the overall system.

[0022] By way of example, an element, or any portion of an element, or any combination of elements may be implemented with a “processing system” that includes one or more processors. Examples of processors include microprocessors, microcontrollers, digital signal processors (DSPs), field programmable gate arrays (FPGAs), programmable logic devices (PLDs), state machines, gated logic, discrete hardware circuits, and other suitable hardware configured to perform the various functionality described throughout this disclosure. One or more processors in the processing system may execute software. Software shall be construed broadly to mean instructions, instruction sets, code, code segments, program code, programs, subprograms, software modules, applications, software applications, software packages, routines, subroutines, objects, executables, threads of execution, procedures, functions, etc., whether referred to as software, firmware, middleware, microcode, hardware description language, or otherwise.

[0023] Accordingly, in one or more exemplary embodiments, the functions described may be implemented in hardware, software, firmware, or any combination thereof. If implemented in software, the functions may be stored on or encoded as one or more instructions or code on a computer-readable medium. Computer-readable media includes computer storage media. Storage media may be any available media that can be accessed by a computer. By way of example, and not limitation, such computer-readable media can comprise RAM, ROM, EEPROM, CD-ROM or other optical disk storage, magnetic disk storage or other magnetic storage devices, or any other medium that can be used to carry or store desired program code in the form of instructions or data structures and that can be accessed by a computer. Disk and disc, as used herein, includes compact disc (CD), laser disc, optical disc, digital versatile disc (DVD), and floppy disk where disks usually reproduce data magnetically, while discs reproduce data optically with lasers. Combinations of the above should also be included within the scope of computer-readable media.

[0024] FIG. 1 is a diagram illustrating an LTE network architecture 100. The LTE network architecture 100 may be referred to as an Evolved Packet System (EPS) 100. The EPS 100 may include one or more user equipment (UE) 102, an Evolved UMTS Terrestrial Radio Access Network (E-UTRAN) 104, an Evolved Packet Core

(EPC) 110, a Home Subscriber Server (HSS) 120, and an Operator's Internet Protocol (IP) Services 122. The EPS can interconnect with other access networks, but for simplicity those entities/interfaces are not shown. As shown, the EPS provides packet-switched services, however, as those skilled in the art will readily appreciate, the various concepts presented throughout this disclosure may be extended to networks providing circuit-switched services.

[0025] The E-UTRAN includes the evolved Node B (eNB) 106 and other eNBs 108. The eNB 106 provides user and control planes protocol terminations toward the UE 102. The eNB 106 may be connected to the other eNBs 108 via a backhaul (e.g., an X2 interface). The eNB 106 may also be referred to as a base station (BS), a Node B, an access point, a base transceiver station, a radio BS, a radio transceiver, a transceiver function, a basic service set (BSS), an extended service set (ESS), or some other suitable terminology. The eNB 106 provides an access point to the EPC 110 for a UE 102. Examples of UEs 102 include a cellular phone, a smart phone, a session initiation protocol (SIP) phone, a laptop, a personal digital assistant (PDA), a satellite radio, a global positioning system, a multimedia device, a video device, a digital audio player (e.g., MP3 player), a camera, a game console, a tablet, or any other similar functioning device. The UE 102 may also be referred to by those skilled in the art as a mobile station, a subscriber station, a mobile unit, a subscriber unit, a wireless unit, a remote unit, a mobile device, a wireless device, a wireless communications device, a remote device, a mobile subscriber station, an access terminal, a mobile terminal, a wireless terminal, a remote terminal, a handset, a user agent, a mobile client, a client, or some other suitable terminology.

[0026] The eNB 106 is connected to the EPC 110. The EPC 110 includes a Mobility Management Entity (MME) 112, other MMEs 114, a Serving Gateway 116, a Multimedia Broadcast Multicast Service (MBMS) Gateway 124, a Broadcast Multicast Service Center (BM-SC) 126, and a Packet Data Network (PDN) Gateway 118. The MME 112 is the control node that processes the signaling between the UE 102 and the EPC 110. Generally, the MME 112 provides bearer and connection management. All user IP packets are transferred through the Serving Gateway 116, which itself is connected to the PDN Gateway 118. The PDN Gateway 118 provides UE IP address allocation as well as other functions. The PDN Gateway 118 is connected to the Operator's IP Services 122. The Operator's IP Services 122

may include the Internet, an intranet, an IP Multimedia Subsystem (IMS), and a PS Streaming Service (PSS). The BM-SC 126 may provide functions for MBMS user service provisioning and delivery. The BM-SC 126 may serve as an entry point for content provider MBMS transmission, may be used to authorize and initiate MBMS Bearer Services within a PLMN, and may be used to schedule and deliver MBMS transmissions. The MBMS Gateway 124 may be used to distribute MBMS traffic to the eNBs (e.g., 106, 108) belonging to an MBSFN area broadcasting a particular service, and may be responsible for session management (start/stop) and for collecting eMBMS related charging information.

[0027] FIG. 2 is a diagram illustrating an example of an access network 200 in an LTE network architecture. In this example, the access network 200 is divided into a number of cellular regions (cells) 202. One or more lower power class eNBs 208 may have cellular regions 210 that overlap with one or more of the cells 202. The lower power class eNB 208 may be a femto cell (e.g., home eNB (HeNB)), pico cell, micro cell, or remote radio head (RRH). The macro eNBs 204 are each assigned to a respective cell 202 and are configured to provide an access point to the EPC 110 for all the UEs 206 in the cells 202. There is no centralized controller in this example of an access network 200, but a centralized controller may be used in alternative configurations. The eNBs 204 are responsible for all radio related functions including radio bearer control, admission control, mobility control, scheduling, security, and connectivity to the serving gateway 116.

[0028] The modulation and multiple access scheme employed by the access network 200 may vary depending on the particular telecommunications standard being deployed. In LTE applications, OFDM is used on the DL and SC-FDMA is used on the UL to support both frequency division duplex (FDD) and time division duplex (TDD). As those skilled in the art will readily appreciate from the detailed description to follow, the various concepts presented herein are well suited for LTE applications. However, these concepts may be readily extended to other telecommunication standards employing other modulation and multiple access techniques. By way of example, these concepts may be extended to Evolution-Data Optimized (EV-DO) or Ultra Mobile Broadband (UMB). EV-DO and UMB are air interface standards promulgated by the 3rd Generation Partnership Project 2 (3GPP2) as part of the CDMA2000 family of standards and employs CDMA to

provide broadband Internet access to mobile stations. These concepts may also be extended to Universal Terrestrial Radio Access (UTRA) employing Wideband-CDMA (W-CDMA) and other variants of CDMA, such as TD-SCDMA; Global System for Mobile Communications (GSM) employing TDMA; and Evolved UTRA (E-UTRA), IEEE 802.11 (Wi-Fi), IEEE 802.16 (WiMAX), IEEE 802.20, and Flash-OFDM employing OFDMA. UTRA, E-UTRA, UMTS, LTE and GSM are described in documents from the 3GPP organization. CDMA2000 and UMB are described in documents from the 3GPP2 organization. The actual wireless communication standard and the multiple access technology employed will depend on the specific application and the overall design constraints imposed on the system.

[0029] The eNBs 204 may have multiple antennas supporting MIMO technology. The use of MIMO technology enables the eNBs 204 to exploit the spatial domain to support spatial multiplexing, beamforming, and transmit diversity. Spatial multiplexing may be used to transmit different streams of data simultaneously on the same frequency. The data streams may be transmitted to a single UE 206 to increase the data rate or to multiple UEs 206 to increase the overall system capacity. This is achieved by spatially precoding each data stream (i.e., applying a scaling of an amplitude and a phase) and then transmitting each spatially precoded stream through multiple transmit antennas on the DL. The spatially precoded data streams arrive at the UE(s) 206 with different spatial signatures, which enables each of the UE(s) 206 to recover the one or more data streams destined for that UE 206. On the UL, each UE 206 transmits a spatially precoded data stream, which enables the eNB 204 to identify the source of each spatially precoded data stream.

[0030] Spatial multiplexing is generally used when channel conditions are good. When channel conditions are less favorable, beamforming may be used to focus the transmission energy in one or more directions. This may be achieved by spatially precoding the data for transmission through multiple antennas. To achieve good coverage at the edges of the cell, a single stream beamforming transmission may be used in combination with transmit diversity.

[0031] In the detailed description that follows, various aspects of an access network will be described with reference to a MIMO system supporting OFDM on the DL. OFDM is a spread-spectrum technique that modulates data over a number of subcarriers within an OFDM symbol. The subcarriers are spaced apart at precise

frequencies. The spacing provides “orthogonality” that enables a receiver to recover the data from the subcarriers. In the time domain, a guard interval (e.g., cyclic prefix) may be added to each OFDM symbol to combat inter-OFDM-symbol interference. The UL may use SC-FDMA in the form of a DFT-spread OFDM signal to compensate for high peak-to-average power ratio (PAPR).

[0032] FIG. 3 is a diagram 300 illustrating an example of a DL frame structure in LTE. A frame (10 ms) may be divided into 10 equally sized sub-frames. Each sub-frame may include two consecutive time slots. A resource grid may be used to represent two time slots, each time slot including a resource block. The resource grid is divided into multiple resource elements. In LTE, a resource block contains 12 consecutive subcarriers in the frequency domain and, for a normal cyclic prefix in each OFDM symbol, 7 consecutive OFDM symbols in the time domain, or 84 resource elements. For an extended cyclic prefix, a resource block contains 6 consecutive OFDM symbols in the time domain and has 72 resource elements. Some of the resource elements, indicated as R 302, 304, include DL reference signals (DL-RS). The DL-RS include Cell-specific RS (CRS) (also sometimes called common RS) 302 and UE-specific RS (UE-RS) 304. UE-RS 304 are transmitted only on the resource blocks upon which the corresponding physical DL shared channel (PDSCH) is mapped. The number of bits carried by each resource element depends on the modulation scheme. Thus, the more resource blocks that a UE receives and the higher the modulation scheme, the higher the data rate for the UE.

[0033] FIG. 4 is a diagram 400 illustrating an example of an UL frame structure in LTE. The available resource blocks for the UL may be partitioned into a data section and a control section. The control section may be formed at the two edges of the system bandwidth and may have a configurable size. The resource blocks in the control section may be assigned to UEs for transmission of control information. The data section may include all resource blocks not included in the control section. The UL frame structure results in the data section including contiguous subcarriers, which may allow a single UE to be assigned all of the contiguous subcarriers in the data section.

[0034] A UE may be assigned resource blocks 410a, 410b in the control section to transmit control information to an eNB. The UE may also be assigned resource

blocks 420a, 420b in the data section to transmit data to the eNB. The UE may transmit control information in a physical UL control channel (PUCCH) on the assigned resource blocks in the control section. The UE may transmit only data or both data and control information in a physical UL shared channel (PUSCH) on the assigned resource blocks in the data section. A UL transmission may span both slots of a subframe and may hop across frequency.

[0035] A set of resource blocks may be used to perform initial system access and achieve UL synchronization in a physical random access channel (PRACH) 430. The PRACH 430 carries a random sequence and cannot carry any UL data/signaling. Each random access preamble occupies a bandwidth corresponding to six consecutive resource blocks. The starting frequency is specified by the network. That is, the transmission of the random access preamble is restricted to certain time and frequency resources. There is no frequency hopping for the PRACH. The PRACH attempt is carried in a single subframe (1 ms) or in a sequence of few contiguous subframes and a UE can make only a single PRACH attempt per frame (10 ms).

[0036] FIG. 5 is a diagram 500 illustrating an example of a radio protocol architecture for the user and control planes in LTE. The radio protocol architecture for the UE and the eNB is shown with three layers: Layer 1, Layer 2, and Layer 3. Layer 1 (L1 layer) is the lowest layer and implements various physical layer signal processing functions. The L1 layer will be referred to herein as the physical layer 506. Layer 2 (L2 layer) 508 is above the physical layer 506 and is responsible for the link between the UE and eNB over the physical layer 506.

[0037] In the user plane, the L2 layer 508 includes a media access control (MAC) sublayer 510, a radio link control (RLC) sublayer 512, and a packet data convergence protocol (PDCP) 514 sublayer, which are terminated at the eNB on the network side. Although not shown, the UE may have several upper layers above the L2 layer 508 including a network layer (e.g., IP layer) that is terminated at the PDN gateway 118 on the network side, and an application layer that is terminated at the other end of the connection (e.g., far end UE, server, etc.).

[0038] The PDCP sublayer 514 provides multiplexing between different radio bearers and logical channels. The PDCP sublayer 514 also provides header compression for upper layer data packets to reduce radio transmission overhead, security by

ciphering the data packets, and handover support for UEs between eNBs. The RLC sublayer 512 provides segmentation and reassembly of upper layer data packets, retransmission of lost data packets, and reordering of data packets to compensate for out-of-order reception due to hybrid automatic repeat request (HARQ). The MAC sublayer 510 provides multiplexing between logical and transport channels. The MAC sublayer 510 is also responsible for allocating the various radio resources (e.g., resource blocks) in one cell among the UEs. The MAC sublayer 510 is also responsible for HARQ operations.

[0039] In the control plane, the radio protocol architecture for the UE and eNB is substantially the same for the physical layer 506 and the L2 layer 508 with the exception that there is no header compression function for the control plane. The control plane also includes a radio resource control (RRC) sublayer 516 in Layer 3 (L3 layer). The RRC sublayer 516 is responsible for obtaining radio resources (i.e., radio bearers) and for configuring the lower layers using RRC signaling between the eNB and the UE.

[0040] FIG. 6 is a block diagram of an eNB 610 in communication with a UE 650 in an access network. In the DL, upper layer packets from the core network are provided to a controller/processor 675. The controller/processor 675 implements the functionality of the L2 layer. In the DL, the controller/processor 675 provides header compression, ciphering, packet segmentation and reordering, multiplexing between logical and transport channels, and radio resource allocations to the UE 650 based on various priority metrics. The controller/processor 675 is also responsible for HARQ operations, retransmission of lost packets, and signaling to the UE 650.

[0041] The transmit (TX) processor 616 implements various signal processing functions for the L1 layer (i.e., physical layer). The signal processing functions include coding and interleaving to facilitate forward error correction (FEC) at the UE 650 and mapping to signal constellations based on various modulation schemes (e.g., binary phase-shift keying (BPSK), quadrature phase-shift keying (QPSK), M-phase-shift keying (M-PSK), M-quadrature amplitude modulation (M-QAM)). The coded and modulated symbols are then split into parallel streams. Each stream is then mapped to an OFDM subcarrier, multiplexed with a reference signal (e.g., pilot) in the time and/or frequency domain, and then combined together using an Inverse Fast Fourier Transform (IFFT) to produce a physical channel carrying a time domain

OFDM symbol stream. The OFDM stream is spatially precoded to produce multiple spatial streams. Channel estimates from a channel estimator 674 may be used to determine the coding and modulation scheme, as well as for spatial processing. The channel estimate may be derived from a reference signal and/or channel condition feedback transmitted by the UE 650. Each spatial stream is then provided to a different antenna 620 via a separate transmitter 618TX. Each transmitter 618TX modulates an RF carrier with a respective spatial stream for transmission.

[0042] At the UE 650, each receiver 654RX receives a signal through its respective antenna 652. Each receiver 654RX recovers information modulated onto an RF carrier and provides the information to the receive (RX) processor 656. The RX processor 656 implements various signal processing functions of the L1 layer. The RX processor 656 performs spatial processing on the information to recover any spatial streams destined for the UE 650. If multiple spatial streams are destined for the UE 650, they may be combined by the RX processor 656 into a single OFDM symbol stream. The RX processor 656 then converts the OFDM symbol stream from the time-domain to the frequency domain using a Fast Fourier Transform (FFT). The frequency domain signal comprises a separate OFDM symbol stream for each subcarrier of the OFDM signal. The symbols on each subcarrier, and the reference signal, are recovered and demodulated by determining the most likely signal constellation points transmitted by the eNB 610. These soft decisions may be based on channel estimates computed by the channel estimator 658. The soft decisions are then decoded and deinterleaved to recover the data and control signals that were originally transmitted by the eNB 610 on the physical channel. The data and control signals are then provided to the controller/processor 659.

[0043] The controller/processor 659 implements the L2 layer. The controller/processor can be associated with a memory 660 that stores program codes and data. The memory 660 may be referred to as a computer-readable medium. In the UL, the controller/processor 659 provides demultiplexing between transport and logical channels, packet reassembly, deciphering, header decompression, control signal processing to recover upper layer packets from the core network. The upper layer packets are then provided to a data sink 662, which represents all the protocol layers above the L2 layer. Various control signals may also be provided to the data sink 662 for L3 processing. The controller/processor 659 is also responsible for error

detection using an acknowledgement (ACK) and/or negative acknowledgement (NACK) protocol to support HARQ operations.

[0044] In the UL, a data source 667 is used to provide upper layer packets to the controller/processor 659. The data source 667 represents all protocol layers above the L2 layer. Similar to the functionality described in connection with the DL transmission by the eNB 610, the controller/processor 659 implements the L2 layer for the user plane and the control plane by providing header compression, ciphering, packet segmentation and reordering, and multiplexing between logical and transport channels based on radio resource allocations by the eNB 610. The controller/processor 659 is also responsible for HARQ operations, retransmission of lost packets, and signaling to the eNB 610.

[0045] Channel estimates derived by a channel estimator 658 from a reference signal or feedback transmitted by the eNB 610 may be used by the TX processor 668 to select the appropriate coding and modulation schemes, and to facilitate spatial processing. The spatial streams generated by the TX processor 668 are provided to different antenna 652 via separate transmitters 654TX. Each transmitter 654TX modulates an RF carrier with a respective spatial stream for transmission.

[0046] The UL transmission is processed at the eNB 610 in a manner similar to that described in connection with the receiver function at the UE 650. Each receiver 618RX receives a signal through its respective antenna 620. Each receiver 618RX recovers information modulated onto an RF carrier and provides the information to a RX processor 670. The RX processor 670 may implement the L1 layer.

[0047] The controller/processor 675 implements the L2 layer. The controller/processor 675 can be associated with a memory 676 that stores program codes and data. The memory 676 may be referred to as a computer-readable medium. In the UL, the control/processor 675 provides demultiplexing between transport and logical channels, packet reassembly, deciphering, header decompression, control signal processing to recover upper layer packets from the UE 650. Upper layer packets from the controller/processor 675 may be provided to the core network. The controller/processor 675 is also responsible for error detection using an ACK and/or NACK protocol to support HARQ operations.

[0048] Interference alignment schemes have been developed to mitigate interference. The interference alignment schemes often require global channel state information

(CSI), making it difficult to implement the schemes in practice. An opportunistic interference alignment scheme on the downlink can be more easily implemented in practice by taking advantage of the presence of many UEs being served by a BS. There is currently a need for an interference alignment scheme for the uplink.

[0049] FIG. 7 is a first diagram 700 for illustrating exemplary methods. As shown in FIG. 7, the serving BS BS₁ sends to each of the UEs UE₁, UE₂, UE₃ being served by the BS₁ information 702 indicating receive direction vectors that the BS₁, BS₂, and BS₃ will use to process received uplink transmissions. The information 702 may indicate the receive direction vectors to be used in sequence by the BS₁ and the neighboring BSs BS₂ and BS₃. As such, each of the UEs UE₁, UE₂, UE₃ receives information indicating a receive direction vector $v_{R1}(t)$ for the BS₁, a receive direction vector $v_{R2}(t)$ for the BS₂, and a receive direction vector $v_{R3}(t)$ for the BS₃. The receive direction vector $v_{R1}(t)$ is an ideal direction vector for the BS₁ for receiving an uplink transmission from the UEs UE₁, UE₂, and UE₃ that allows the BS₁ to cancel interference due to uplink transmissions from UEs served by the BS₂ and BS₃. The receive direction vector $v_{R2}(t)$ is an ideal direction vector for the BS₂ for receiving an uplink transmission from UEs served by the BS₂ that allows the BS₂ to cancel interference due to uplink transmissions from UEs served by the BS₃ and from the UEs UE₁, UE₂, UE₃ served by the BS₁. The receive direction vector $v_{R3}(t)$ is an ideal direction vector for the BS₃ for receiving an uplink transmission from UEs served by the BS₃ that allows the BS₃ to cancel interference due to uplink transmissions from UEs served by the BS₂ and from the UEs UE₁, UE₂, UE₃ served by the BS₁.

[0050] Each of the UEs UE₁, UE₂, UE₃ determines a channel between the UE and the BS₁ and a channel between the UE and each of the neighboring BSs BS₂ and BS₃. As such, the UE₁ determines the channel $H_{1,1}$ between the UE₁ and the BS₁, the channel $H_{1,2}$ between the UE₁ and the BS₂, and the channel $H_{1,3}$ between the UE₁ and the BS₃. Each of the UEs UE₁, UE₂, UE₃ then determines a transmit direction vector to apply to modulated data symbols for mapping to a set of resource blocks for an uplink transmission. The UE₁ determines the transmit direction vector $v_{T1}(t)$, the UE₂ determines the transmit direction vector $v_{T2}(t)$, and the UE₃ determines the transmit direction vector $v_{T3}(t)$. The transmit direction vectors are determined based on the determined channels and the receive direction vectors. The determined

transmit direction vector may also be a function of a quality metric M. The UEs may attempt to minimize an error to the serving BS and an interference caused to neighboring BSs by the uplink transmission by minimizing the quality metric M. The determined transmit direction vector may be less than optimum for the serving BS so as to reduce the interference caused to neighboring BSs by the uplink transmission. With respect to the UE₁, the UE₁ determines its transmit direction vector $v_{T1}(t)$ as a function of $H_{1,1}$, $H_{1,2}$, $H_{1,3}$, $v_{R1}(t)$, $v_{R2}(t)$, and $v_{R3}(t)$. The UE₁ may also determine $v_{T1}(t)$ as a function of the quality metric M.

[0051] The transmit direction vectors are applied to modulated data symbols for mapping to a set of resource blocks for an uplink transmission. The transmit direction vectors have N dimensions greater than or equal to two. The N dimensions may be antenna dimensions (MIMO) and/or frequency dimensions (e.g., resource blocks in OFDM). Each dimension may modify modulated data symbols in amplitude and/or phase. For frequency dimensions, the same modulated data symbol is duplicated N times (e.g., 2 times) (and therefore data redundancy is created) and an N-dimensional transmit direction vector is applied to the N modulated data symbols when mapping the N modulated data symbols to N resource elements. Each of the N modulated data symbols in the N resource elements may have a different amplitude and/or phase due to the application of the transmit direction vector. For antenna dimensions, the modulated data symbols are duplicated not through a mapping of modulated data symbols onto resource blocks/elements, but through the transmission of the same modulated data symbols through a plurality of transmit antennas. Each of the N sets of transmit antennas modifies an amplitude and/or phase based on the transmit direction vector.

[0052] Each of the UEs UE₁, UE₂, UE₃ determines an interference that would be caused to the BS₂ and the BS₃ by the uplink transmission based on the transmit direction vector, the channels between the UEs and the neighboring BSs, and the receive direction vectors of the neighboring BSs. Accordingly, the UE₁ determines the interference that would be caused to the BS₂ by an uplink transmission as a function of the transmit direction vector $v_{T1}(t)$, the receive direction vector $v_{R2}(t)$, and the channel $H_{1,2}$, and determines the interference that would be caused to the BS₃ by the uplink transmission as a function of the transmit direction vector $v_{T1}(t)$, the receive direction vector $v_{R3}(t)$, and the channel $H_{1,3}$. Each of the UEs then transmits

information 704 to the BS₁ indicating the interference. The UEs may transmit the information 704 through the quality metric M, which contains information indicating the interference that would be caused to the BS₂ and the BS₃ by the uplink transmission.

[0053] Each of the UEs UE₁, UE₂, UE₃ may also determine a receive signal power of the uplink transmission based on the transmit direction vector, the channel between the UEs and the serving BS, and the receive direction vector of the serving BS. Accordingly, the UE₁ determines the receive signal power of the uplink transmission as a function of the transmit direction vector $v_{T1}(t)$, the receive direction vector $v_{R1}(t)$, and the channel $H_{1,1}$. Each of the UEs may then transmit information 704 to the BS₁ indicating the receive signal power. The UEs may transmit the information 704 through the quality metric M, which contains information indicating the signal power at which the BS₁ would receive the uplink transmission.

[0054] For example, the UE₁ may determine the receive signal power S as $S = v_{R1}^T H_{1,1} v_{T1}$ (v_{R1}^T is the transpose of v_{R1}), the interference I_2 to the BS₂ as $I_2 = v_{R2}^T H_{1,2} v_{T1}$, and the interference I_3 to the BS₃ as $I_3 = v_{R3}^T H_{1,3} v_{T1}$. An error E at the BS₁ may be determined as $E = (1 - S)^2 = (1 - v_{R1}^T H_{1,1} v_{T1})^2$. The quality metric M(v_{T1}) may be equal to $E + (I_2)^2 + (I_3)^2$. The UE₁ may determine the v_{T1}^* that minimizes M(v_{T1}). The UE₁ may then report back to the BS₁ one or more of the receive signal power S(v_{T1}^*) of the uplink transmission, the interference $I_2(v_{T1}^*)$ to the BS₂, the interference $I_3(v_{T1}^*)$ to the BS₃, and value of the quality metric M(v_{T1}^*) (which is a function of the determined transmit direction vector v_{T1}^*).

[0055] The BS₁ receives the transmitted information 704 from each of the UEs UE₁, UE₂, and UE₃, and opportunistically schedules (selects) one of the UEs for the opportunistic interference alignment uplink transmission based on the received information. The BS₁ may select the UE that can provide the least error and the least interference based on the quality metric M. The BS₁ may also base its selection of the UE on other factors, such as how often the UE has been scheduled with the opportunistic interference alignment uplink transmission, how long ago the UE has been scheduled with the opportunistic interference alignment uplink transmission, how much data the UE has to transmit to the BS₁, the information provided by other UEs, etc.

[0056] The BS₁ then sends an indication 706 to the UE indicating to the UE that the UE was selected for the uplink transmission. Assume the BS₁ selects the UE₁ for the uplink transmission. The UE₁ receives the indication 706 of the selection for the uplink transmission from the BS₁. Subsequently, at the scheduled time, the UE₁ transmits the set of resource blocks 708 in the uplink transmission to the BS₁ using the transmit direction vector v_{T1}^* . The transmission has N dimensions (e.g., 2), which may be frequency dimensions or antenna dimensions.

[0057] Each of the UEs UE₁, UE₂, UE₃ may determine the channels between the UE and the neighboring BSs based on downlink pilot signals received from each of the neighboring BSs. Accordingly, each UE may receive downlink pilot signals from the neighboring BSs, determine a channel between the neighboring BSs and the UE, and assume the channel between the UE and each of the neighboring BSs is equal to the channel between the neighboring BSs and the UE. For example, the UE₁ may receive downlink pilot signals from the BS₂, determine a downlink channel $H_{BS2,UE1}$ between the BS₂ and the UE₁, and assume the uplink channel $H_{1,2}$ between the UE₁ and the BS₂ is equal to the downlink channel $H_{BS2,UE1}$ (i.e., $H_{1,2}=H_{BS2,UE1}$). Similarly, the UE₁ may receive downlink pilot signals from the BS₃, determine a downlink channel $H_{BS3,UE1}$ between the BS₃ and the UE₁, and assume the uplink channel $H_{1,3}$ between the UE₁ and the BS₃ is equal to the downlink channel $H_{BS3,UE1}$ (i.e., $H_{1,3}=H_{BS3,UE1}$). The UEs may make the assumption that the uplink channel is the same as the downlink channel only in TDD systems. Alternatively, each of the UEs UE₁, UE₂, UE₃ may determine the channels between the UE and the neighboring BSs based on channel feedback received from the serving BS. Each of the UEs may transmit an uplink pilot signal to the serving BS; the neighboring BSs may receive the uplink pilot signal, compute channel feedback, and provide the channel feedback to the serving BS; and the serving BS may provide the received channel feedback to the corresponding UE. Each of the UEs may then determine the channels between the UE and the interfering BSs based on the received channel feedback. For example, the UE₁ may transmit uplink pilot signals to the BS₁. The BS₂ may receive the uplink pilot signals, compute channel feedback based on the received uplink pilot signals, and provide the channel feedback to the BS₁. The BS₁ may provide the received channel feedback to the UE₁, which subsequently computes the channel $H_{1,2}$ based on the received channel feedback.

[0058] Each of the UEs UE₁, UE₂, UE₃ may determine the channel between the UE and the serving BS based on downlink pilot signals received from the serving BS. Accordingly, each UE may receive downlink pilot signals from the serving BS, determine a channel between the serving BS and the UE, and assume the channel between the UE and the serving BS is equal to the channel between the serving BS and the UE. For example, the UE₁ may receive downlink pilot signals from the BS₁, determine a downlink channel H_{BS1,UE1} between the BS₁ and the UE₁, and assume the uplink channel H_{1,1} between the UE₁ and the BS₁ is equal to the downlink channel H_{BS1,UE1} (i.e., H_{1,1}=H_{BS1,UE1}). The UEs may make the assumption that the uplink channel is the same as the downlink channel only in TDD systems. Alternatively, each of the UEs UE₁, UE₂, UE₃ may determine the channel between the UE and the serving BS based on channel feedback received from the serving BS. Each of the UEs may transmit an uplink pilot signal to the serving BS, and receive channel feedback from the serving BS based on the uplink pilot signal. Each of the UEs may then determine the channel between the UE and the serving BS based on the received channel feedback. For example, the UE₁ may transmit uplink pilot signals to the BS₁, receive channel feedback based on the uplink pilot signals, and determine the channel H_{1,1} based on the received channel feedback.

[0059] FIG. 8 is a second diagram 800 for illustrating exemplary methods. As shown in FIG. 8, each of the BSs BS₁, BS₂, BS₃ may synchronously change the direction vectors each subframe/slot. The direction vectors may be predetermined and known *a priori* by each of the BSs. The direction vectors may be based on different pseudo-random sequences or seeds and may hop around to different values. The direction vectors may be dependent on an identifier of the BS, subcarriers of the utilized resource blocks, or a corresponding subframe and/or system frame number. When a direction vector depends on the subframe and/or on a system frame number, the direction vector may be said to be time-varying.

[0060] FIG. 9 is a third diagram 900 for illustrating exemplary methods. FIG. 9 specifically illustrates the phase rotation of a modulated data symbol. As discussed *supra*, a UE selected for an uplink transmission applies a direction vector to modulated data symbols before transmitting (frequency dimensions) the modulated data symbols or when transmitting (antenna dimensions) the modulated data symbols. The direction vector modifies an amplitude and/or a phase of the

modulated data symbols. Assume that the number of dimensions is two (i.e., $N=2$). Accordingly, with respect to the UE₁, $v_{T1}(t)=[v_1(t) \ v_2(t)]$, where $v_1(t)=A_1e^{j\Theta_1}$ and $v_2(t)=A_2e^{j\Theta_2}$. Assume also that the direction vector $v_{T1}(t)$ modifies the modulated data symbols in phase only (i.e., $A_1=1$ and $A_2=1$). Further, assume the UE₁ modulates the data using QPSK. The diagram 900 illustrates possible QPSK values. As shown in the diagram 950, if the UE₁ applies a phase rotation to the QPSK value 11, the UE₁ may rotate a phase of the modulated symbol by Θ . In a frequency dimension configuration, the UE₁ duplicates the data by mapping the same data to both a first set of resource blocks/elements and a second set of resource blocks/elements. The UE₁ applies a first phase rotation Θ_1 to modulated data symbols in the first set of resource blocks/elements and a second phase rotation Θ_2 to modulated data symbols in the second set of resource blocks/elements. In an antenna dimension configuration, the UE₁ duplicates the modulated data symbols not through a mapping of modulated data symbols onto resource blocks/elements, but through the transmission of the same modulated data symbols through a plurality of transmit antennas. A first set of transmit antennas applies a first phase rotation Θ_1 to the modulated data symbols and a second set of transmit antennas applies a second phase rotation Θ_2 to the modulated data symbols.

[0061] FIG. 10A is a fourth diagram 1000 for illustrating exemplary methods. When applying frequency dimensions, the UEs UE₁, UE₂, UE₃ map the same modulated data symbols to both a first set of resource blocks/elements and to a second set of resource blocks/elements. For FIG. 10A, assume that the UEs UE₁, UE₂, UE₃ map the same modulated data symbols to different sets of resource blocks (i.e., the granularity is resource blocks and not resource elements). Accordingly, the UE₁ may map the same modulated data symbols to a first set of resource blocks 1002 and to a second set of resource blocks 1004. The UE₁ applies the direction vector $v_{T1}(t)$ to the modulated data symbols in the first set of resource blocks 1002 and the second set of resource blocks 1004, which results in the modulated data symbols in the first set of resource blocks and the second set of resource blocks being modified in amplitude and/or phase as shown by the arrows 1012, 1014.

[0062] FIG. 10B is a fifth diagram 1050 for illustrating exemplary methods. When applying antenna dimensions, the UEs UE₁, UE₂, UE₃ map modulated data symbols to a set of resource blocks/elements and transmit the same set of resource

blocks/elements using a different set of transmit antennas to apply the direction vector on the modulated data symbols. Accordingly, the UE₁ may map modulated data symbols to a set of resource blocks 1052 and transmit the set of resource blocks 1052 through different transmit antennas so as to modify an amplitude and/or a phase of the modulated data symbols based on the direction vector $v_{T1}(t)$ as shown by the arrows 1062, 1072.

[0063] FIG. 11 is a flow chart 1100 of a method of wireless communication. The method may be performed by a UE. As shown in FIG. 11, in step 1102, a UE receives information indicating a receive direction vector for a serving BS and a set of receive direction vectors for at least one interfering BS. In step 1102, the received information may indicate the receive direction vector to be used in sequence by the serving BS and the set of receive direction vectors to be used in sequence by the at least one interfering BS. In step 1104, the UE determines a channel between the UE and the serving BS. In step 1106, the UE determines a set of channels between the UE and each of the at least one interfering BS. In step 1108, the UE determines a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors. Each modulated symbol of the modulated symbols is mapped to a plurality of resource blocks of the set of resource blocks. In step 1110, the UE determines an interference caused to the at least one interfering BS by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors. In step 1110, the UE may also determine a receive signal power of the uplink transmission based on the transmit direction vector. In step 1112, the UE transmits information indicating the interference to the serving BS. In step 1112, the UE may also transmit information indicating the receive signal power to the serving BS. In step 1114, the UE may receive an indication of a selection for the uplink transmission from the serving BS. The indication may be based on the transmitted information. In step 1116, the UE may transmit the set of resource blocks in the uplink transmission to the serving BS using the transmit direction vector.

[0064] The UE may receive a downlink pilot signal from the serving BS and determine the channel based on the received downlink pilot signal (e.g., in TDD systems). The

UE may receive a set of downlink pilot signals from the at least one interfering BS and determine the set of channels based on the received set of downlink pilot signals (e.g., in TDD systems). The UE may transmit an uplink pilot signal to the serving BS, receive channel feedback from the serving BS based on the uplink pilot signal, and determine the channel based on the channel feedback. The UE may also determine the set of channels based on the channel feedback. As such, the channel feedback may include channel feedback from the at least one interfering BS, which provided the channel feedback to the serving BS.

[0065] For example, referring to FIG. 7, the UE₁ receives information indicating a receive direction vector $v_{R1}(t)$ for the serving BS BS₁ and a set of receive direction vectors $v_{R2}(t)$ and $v_{R3}(t)$ for at least one interfering BS BS₂ and BS₃. The UE₁ determines a channel H_{1,1} between the UE₁ and the serving BS BS₁. The UE₁ determines a set of channels H_{1,2} and H_{1,3} between the UE₁ and each of the at least one interfering BS BS₂ and BS₃. The UE₁ determines a transmit direction vector v_{T1}^* to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel H_{1,1}, the set of channels H_{1,2} and H_{1,3}, the receive direction vector $v_{R1}(t)$, and the set of receive direction vectors $v_{R2}(t)$ and $v_{R3}(t)$. Each modulated symbol of the modulated symbols is mapped to a plurality of resource blocks of the set of resource blocks (see FIGs. 10A, 10B). The UE₁ determines an interference I_2 and I_3 caused to the at least one interfering BS BS₂ and BS₃ by the uplink transmission based on the transmit direction vector v_{T1}^* , the set of channels H_{1,2} and H_{1,3}, and the set of receive direction vectors $v_{R2}(t)$ and $v_{R3}(t)$. The UE may also determine a receive signal power S of the uplink transmission based on the transmit direction vector v_{T1}^* . The UE₁ transmits information indicating the interference (e.g., I_2 and I_3 and/or $M(v_{T1}^*)$) to the serving BS BS₁. The UE₁ may also transmit information indicating the receive signal power (e.g., S and/or $M(v_{T1}^*)$) to the serving BS BS₁. If the UE₁ is selected for the uplink transmission, the UE₁ receives an indication of a selection for the uplink transmission from the serving BS BS₁. The indication may be based on the transmitted information. The UE₁ may then transmit the set of resource blocks in the uplink transmission to the serving BS BS₁ using the transmit direction vector v_{T1}^* .

[0066] FIG. 12 is a conceptual data flow diagram 1200 illustrating the data flow between different modules/means/components in an exemplary apparatus 1202. The

apparatus may be a UE. The apparatus includes a receiving module 1204 that is configured to receive information indicating a receive direction vector for a serving BS and a set of receive direction vectors for at least one interfering BS. The received information may indicate the receive direction vector to be used in sequence by the serving BS and the set of receive direction vectors to be used in sequence by the at least one interfering BS. The apparatus further includes a channel determination module 1206 that is configured to determine a channel between the UE and the serving BS. The channel determination module 1206 is further configured to determine a set of channels between the UE and each of the at least one interfering BS. The apparatus further includes a direction vector determination module 1208 that is configured to determine a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors. Each modulated symbol of the modulated symbols is mapped to a plurality of resource blocks of the set of resource blocks. The apparatus further includes an interference and signal power determination module 1212 that is configured to determine an interference caused to the at least one interfering BS by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors. The apparatus further includes a transmission module 1210 that is configured to transmit information indicating the interference to the serving BS.

[0067] The receiving module 1204 may be configured to receive an indication of a selection for the uplink transmission from the serving BS, the indication being based on the transmitted information. The transmission module 1210 may be configured to transmit the set of resource blocks in the uplink transmission to the serving BS using the transmit direction vector. The interference and signal power determination module 1212 may be further configured to determine a receive signal power of the uplink transmission based on the transmit direction vector. The transmission module 1210 may be further configured to transmit information indicating the receive signal power to the serving BS. The receiving module 1204 may be further configured to receive a downlink pilot signal from the serving BS. The channel determination module 1206 may be configured to determine the channel based on the received downlink pilot signal (e.g., in TDD systems). The receiving module

1204 may be configured to receive a set of downlink pilot signals from the at least one interfering BS. The channel determination module 1206 may be configured to determine the set of channels based on the received set of downlink pilot signals (e.g., in TDD systems). The transmission module 1210 may be configured to transmit an uplink pilot signal to the serving BS. The receiving module 1204 may be configured to receive channel feedback from the serving BS based on the uplink pilot signal. The channel determination module 1206 may be configured to determine the channel based on the channel feedback. The channel determination module 1206 may be further configured to determine the set of channels based on the channel feedback, assuming the interfering BSs received the uplink pilot signal and provided the channel feedback to the serving BS, which then provided the channel feedback to the UE.

[0068] The apparatus may include additional modules that perform each of the steps of the algorithm in the aforementioned flow chart of FIG. 11. As such, each step in the aforementioned flow chart of FIG. 11 may be performed by a module and the apparatus may include one or more of those modules. The modules may be one or more hardware components specifically configured to carry out the stated processes/algorithm, implemented by a processor configured to perform the stated processes/algorithm, stored within a computer-readable medium for implementation by a processor, or some combination thereof.

[0069] FIG. 13 is a diagram 1300 illustrating an example of a hardware implementation for an apparatus 1302' employing a processing system 1314. The processing system 1314 may be implemented with a bus architecture, represented generally by the bus 1324. The bus 1324 may include any number of interconnecting buses and bridges depending on the specific application of the processing system 1314 and the overall design constraints. The bus 1324 links together various circuits including one or more processors and/or hardware modules, represented by the processor 1304, the modules 1204, 1206, 1208, 1210, 1212 and the computer-readable medium 1306. The bus 1324 may also link various other circuits such as timing sources, peripherals, voltage regulators, and power management circuits, which are well known in the art, and therefore, will not be described any further.

[0070] The processing system 1314 may be coupled to a transceiver 1310. The transceiver 1310 is coupled to one or more antennas 1320. The transceiver 1310

provides a means for communicating with various other apparatus over a transmission medium. The transceiver 1310 receives a signal from the one or more antennas 1320, extracts information from the received signal, and provides the extracted information to the processing system 1314, specifically the receiving module 1304. In addition, the transceiver 1310 receives information from the processing system 1314, specifically the transmission module 1310, and based on the received information, generates a signal to be applied to the one or more antennas 1320. The processing system 1314 includes a processor 1304 coupled to a computer-readable medium 1306. The processor 1304 is responsible for general processing, including the execution of software stored on the computer-readable medium 1306. The software, when executed by the processor 1304, causes the processing system 1314 to perform the various functions described *supra* for any particular apparatus. The computer-readable medium 1306 may also be used for storing data that is manipulated by the processor 1304 when executing software. The processing system further includes at least one of the modules 1204, 1206, 1208, 1210, and 1212. The modules may be software modules running in the processor 1304, resident/stored in the computer readable medium 1306, one or more hardware modules coupled to the processor 1304, or some combination thereof. The processing system 1314 may be a component of the UE 650 and may include the memory 660 and/or at least one of the TX processor 668, the RX processor 656, and the controller/processor 659.

[0071] In one configuration, the apparatus 1302/1302' for wireless communication is a UE and includes means for receiving information indicating a receive direction vector for a serving base station and a set of receive direction vectors for at least one interfering base station, means for determining a channel between the UE and the serving base station, means for determining a set of channels between the UE and each of the at least one interfering base station, and means for determining a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors. Each modulated symbol of the modulated symbols is mapped to a plurality of resource blocks of the set of resource blocks. The apparatus further includes means for determining an interference caused to the at least one interfering base station by the

uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors. The apparatus further includes means for transmitting information indicating the interference to the serving base station. The apparatus may further include means for receiving an indication of a selection for the uplink transmission from the serving base station. The indication is based on the transmitted information. The apparatus may further include means for transmitting the set of resource blocks in the uplink transmission to the serving base station using the transmit direction vector. The apparatus may further include means for determining a receive signal power of the uplink transmission based on the transmit direction vector, and means for transmitting information indicating the receive signal power to the serving base station. The apparatus may further include means for receiving a downlink pilot signal from the serving base station, wherein the channel is determined based on the received downlink pilot signal. The apparatus may further include means for receiving a set of downlink pilot signals from the at least one interfering base station. The set of channels is determined based on the received set of downlink pilot signals. The apparatus further includes means for transmitting an uplink pilot signal to the serving base station, and means for receiving channel feedback from the serving base station based on the uplink pilot signal. The channel is determined based on the channel feedback. As discussed *supra*, the set of channels may also be determined based on the channel feedback. The received information may indicate the receive direction vector to be used in sequence by the serving base station and the set of receive direction vectors to be used in sequence by the at least one interfering base station.

[0072] The aforementioned means may be one or more of the aforementioned modules of the apparatus 1202 and/or the processing system 1314 of the apparatus 1202' configured to perform the functions recited by the aforementioned means. As described *supra*, the processing system 1314 may include the TX Processor 668, the RX Processor 656, and the controller/processor 659. As such, in one configuration, the aforementioned means may be the TX Processor 668, the RX Processor 656, and the controller/processor 659 configured to perform the functions recited by the aforementioned means.

[0073] It is understood that the specific order or hierarchy of steps in the processes disclosed is an illustration of exemplary approaches. Based upon design

preferences, it is understood that the specific order or hierarchy of steps in the processes may be rearranged. Further, some steps may be combined or omitted. The accompanying method claims present elements of the various steps in a sample order, and are not meant to be limited to the specific order or hierarchy presented.

[0074] The previous description is provided to enable any person skilled in the art to practice the various aspects described herein. Various modifications to these aspects will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other aspects. Thus, the claims are not intended to be limited to the aspects shown herein, but is to be accorded the full scope consistent with the language claims, wherein reference to an element in the singular is not intended to mean “one and only one” unless specifically so stated, but rather “one or more.” Unless specifically stated otherwise, the term “some” refers to one or more. All structural and functional equivalents to the elements of the various aspects described throughout this disclosure that are known or later come to be known to those of ordinary skill in the art are expressly incorporated herein by reference and are intended to be encompassed by the claims. Moreover, nothing disclosed herein is intended to be dedicated to the public regardless of whether such disclosure is explicitly recited in the claims. No claim element is to be construed as a means plus function unless the element is expressly recited using the phrase “means for.”

WHAT IS CLAIMED IS:**CLAIMS**

1. A method of wireless communication of a user equipment (UE), comprising:
 - receiving information indicating a receive direction vector for a serving base station and a set of receive direction vectors for at least one interfering base station;
 - determining a channel between the UE and the serving base station;
 - determining a set of channels between the UE and each of the at least one interfering base station;
 - determining a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors, each modulated symbol of the modulated symbols being mapped to a plurality of resource blocks of the set of resource blocks;
 - determining an interference caused to the at least one interfering base station by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors; and
 - transmitting information indicating the interference to the serving base station.
2. The method of claim 1, further comprising:
 - receiving an indication of a selection for the uplink transmission from the serving base station, the indication being based on the transmitted information; and
 - transmitting the set of resource blocks in the uplink transmission to the serving base station using the transmit direction vector.
3. The method of claim 1, further comprising:
 - determining a receive signal power of the uplink transmission based on the transmit direction vector; and
 - transmitting information indicating the receive signal power to the serving base station.

4. The method of claim 1, further comprising receiving a downlink pilot signal from the serving base station, wherein the channel is determined based on the received downlink pilot signal.
5. The method of claim 1, further comprising receiving a set of downlink pilot signals from the at least one interfering base station, wherein the set of channels is determined based on the received set of downlink pilot signals.
6. The method of claim 1, further comprising:
 - transmitting an uplink pilot signal to the serving base station; and
 - receiving channel feedback from the serving base station based on the uplink pilot signal,wherein the channel is determined based on the channel feedback.
7. The method of claim 6, wherein the set of channels is determined based on the channel feedback.
8. The method of claim 1, wherein the received information indicates the receive direction vector to be used in sequence by the serving base station and the set of receive direction vectors to be used in sequence by the at least one interfering base station.
9. An apparatus for wireless communication, the apparatus being a user equipment (UE), comprising:
 - means for receiving information indicating a receive direction vector for a serving base station and a set of receive direction vectors for at least one interfering base station;
 - means for determining a channel between the UE and the serving base station;
 - means for determining a set of channels between the UE and each of the at least one interfering base station;
 - means for determining a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors,

each modulated symbol of the modulated symbols being mapped to a plurality of resource blocks of the set of resource blocks;

means for determining an interference caused to the at least one interfering base station by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors; and

means for transmitting information indicating the interference to the serving base station.

10. The apparatus of claim 9, further comprising:

means for receiving an indication of a selection for the uplink transmission from the serving base station, the indication being based on the transmitted information; and

means for transmitting the set of resource blocks in the uplink transmission to the serving base station using the transmit direction vector.

11. The apparatus of claim 9, further comprising:

means for determining a receive signal power of the uplink transmission based on the transmit direction vector; and

means for transmitting information indicating the receive signal power to the serving base station.

12. The apparatus of claim 9, further comprising means for receiving a downlink pilot signal from the serving base station, wherein the channel is determined based on the received downlink pilot signal.

13. The apparatus of claim 9, further comprising means for receiving a set of downlink pilot signals from the at least one interfering base station, wherein the set of channels is determined based on the received set of downlink pilot signals.

14. The apparatus of claim 9, further comprising:

means for transmitting an uplink pilot signal to the serving base station; and

means for receiving channel feedback from the serving base station based on the uplink pilot signal,

wherein the channel is determined based on the channel feedback.

15. The apparatus of claim 14, wherein the set of channels is determined based on the channel feedback.

16. The apparatus of claim 9, wherein the received information indicates the receive direction vector to be used in sequence by the serving base station and the set of receive direction vectors to be used in sequence by the at least one interfering base station.

17. An apparatus for wireless communication, the apparatus being a user equipment (UE), comprising:

a processing system configured to:

receive information indicating a receive direction vector for a serving base station and a set of receive direction vectors for at least one interfering base station;

determine a channel between the UE and the serving base station;

determine a set of channels between the UE and each of the at least one interfering base station;

determine a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors, each modulated symbol of the modulated symbols being mapped to a plurality of resource blocks of the set of resource blocks;

determine an interference caused to the at least one interfering base station by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors; and

transmit information indicating the interference to the serving base station.

18. The apparatus of claim 17, wherein the processing system is further configured to:

receive an indication of a selection for the uplink transmission from the serving base station, the indication being based on the transmitted information; and

transmit the set of resource blocks in the uplink transmission to the serving base station using the transmit direction vector.

19. The apparatus of claim 17, wherein the processing system is further configured to:

determine a receive signal power of the uplink transmission based on the transmit direction vector; and

transmit information indicating the receive signal power to the serving base station.

20. The apparatus of claim 17, wherein the processing system is further configured to receive a downlink pilot signal from the serving base station, wherein the channel is determined based on the received downlink pilot signal.

21. The apparatus of claim 17, wherein the processing system is further configured to receive a set of downlink pilot signals from the at least one interfering base station, wherein the set of channels is determined based on the received set of downlink pilot signals.

22. The apparatus of claim 17, wherein the processing system is further configured to:

transmit an uplink pilot signal to the serving base station; and

receive channel feedback from the serving base station based on the uplink pilot signal,

wherein the channel is determined based on the channel feedback.

23. The apparatus of claim 22, wherein the set of channels is determined based on the channel feedback.

24. The apparatus of claim 17, wherein the received information indicates the receive direction vector to be used in sequence by the serving base station and the set of receive direction vectors to be used in sequence by the at least one interfering base station.

25. A computer program product, comprising:

a computer-readable medium comprising code for:

receiving information indicating a receive direction vector for a serving base station and a set of receive direction vectors for at least one interfering base station;

determining a channel between the UE and the serving base station;

determining a set of channels between the UE and each of the at least one interfering base station;

determining a transmit direction vector to apply to modulated symbols for mapping to a set of resource blocks for an uplink transmission based on the channel, the set of channels, the receive direction vector, and the set of receive direction vectors, each modulated symbol of the modulated symbols being mapped to a plurality of resource blocks of the set of resource blocks;

determining an interference caused to the at least one interfering base station by the uplink transmission based on the transmit direction vector, the set of channels, and the set of receive direction vectors; and

transmitting information indicating the interference to the serving base station.

26. The computer program product of claim 25, wherein the computer-readable medium further comprises code for:

receiving an indication of a selection for the uplink transmission from the serving base station, the indication being based on the transmitted information; and

transmitting the set of resource blocks in the uplink transmission to the serving base station using the transmit direction vector.

27. The computer program product of claim 25, wherein the computer-readable medium further comprises code for:

determining a receive signal power of the uplink transmission based on the transmit direction vector; and

transmitting information indicating the receive signal power to the serving base station.

28. The computer program product of claim 25, wherein the computer-readable medium further comprises code for receiving a downlink pilot signal from the serving base station, wherein the channel is determined based on the received downlink pilot signal.

29. The computer program product of claim 25, wherein the computer-readable medium further comprises code for receiving a set of downlink pilot signals from the at least one

interfering base station, wherein the set of channels is determined based on the received set of downlink pilot signals.

30. The computer program product of claim 25, wherein the computer-readable medium further comprises code for:

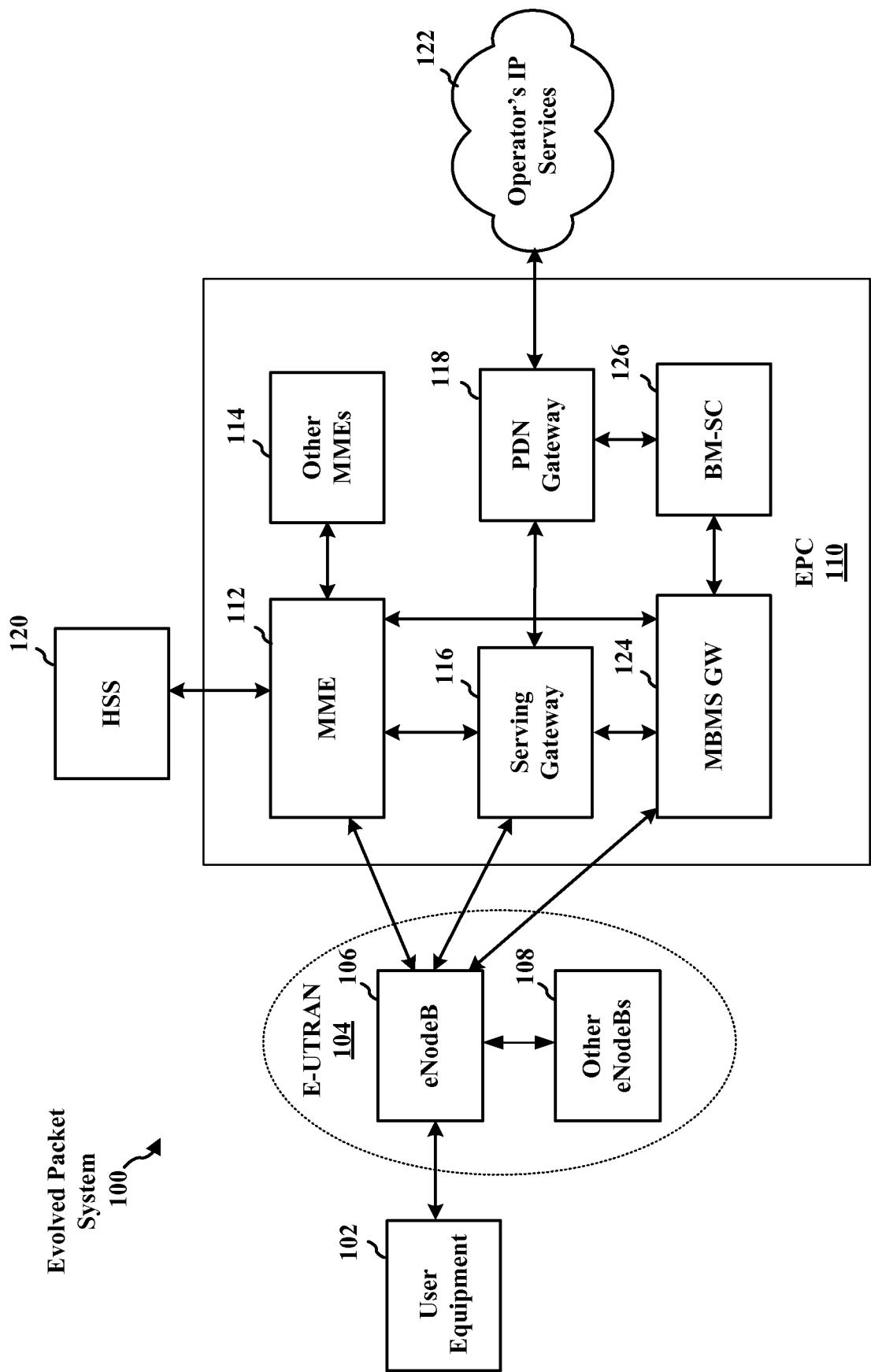
transmitting an uplink pilot signal to the serving base station; and

receiving channel feedback from the serving base station based on the uplink pilot signal,

wherein the channel is determined based on the channel feedback.

31. The computer program product of claim 30, wherein the set of channels is determined based on the channel feedback.

32. The computer program product of claim 25, wherein the received information indicates the receive direction vector to be used in sequence by the serving base station and the set of receive direction vectors to be used in sequence by the at least one interfering base station.

**FIG. 1**

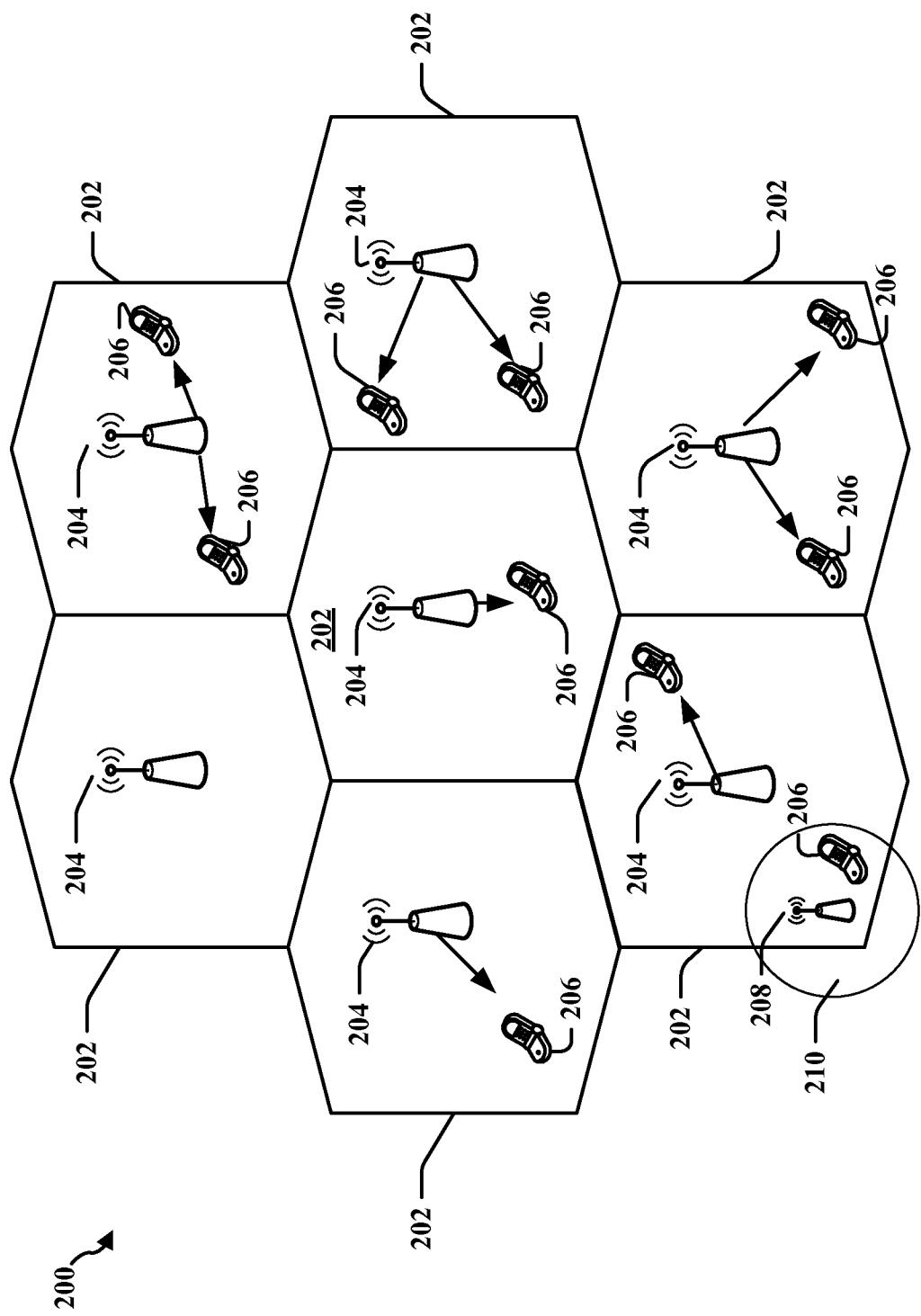


FIG. 2

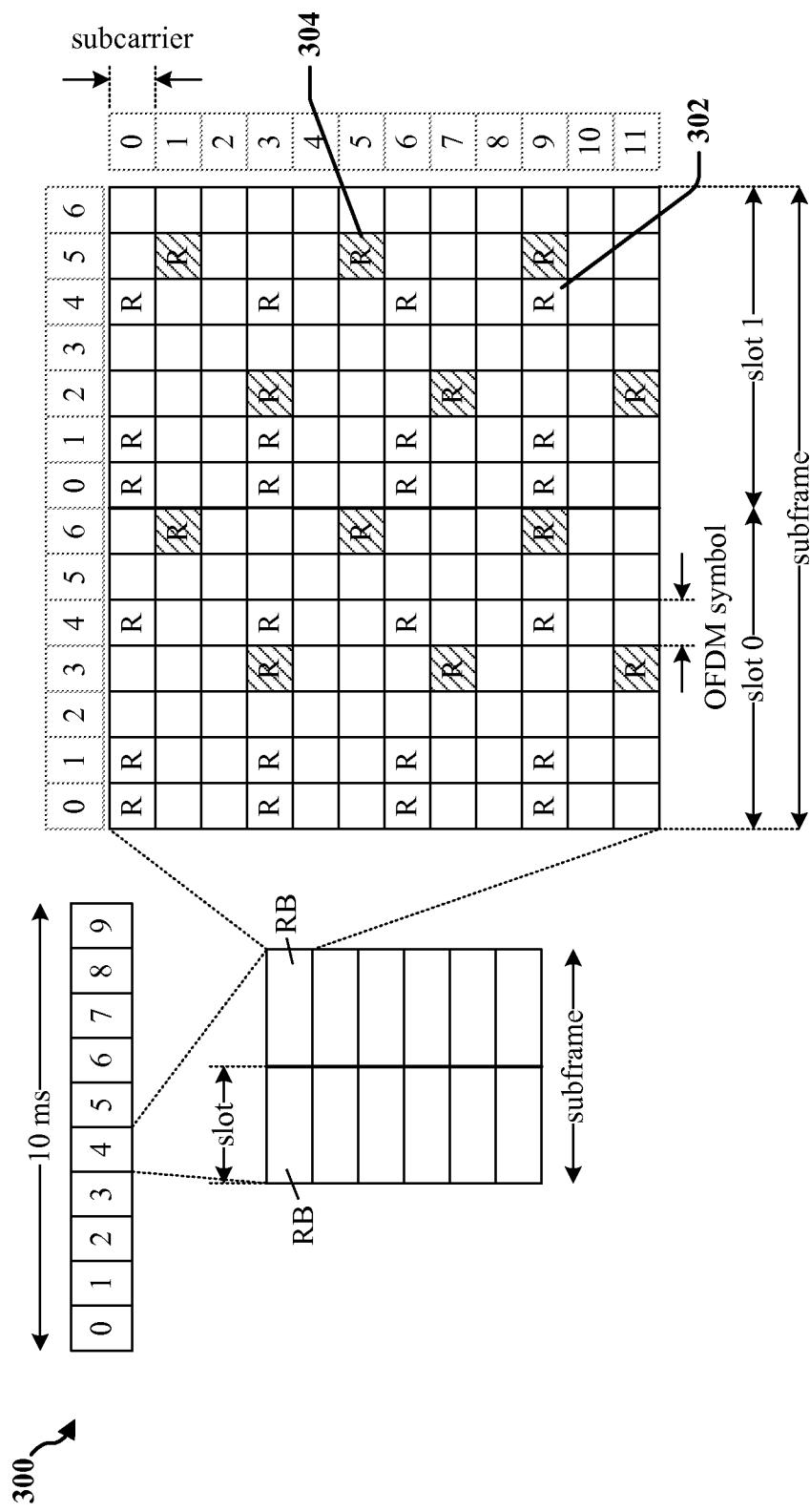
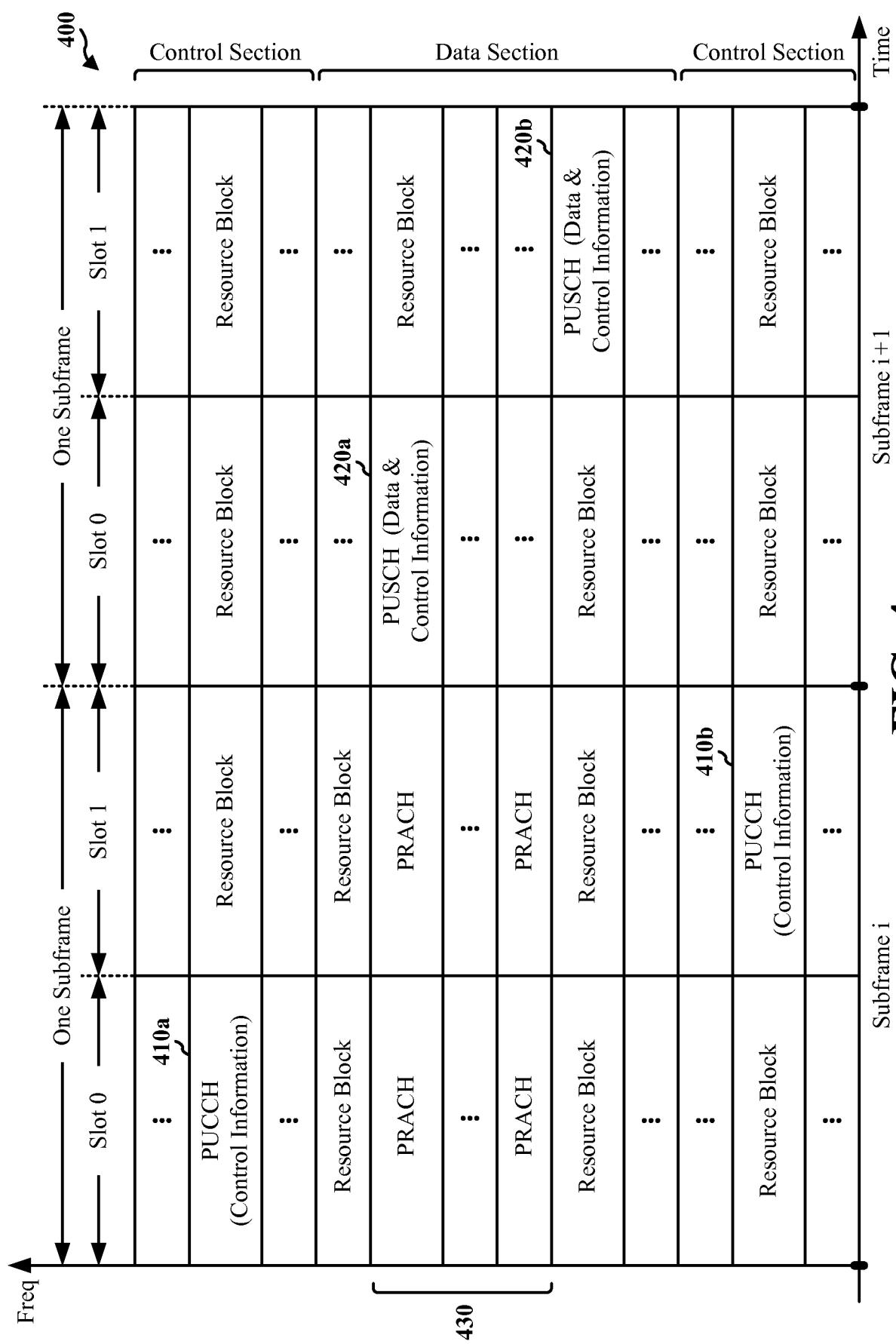


FIG. 3

**FIG. 4**

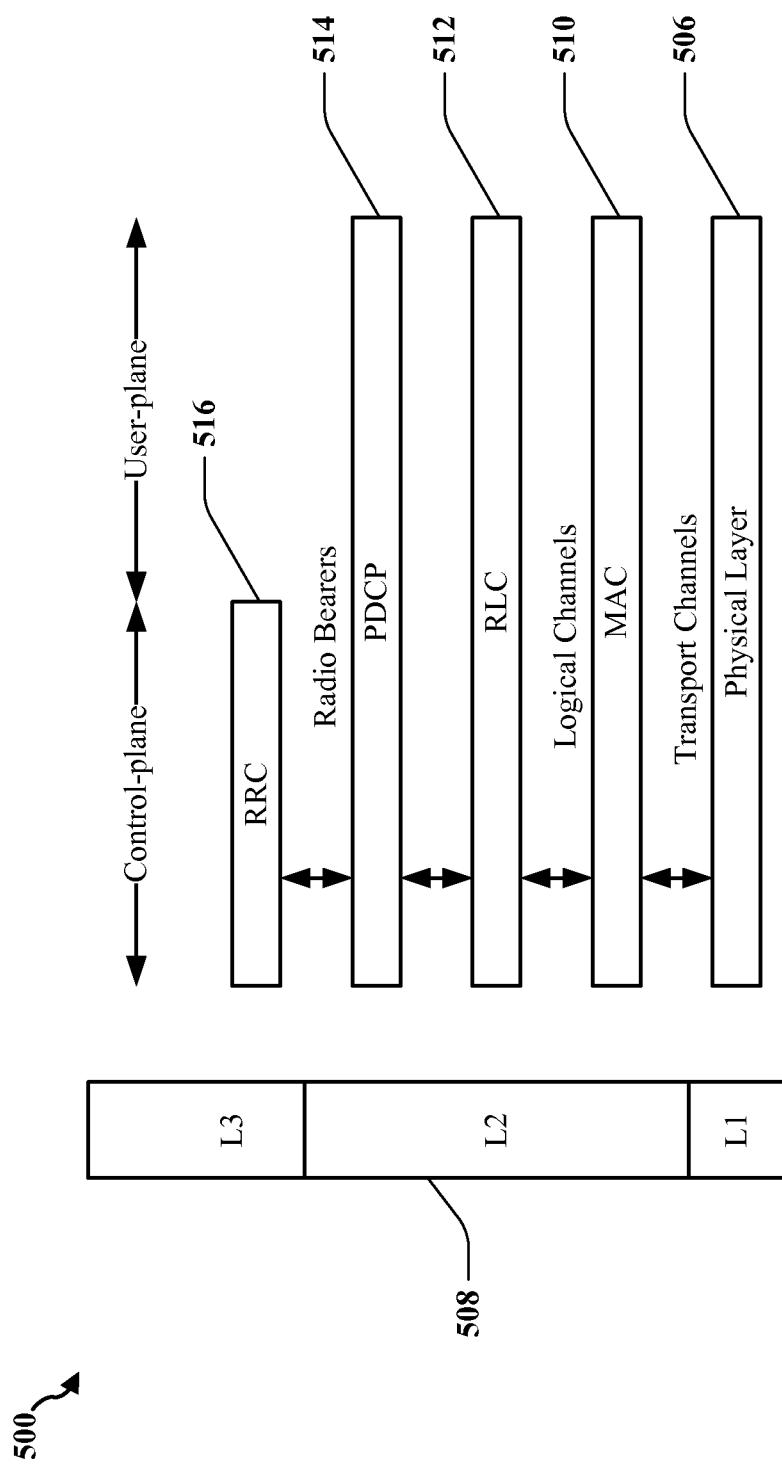


FIG. 5

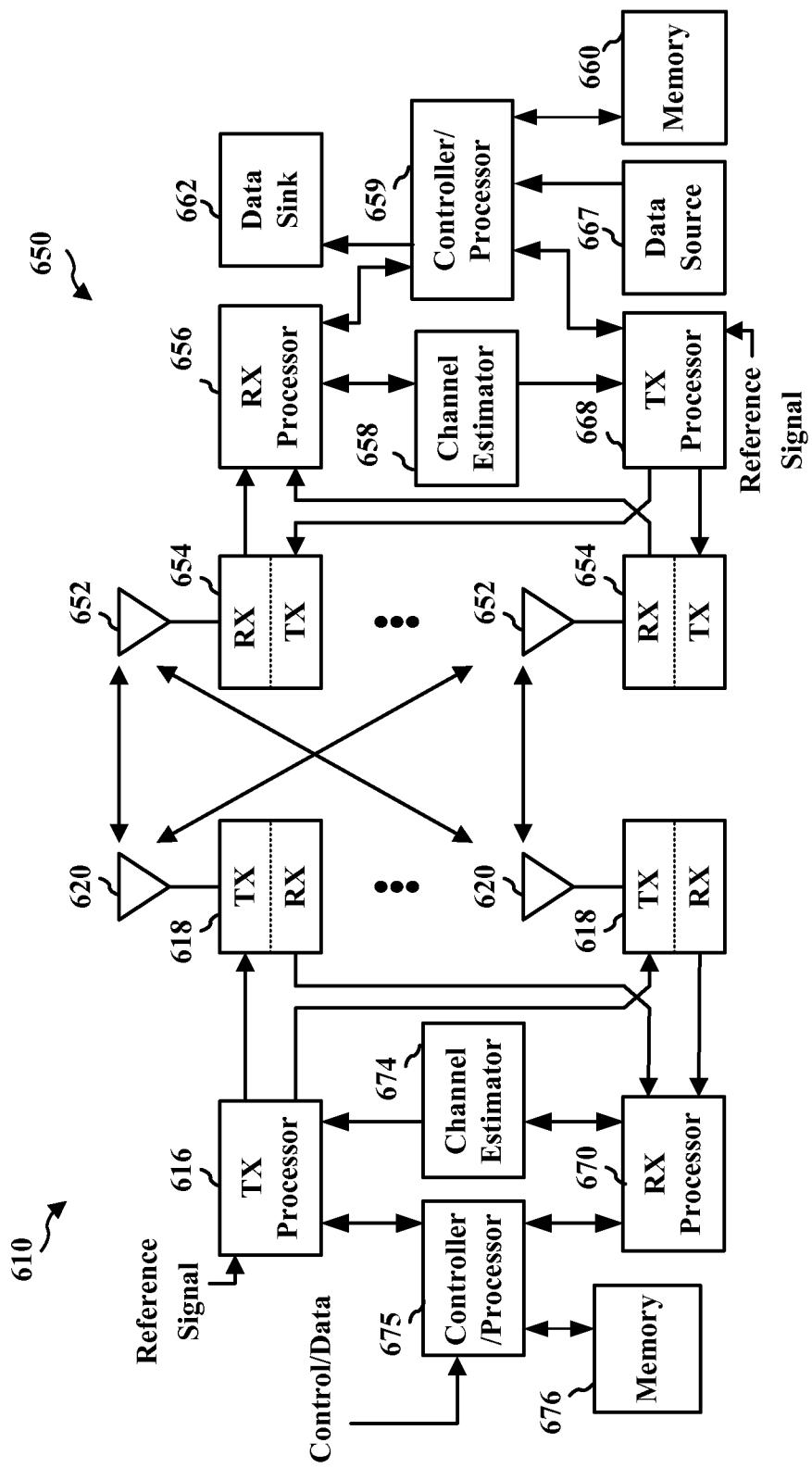
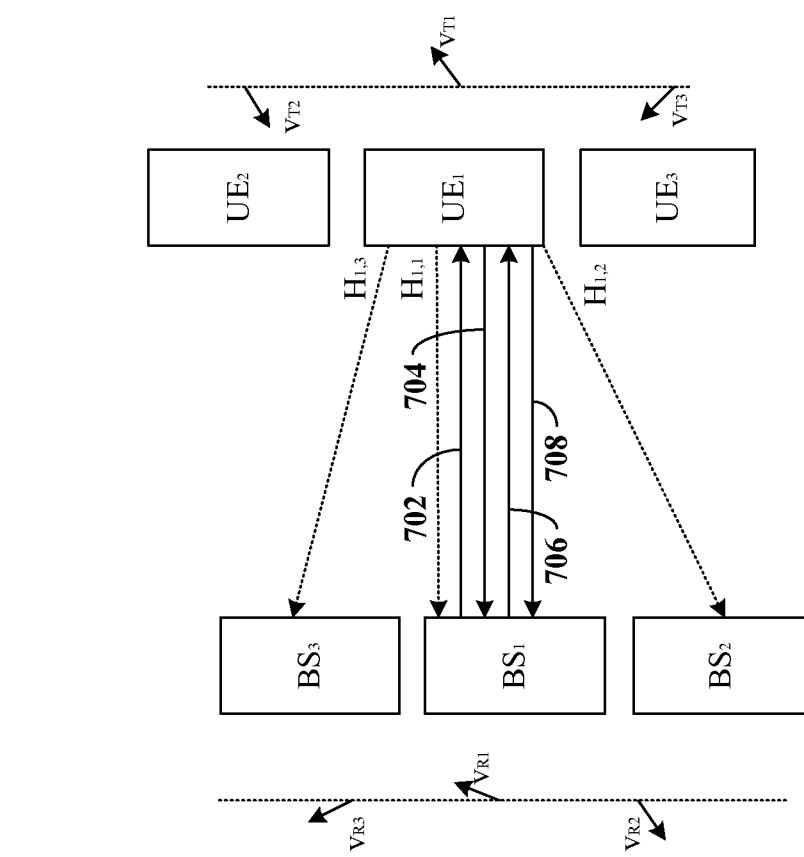
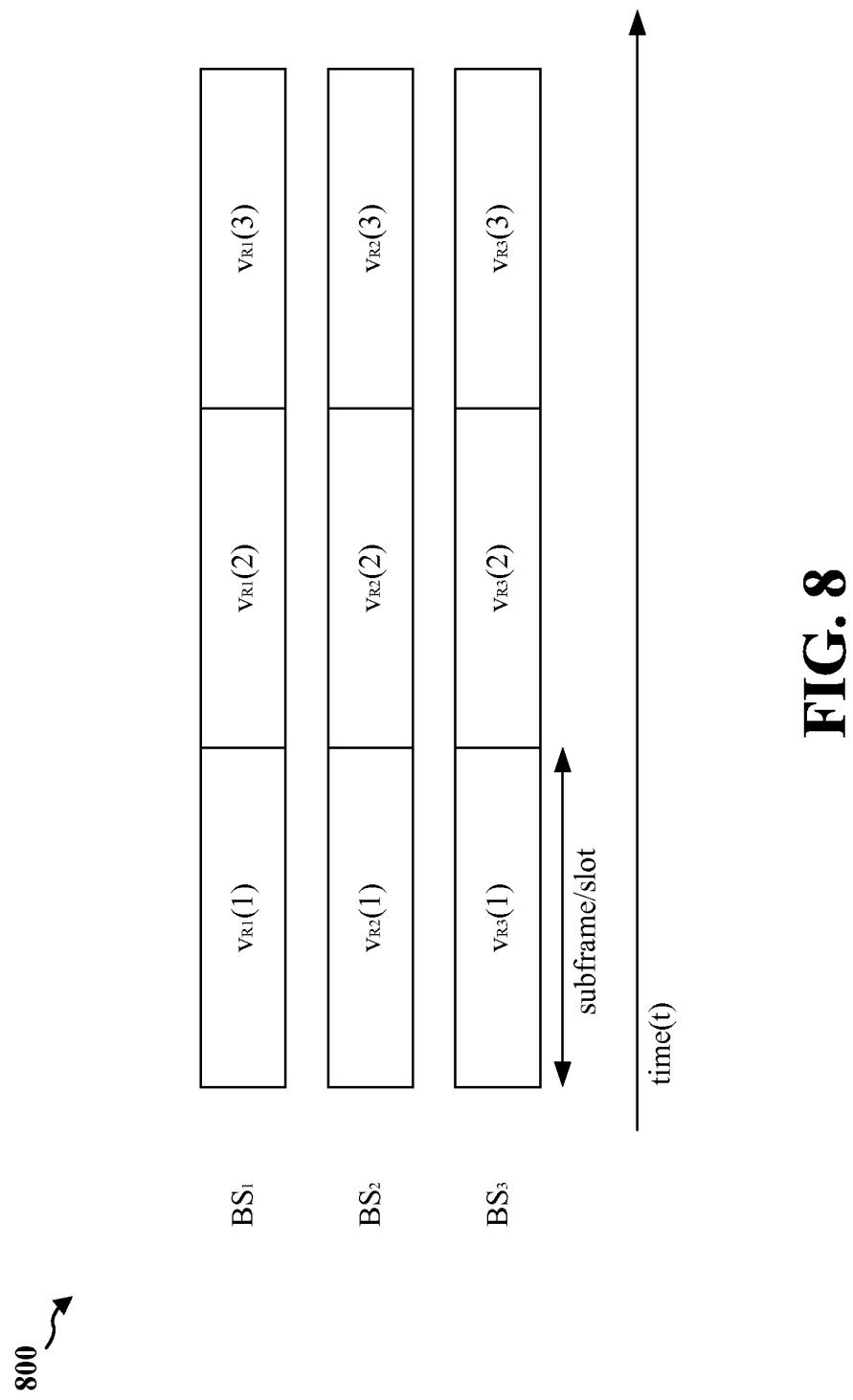


FIG. 6

**FIG. 7**

700 ↗

**FIG. 8**

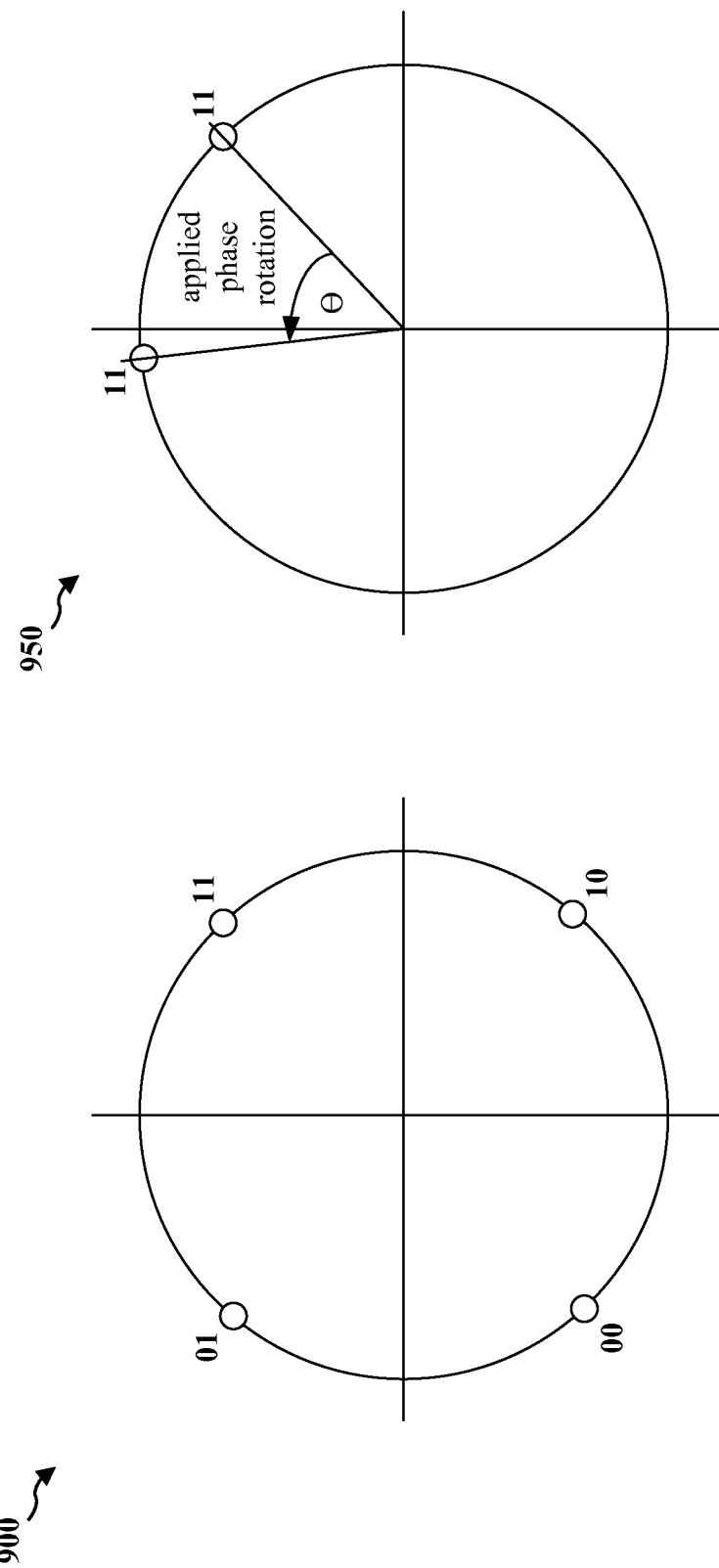


FIG. 9

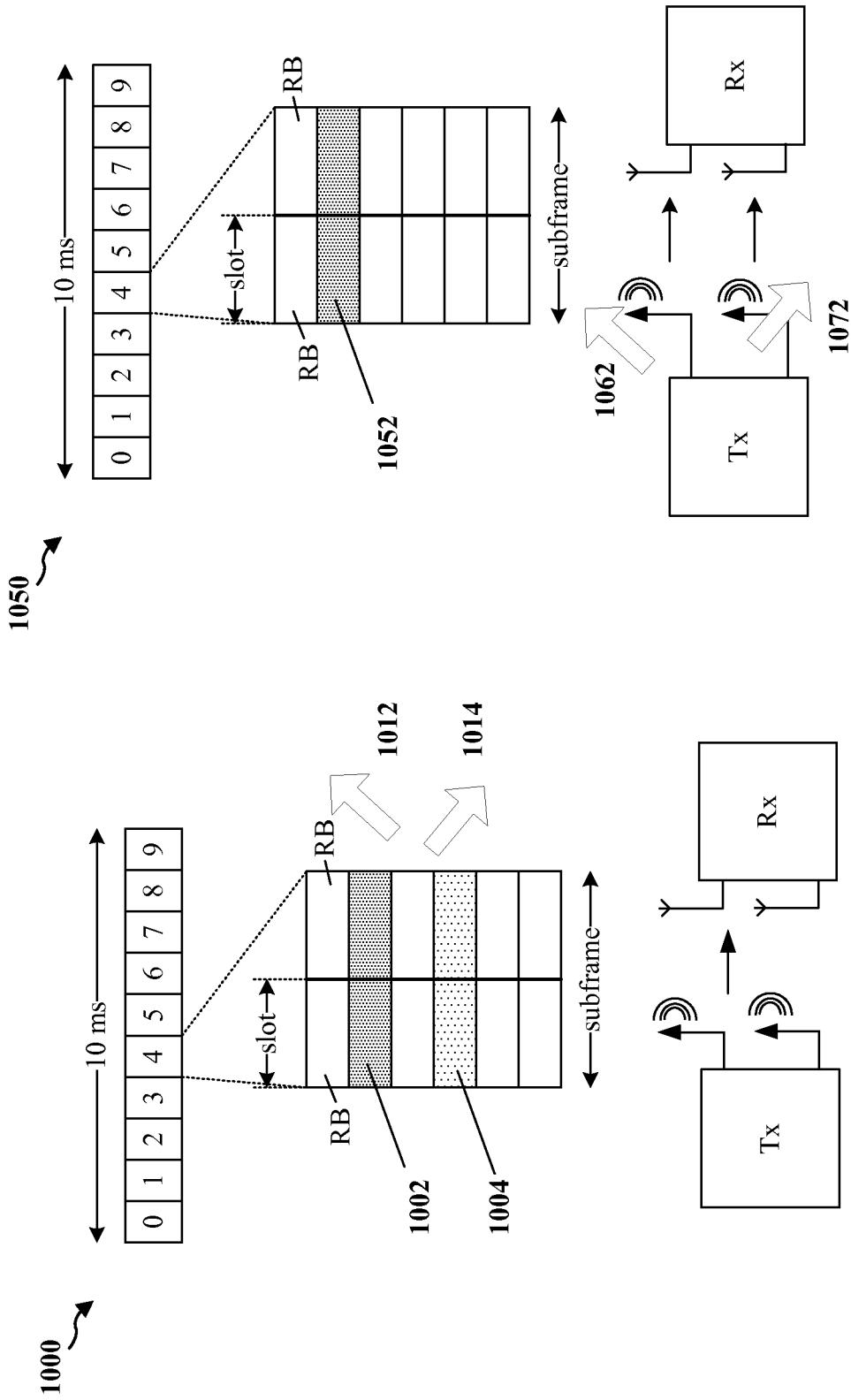


FIG. 10B

FIG. 10A

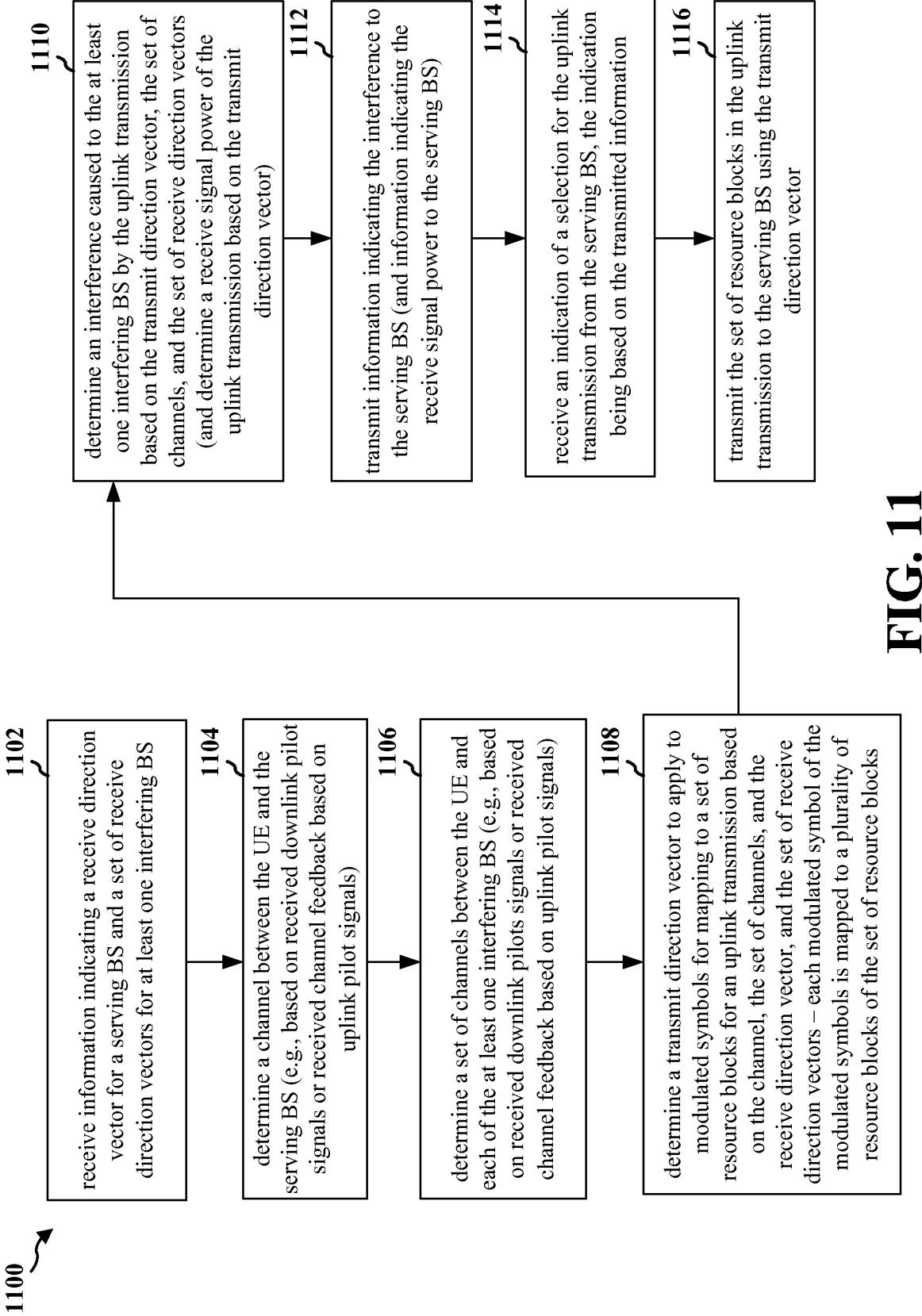


FIG. 11

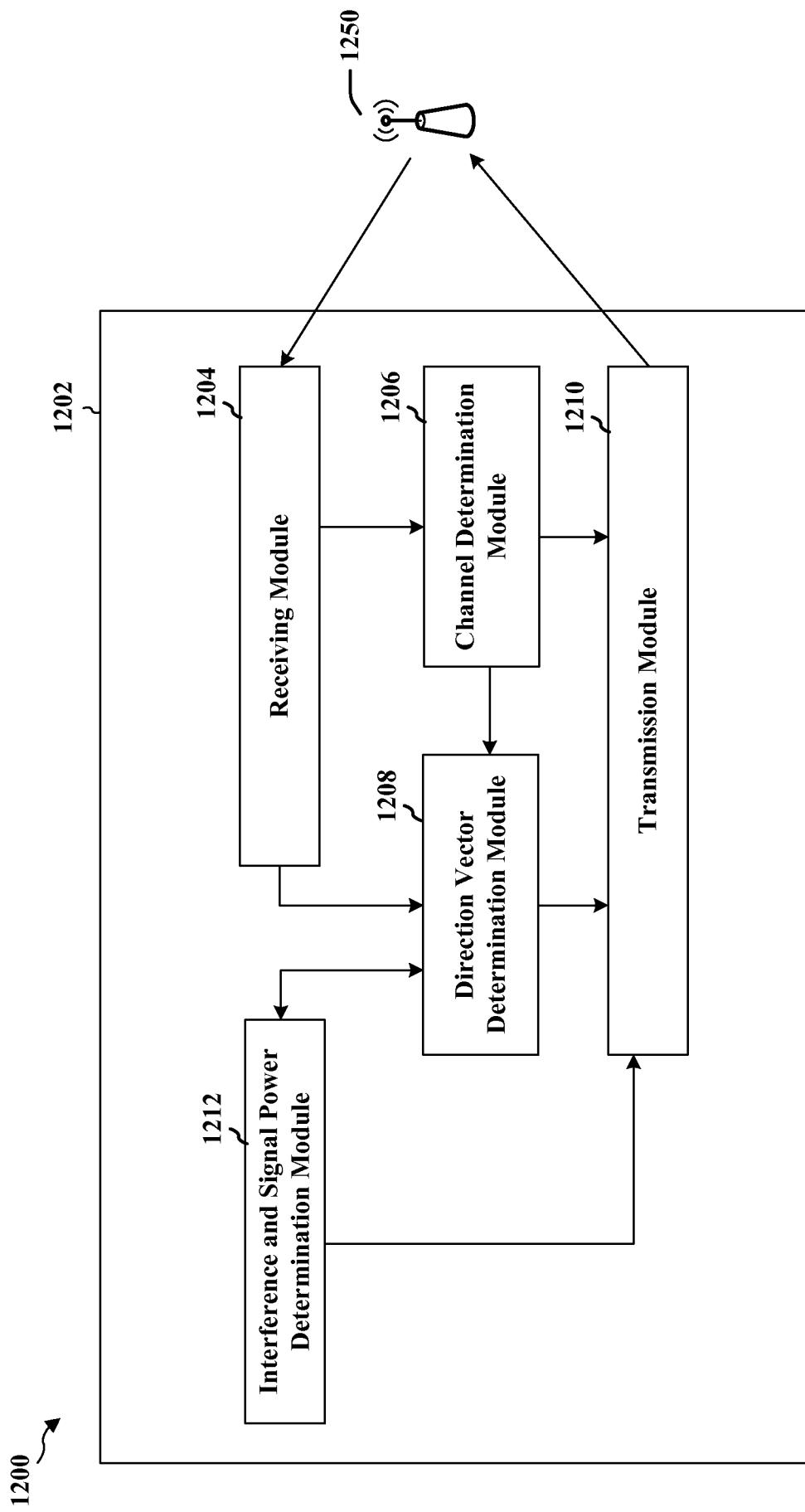
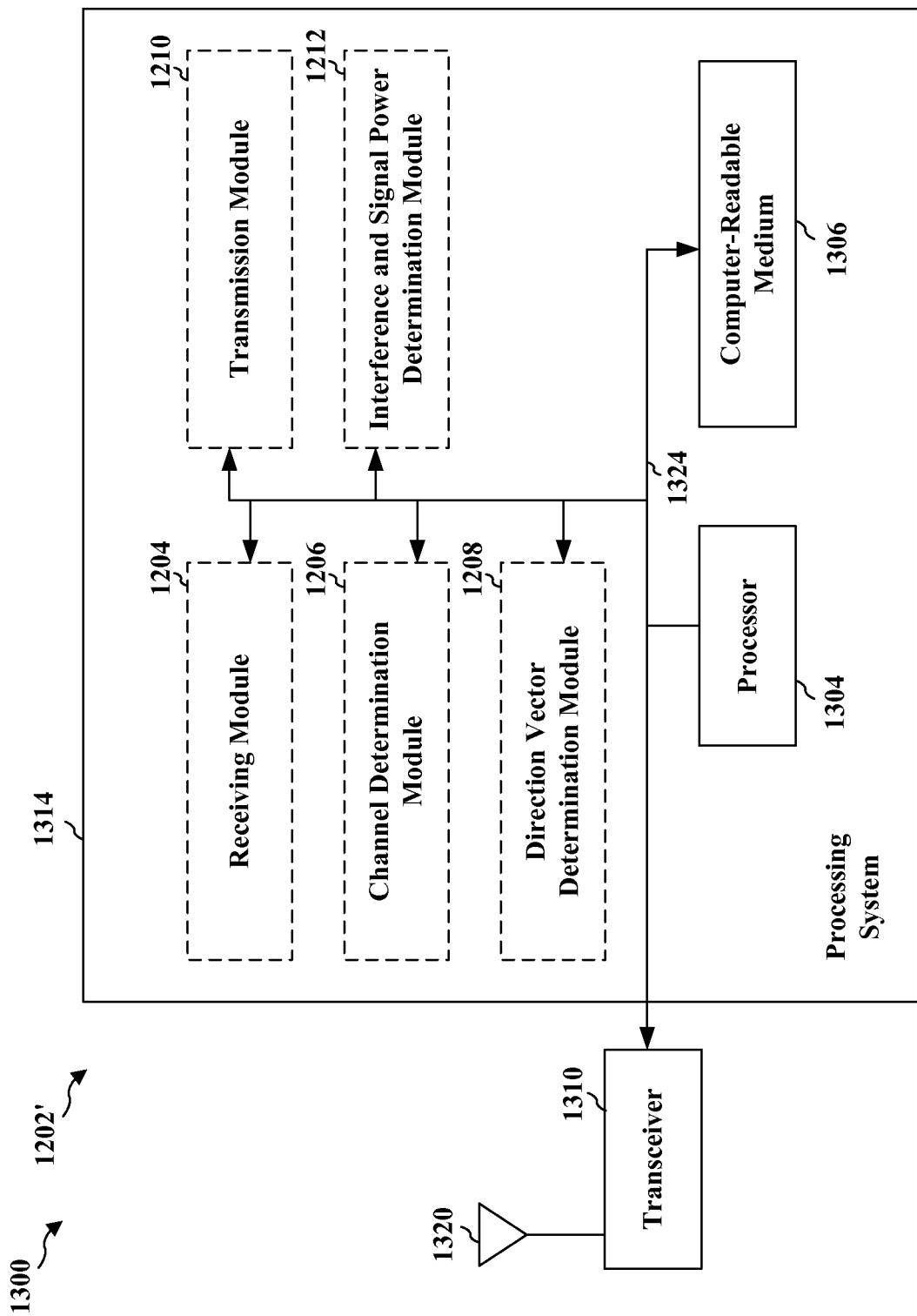


FIG. 12

**FIG. 13**

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2014/011609

A. CLASSIFICATION OF SUBJECT MATTER
INV. H04B7/04 H04B7/06
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	<p>BANG CHUL JUNG ET AL: "Opportunistic Interference Alignment for Interference-Limited Cellular TDD Uplink", IEEE COMMUNICATIONS LETTERS, IEEE SERVICE CENTER, PISCATAWAY, NJ, US, vol. 15, no. 2, 1 February 2011 (2011-02-01), pages 148-150, XP011345749, ISSN: 1089-7798, DOI: 10.1109/LCOMM.2011.121310.101439 page 148, right-hand column, line 13 - line 17 page 149, left-hand column, line 8 - line 10 III OIA in cellular networks ----- -/-</p>	1-32

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier application or patent but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
9 April 2014	17/04/2014

Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Authorized officer Bossen, Michael
--	---

INTERNATIONAL SEARCH REPORTInternational application No
PCT/US2014/011609

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2011/090882 A1 (LEE WOOK BONG [KR] ET AL) 21 April 2011 (2011-04-21) paragraph [0012] paragraph [0020] paragraph [0046] - paragraph [0047] paragraph [0067] paragraph [0074] - paragraph [0077] -----	1-32

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2014/011609

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2011090882 A1	21-04-2011	US 2011090882 A1 WO 2011049295 A2	21-04-2011 28-04-2011