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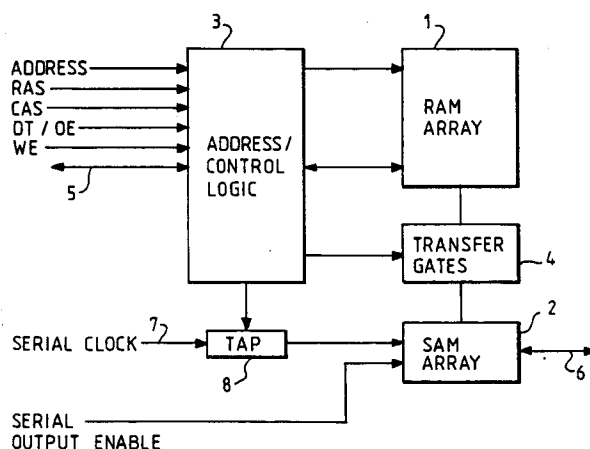
0 462 708 A3

(12)

EUROPEAN PATENT APPLICATION(21) Application number: **91304736.1**(51) Int. Cl.⁵: **G11C 7/00, G06F 12/02, G09G 1/16**(22) Date of filing: **24.05.91**(30) Priority: **19.06.90 US 540546**(43) Date of publication of application:
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DE FR GB(88) Date of deferred publication of the search report:
22.04.92 Bulletin 92/17(71) Applicant: **International Business Machines Corporation**
Old Orchard Road**Armonk, N.Y. 10504(US)**(72) Inventor: **West, Roderick Michael Peters**
Unit 10, Carriage Way
Colchester, Vermont 05446(US)
Inventor: **Williams, Todd**
Box RR1, Box 1015, Kingshill Road
Westford, VT 05494(US)(74) Representative: **Killgren, Neil Arthur**
IBM United Kingdom Limited Intellectual
Property Department Hursley Park
Winchester Hampshire SO21 2JN(GB)(54) **Video random access memory.**

(57) A Video Random Access Memory device wherein full and efficient use of a serial access memory portion provides a simple and efficient means of avoiding Mid-Line Reloads and Real-Time Data Transfers whereby selected parts of two dif-

ferent rows in a random access memory portion are transferred simultaneously to the serial access memory portion via addressable transfer gates under the control of address/control logic.

**FIG. 1** PRIOR ART**EP 0 462 708 A3**



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EUROPEAN SEARCH REPORT

Application Number

EP 91 30 4736

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	WORLD PATENTS INDEX LATEST Week 8942, Derwent Publications Ltd., London, GB; AN 89-308298 ANONYMOUS: "Row addressable random access memory for video monitor storage, allows serial read-out to begin at arbitrary column location, while retaining column redundancy". * abstract * EP 91304736030 & INTERNATIONAL TECHNOLOGY DISCLOSURE no. 20, September 1989, *	1,2,4,8	G 11 C 7/00 G 06 F 12/02 G 09 G 1/16
D,X	US-A-4 825 411 (H. HAMANO) * abstract; figures 3-5 * * column 2, line 42 - column 6, line 37 * *	1	
A		2-8	
D,X	US-A-4 855 959 (S. KOBAYASHI) * the whole document * *	1	
X	US-A-4 688 032 (M. SAITO ET AL.) * abstract; figures 5,6 * * column 4, line 32 - column 5, line 40 * *	1	
A	WORLD PATENTS INDEX LATEST Week 8942, Derwent Publications Ltd., London, GB; AN 89-308294 ANONYMOUS: "Continuous read row addressable random access memory- has data divided into segments that can be independently loaded into read-out shift register to provide long continuous data stream". EP 91304736030 * abstract * & INTERNATIONAL TECHNOLOGY DISCLOSURE no. 16, September 1989, *	1-8	TECHNICAL FIELDS SEARCHED (Int. Cl.5) G 11 C G 09 G
P,X	EP-A-0 398 510 (IBM) * the whole document * *	1	
The present search report has been drawn up for all claims			
Place of search Berlin		Date of completion of search 20 February 92	Examiner SAAM C A
<div><div>CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention</div><div>E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ----- & : member of the same patent family, corresponding document</div></div>			



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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
P,X	EP-A-0 427 114 (MICRON TECHNOLOGY) * abstract; figure 3 * * column 2, line 45 - column 3, line 16 ** - - - - -	1	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
The present search report has been drawn up for all claims			
Place of search		Date of completion of search	Examiner
Berlin		20 February 92	SAAM C A
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