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Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))

[Continued on next page]

(54) Title: PROGRAMMABLE SWITCH CIRCUIT AND METHOD, METHOD OF MANUFACTURE, AND DEVICES AND SYSTEMS INCLUDING THE SAME

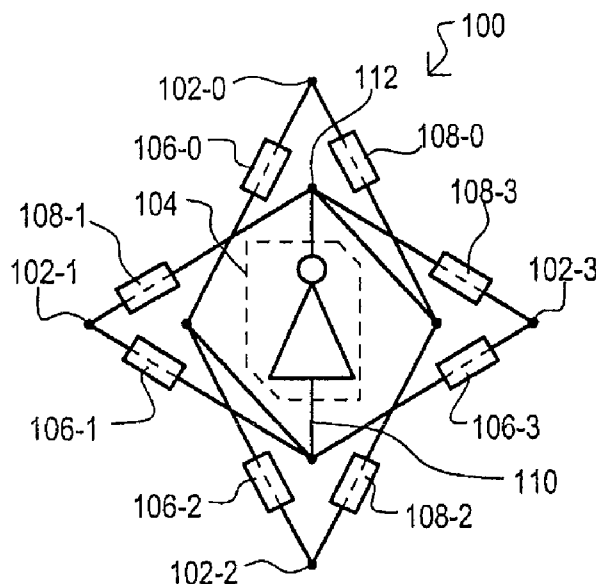


FIG. 1

(57) Abstract: A switching circuit can include a logic circuit having a logic circuit input and a logic circuit output and at least three input transistors coupled to provide three separate paths between three input/output (I/O) nodes and the logic circuit input. The switching circuit can further include at least three output transistors coupled to provide three separate paths between the three I/O nodes and the logic circuit output. Methods of fabricating such switch circuits and devices and/or systems including such switching circuits are also disclosed.



- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))* — *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))*

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A. CLASSIFICATION OF SUBJECT MATTER**H03K 17/687(2006.01)i, H03K 19/177(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Utility models and applications for Utility models since 1975

Japanese Utility models and applications for Utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal), IEEE & Keyword: "SWITCH," "FABRIC," "LOGIC," "PROGRAMMABLE," "TRANSISTOR," "FET," "JFET," and "MOS"

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y	US 6105106 A (Troy A. Manning) 15 August 2000 See the abstract; figures 4-6; claims 1-17	1, 3, 4, 8, 11 16-19
X Y	US 5003200 A (Makoto Sakamoto) 26 March 1991 See the abstract; figures 1-5; claims 1-5	15 16-19
A	US 2003-0012186 A1 (Brent Rowe and Lee Sledjeski) 16 January 2003 See the abstract; figures 1-7; claims 1-14	1-20
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A	US 2004-0061578 A1 (Michael Wendell Vice) 01 April 2004 See the abstract; figures 1-6; claims 1-20	1-20



Further documents are listed in the continuation of Box C.



See patent family annex.

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"P" document published prior to the international filing date but later than the priority date claimed

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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