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ZHU MENGYAO ET AL: "Efficient Algorithm for Packet Loss Concealment Based on Sinusoid and Transient in MDCT Domain", CIRCUITS, COMMUNICATIONS AND SYSTEMS, 2009. PACCS '09. PACIFIC-ASIA CONFERENCE ON, IEEE, PISCATAWAY, NJ, USA, 16 May 2009 (2009-05-16), pages 330-333, XP031526127, ISBN: 978-0-7695-3614-9

DESCRIPTION

TECHNICAL FIELD

[0001] The proposed technology relates to frame error concealment based on frames including transform coefficient vectors.

BACKGROUND

[0002] High quality audio transmission may typically utilize transform-based coding schemes. The input audio signal is usually processed in time-blocks called frames of certain size e.g. 20ms. A frame is transformed by a suitable transform, such as e.g. the Modified Discrete Cosine Transform (MDCT), and the transform coefficients are then quantized and transmitted over the network.

[0003] However, when an audio codec is operated in a communication system which includes wireless or packet networks, a frame could get lost in the transmission, or arrive too late, in order to be used in a real-time scenario. A similar problem arises when the data within a frame has been corrupted, and the codec may be set to discard such corrupted frames. The above examples are called frame erasure or packet loss, and when it occurs the decoder typically invokes certain algorithms to avoid or reduce the degradation in audio quality caused by the frame erasure, and such algorithms are called frame erasure (or error) concealment-algorithms (FEC) or packet loss concealment-algorithms (PLC).

[0004] Fig. 1 illustrates an audio signal input in an encoder 10. A transform to a frequency domain is performed in step S1, a quantization is performed in step S2, and a packetization and transmission of the quantized frequency coefficients (represented by indices) is performed in step S2. The packets are received by a decoder 12 in step S4, after transmission, and the frequency coefficients are reconstructed in step S5, wherein a frame erasure (or error) concealment algorithm is performed, as indicated by an FEC unit 14. The reconstructed frequency coefficients are inverse transformed to the time domain in step S6. Thus, Fig. 1 is a system overview, in which transmission errors are handled at the audio decoder 12 in the process of parameter/waveform reconstruction, and a frame erasure concealment-algorithm performs a reconstruction of lost or corrupt frames.

[0005] The purpose of error concealment is to synthesize lost parts of the audio signal that do not arrive or do not arrive on time at the decoder, or are corrupt. When additional delay can be tolerated and/or additional bits are available one could use various powerful FEC concepts that can be based e.g. on interpolating lost frame between two good frames or transmitting essential side information.

[0006] However, in a real-time conversational scenario it is typically not possible to introduce additional delay, and rarely possible to increase bit-budget and computational complexity of the algorithm. Three exemplary FEC- approaches for a real-time scenario are the following:

- Muting, wherein missing spectral coefficients are set to zero.
- Repetition, wherein coefficients from the last good frame are repeated.
- Noise injection, wherein missing spectral coefficients are the output of a random noise generator.

[0007] An example of an FEC algorithm that is commonly used by transform-based codecs is a frame repeat-algorithm that uses the repetition-approach, and repeats the transform coefficients of the previously received frame, sometimes with a scaling factor, for example as described in [1]. The repeated transform coefficients are then used to reconstruct the audio signal for the lost frame. Frame repeat-algorithms and algorithms for inserting noise or silence are attractive algorithms, because they have low computational complexity and do not require any extra bits to be transmitted or any extra delay. However, the error concealment may degrade the reconstructed signal. For example, a muting-based FEC-scheme could create large energy discontinuities and a poor perceived quality, and the use of a noise injection algorithm could lead to negative perceptual impact, especially when applied to a region with prominent tonal components.

[0008] Another approach described in [2] involves transmission of side information for reconstruction of erroneous frames by interpolation. A drawback of this method is that it requires extra bandwidth for the side information. For MDCT coefficients without side information available, amplitudes are estimated by interpolation, whereas signs are estimated by using a probabilistic model that requires a large number of past frames (50 are suggested), which may not be available in reality.

[0009] A rather complex interpolation method with multiplicative corrections for reconstruction of lost frames is described in [3].

[0010] A further drawback of interpolation based frame error concealment methods is that they introduce extra delays (the frame after the erroneous frame has to be received before any interpolation may be attempted) that may not be acceptable in, for example, real-time applications such as conversational applications.

[0011] Prior art "Robust Transmission of Audio Signals over the Internet: An Advanced Packet Loss Concealment for MP3-Based Audio Signals" by Akinori et al. discloses a method for estimating MDCT coefficients in higher frequencies. The absolute values of the coefficients of an erroneous frame are estimated by a linear interpolation between a previous and a next frame. The sign of a reconstructed coefficient is estimated based on sign changes of a coefficient between each two consecutive frames in the 50 preceding frames. The coefficients are compared one by one.

SUMMARY

[0012] An object of the proposed technology is improved frame error concealment.

[0013] This object is met by embodiments of the proposed technology.

[0014] According to a first aspect, there is provided a frame error concealment method according to claim 1.

[0015] According to a second aspect, there is provided a computer program for frame error concealment according to claim 5.

[0016] According to a third aspect, there is provided a computer program product, comprising a computer readable medium and a computer program according to the second aspect stored on the computer readable medium.

[0017] According to a fourth aspect, the proposed technology involves an embodiment of a decoder configured for frame error concealment according to claim 7.

[0018] According to a fifth aspect, the proposed technology involves another embodiment of a decoder configured for frame error concealment according to claim 8.

[0019] According to a sixth aspect, the proposed technology involves a further embodiment of a decoder configured for frame error concealment according to claim 9.

[0020] According to a seventh aspect, the proposed technology involves a user terminal including a decoder in accordance with the fourth, fifth or sixth aspect.

[0021] At least one of the embodiments is able to improve the subjective audio quality in case of frame loss, frame delay or frame corruption, and this improvement is achieved without transmitting additional side parameters or generating extra delays required by interpolation, and with low complexity and memory requirements.

BRIEF DESCRIPTION OF THE DRAWINGS

[0022] The proposed technology, together with further objects and advantages thereof, may best be understood by making reference to the following description taken together with the accompanying drawings, in which:

Fig. 1 is a diagram illustrating the concept of frame error concealment;

Fig. 2 is a diagram illustrating sign change tracking;

Fig. 3 is a diagram illustrating situations in which sign changes are not considered meaningful;

Fig. 4 is a diagram illustrating frame structure;

Fig. 5 is a diagram illustrating an example of reconstruction of a sub-vector of an erroneous frame;

Fig. 6 is a flow chart illustrating a general embodiment of the proposed method;

Fig. 7 is a block diagram giving an overview of the proposed technology;

Fig. 8 is a block diagram of an example embodiment of a decoder in accordance with the proposed technology;

Fig. 9 is a block diagram of an example embodiment of a decoder in accordance with the proposed technology;

Fig. 10 is a block diagram of an example embodiment of a decoder in accordance with the proposed technology;

Fig. 11 is a block diagram of an example embodiment of a decoder in accordance with the proposed technology;

Fig. 12 is a block diagram of a user terminal; and

Fig. 13 is a diagram illustrating another embodiment of frame error concealment.

DETAILED DESCRIPTION

[0023] Throughout the drawings, the same reference designations are used for similar or corresponding elements.

[0024] The technology proposed herein is generally applicable to Modulated Lapped Transform (MLT) types, for example MDCT, which is the presently preferred transform. In order to simplify the description only the MDCT will be discussed below.

[0025] Furthermore, in the description below the terms lost frame, delayed frame, corrupt frame and frames containing corrupted data all represent examples of erroneous frames which are to be reconstructed by the proposed frame error concealment technology. Similarly the term "good frames" will be used to indicate non-erroneous frames.

[0026] The use of a frame repeat-algorithm for concealing frame errors in a transform codec which uses the MDCT may cause degradation in the reconstructed audio signal, due to the fact that in the MDCT-domain, the phase information is conveyed both in the amplitude and in the sign of the MDCT-coefficients. For tonal or harmonic components, the evolution of the corresponding MDCT coefficients in terms of amplitude and sign depends on the frequency and the initial phase of the underlying tones. The MDCT coefficients for the tonal components in the lost frame may sometimes have the same sign and amplitude as in the previous frame, wherein a frame repeat-algorithm will be advantageous. However, sometimes the MDCT coefficients for the tonal components have changed sign and/or amplitude in the lost frame, and in those cases the frame repeat-algorithm will not work well. When this happens, the sign-mismatch caused by repeating the coefficients with the wrong sign will cause the energy of the tonal components to be spread out over a larger frequency region, which will result in an audible distortion.

[0027] The embodiments described herein analyze the sign-changes of MDCT coefficients in previously received frames, e.g. using a sign change tracking algorithm, and use the collected data regarding the sign-change for creating a low complexity FEC algorithm with improved perceptual quality.

[0028] Since the problem with phase discontinuities is most audible for strong tonal components, and such components will affect a group of several coefficients, the transform coefficients may be grouped into sub-vectors on which the sign-analysis is performed. The analysis according to embodiments described herein also takes into account the signal dynamics, for example as measured by a transient detector, in order to determine the reliability of past data. The number of sign changes of the transform coefficients may be determined for each sub-vector over a defined number of previously received frames, and this data is used for determining the signs of the transform coefficients in a reconstructed sub-vector. According to embodiments described herein, the sign of all coefficients in a sub-vector used in a frame repeat algorithm will be switched (reversed), in case the determined number of sign-changes of the transform coefficients in each corresponding sub-vector over the previously received frames is high, i.e. is equal to or exceeds a defined switching threshold.

[0029] Embodiments described herein involve a decoder-based sign extrapolation-algorithm that uses collected data from a sign change tracking algorithm for extrapolating the signs of a reconstructed MDCT vector. The sign extrapolation-algorithm is activated at a frame loss.

[0030] The sign extrapolation-algorithm may further keep track of whether the previously received frames (as stored in a memory, i.e. in a decoder buffer) are stationary or if they contain transients, since the algorithm is only meaningful to perform on

stationary frames, i.e. when the signal does not contain transients. Thus, according to an embodiment, the sign of the reconstructed coefficients will be randomized, in case any of the analyzed frames of interest contain a transient.

[0031] An embodiment of the sign extrapolation-algorithm is based on sign-analysis over three previously received frames, due to the fact that three frames provide sufficient data in order to achieve a good performance. In case only the last two frames are stationary, the frame $n - 3$ is discarded. The analysis of the sign-change over two frames is similar to the analysis of the sign-change over three frames, but the threshold level is adapted accordingly.

[0032] Fig. 2 is a diagram illustrating sign change tracking. If the recent signal history contains only good frames, the sign change is tracked in three consecutive frames, as illustrated in Fig. 2a. In case of a transient or lost frame, as in Fig. 2b and 2c, the sign change is calculated on the two available frames. The current frame has index " n ", a lost frame is denoted by a dashed box, and a transient frame by a dotted box. Thus, in Fig. 2a the sign tracking region is 3 frames, and in Fig. 2b and 2c the sign tracking region is 2 frames.

[0033] Fig. 3 is a diagram illustrating situations in which sign changes are not considered meaningful. In this case one of the last two frames before an erroneous frame n is a transient (or non-stationary) frame. In this case the sign extrapolation algorithm may force a "random" mode for all sub-vectors of the reconstructed frame.

[0034] Tonal or harmonic components in the time-domain audio signal will affect several coefficients in the MDCT domain. A further embodiment captures this behavior in the sign-analysis by determining the number of sign-changes of groups of MDCT coefficients, instead of on the entire vector of MDCT coefficients, such that the MDCT coefficients are grouped into e.g. 4-dimensional bands in which the sign analysis is performed. Since the distortion caused by sign mismatch is most audible in the low frequency region, a further embodiment of the sign analysis is only performed in the frequency range 0-1600 Hz, in order to reduce computational complexity. If the frequency resolution of the MDCT transform used in this embodiment is e.g. 25 Hz per coefficient, the frequency range will consist of 64 coefficients which could be divided into B bands, where $B = 16$ in this example.

[0035] Fig. 4 is a diagram illustrating the frame structure of the above example. A number of consecutive good frames are illustrated. Frame n has been expanded to illustrate that it contains 16 bands or sub-vectors. Band b of frame n has been expanded to illustrate the 4 transform coefficients $\hat{x}_n(1), \dots, \hat{x}_n(4)$. The transform coefficients $\hat{x}_{n-1}(1), \dots, \hat{x}_{n-1}(4)$ and $\hat{x}_{n-2}(1), \dots, \hat{x}_{n-2}(4)$ of the corresponding sub-vector or band b of frames $n - 1$ and $n - 2$, respectively, are also illustrated.

[0036] According to an embodiment, the determining of the number of sign-changes of the transform coefficients in frames received by the decoder is performed by a sign change tracking-algorithm, which is active as long as the decoder receives frames, i.e. as long as there are no frame losses. During this period, the decoder may update two state variables, s_n and Δ_n for each sub-vector or band b used in the sign analysis, and in the example with 16 sub-vectors there will thus be 32 state variables.

[0037] The first state variable s_n for each sub-vector or band b holds the number of sign switches between the current frame n and the past frame $n - 1$, and is updated in accordance with (note that here frame n is considered to be a good frame, while frame n in Fig. 2 and 3 was an erroneous frame):

$$\begin{cases} \text{if } isTransient_n == 0 \\ \quad \text{for } b \in B \\ \quad \quad \text{for } i_b \in b \\ \quad \quad \quad \text{if } \hat{x}_n(i_b) * \hat{x}_{n-1}(i_b) < 0 \\ \quad \quad \quad \quad s_n(b) = s_n(b) + 1 \end{cases} \quad (1)$$

where the index i_b indicates coefficients in sub-vector or band b , n is the frame number, and \hat{x}_n is the vector of received quantized transform coefficients.

[0038] If the frame n is a transient, which is indicated by the variable $isTransient_n$ in (1), the number of sign switches is not relevant information, and will be set to 0 for all bands.

[0039] The variable $isTransient_n$ is obtained as a "transient bit" from the encoder, and may be determined on the encoder side as described in [4].

[0040] The second state variable Δ_n for each sub-vector holds the aggregated number of sign switches between the current frame n and the past frame $n - 1$ and between the past frame $n - 1$ and the frame $n - 2$, in accordance with:

$$\begin{array}{l} \text{if } isTransient_n = 0 \\ \text{for } b \in B \\ \Delta_n(b) = s_n(b) + s_{n-1}(b) \end{array} \quad (2)$$

[0041] The sign extrapolation-algorithm is activated when the decoder does not receive a frame or the frame is bad, i.e. if the data is corrupted.

According to an embodiment, when a frame is lost (erroneous), the decoder first performs a frame repeat-algorithm and copies the transform coefficients from the previous frame into the current frame. Next, the algorithm checks if the three previously received frames contain any transients by checking the stored transient flags for those frames. (However, if any of the last two previously received frames contains transients, there is no useful data in the memory to perform sign analysis on and no sign prediction is performed, as discussed with reference to Fig. 3).

[0042] If at least the two previously received frames are stationary, the sign extrapolation-algorithm compares the number of sign-switches Δ_n for each band with a defined switching threshold T and switches, or flips, the signs of the corresponding coefficients in the current frame if the number of sign-switches is equal to or exceeds the switching threshold.

[0043] According to an embodiment, and under the assumption of 4-dim bands, the level of the switching threshold T depends on the number of stationary frames in the memory, according to the following:

$$T = \begin{cases} 6 & \text{if 3 stationary frames} \\ 3 & \text{if 2 stationary frames} \end{cases} \quad (3)$$

[0044] The comparison with the threshold T and the potential sign flip/switch for each band is done according to the following (wherein a sign flip or reversal is indicated by -1) :

$$\begin{array}{l} \text{for } b \in B \\ \text{for } i_b \in b \\ \text{sign}(\hat{x}_n(i_b)) = \begin{cases} -1 & \text{if } \Delta_n(b) \geq T \\ +1 & \text{if } \Delta_n(b) < T \end{cases} \end{array} \quad (4)$$

[0045] In this scheme, the extrapolated sign of the transform coefficients in the first lost frame is either switched, or kept the same as in the last good frame. In case there is a sequence of lost frames, in one embodiment the sign is randomized from the second frame.

[0046] Table 1 below is a summary of the sign extrapolation-algorithm for concealment of lost frame with index "n", according to an embodiment (Note that here *frame n* is considered erroneous, while *frame n* was considered good in the above equations. Thus, there is an index shift of 1 unit in the table):

Table 1

If any of frames <i>n-1</i> and <i>n-2</i> contains transient	Apply random sign to the copied frequency coefficients
If frames <i>n-1</i> and <i>n-2</i> are good, but <i>n-3</i> is lost or transient frame	Apply sign extrapolation with switching threshold $T=3$
If all <i>n-1</i> , <i>n-2</i> , <i>n-3</i> are good	Apply sign extrapolation with switching threshold $T=6$

[0047] Fig. 5 is a diagram illustrating an example of reconstruction of a sub-vector of an erroneous frame. In this example the sub-vectors from Fig. 4 will be used to illustrate the reconstruction of frame *n + 1*, which is assumed to be erroneous. The 3 frames *n*, *n - 1*, *n - 2* are all considered to be stationary ($isTransient_n = 0$, $isTransient_{n-1} = 0$, $isTransient_{n-2}=0$). First the sign change tracking of (1) above is used to calculate $s_n(b)$ and $s_{n-1}(b)$. In the example there are 3 sign reversals between corresponding sub-vector coefficients of frame *n* and *n - 1*, and 3 sign reversals between corresponding sub-vector coefficients of frame *n - 1* and *n - 2*. Thus, $s_n(b) = 3$ and $s_{n-1}(b) = 3$, which according to the sign change accumulation of (2) above implies that $\Delta_n(b) = 6$. According to the threshold definition (3) and the sign extrapolation (4) this is sufficient (in this example) to reverse the signs of the coefficients that are copied from sub-vector *b* of *frame n* into sub-vector *b* of frame *n + 1*, as illustrated in Fig. 5.

[0048] Fig. 6 is a flow chart illustrating a general embodiment of the proposed method. This flow chart may also be viewed as a computer flow diagram. Step S11 tracks sign changes between corresponding transform coefficients of predetermined sub-

vectors of consecutive good stationary frames. Step S12 accumulates the number of sign changes in corresponding sub-vectors of a predetermined number of consecutive good stationary frames. Step S12 reconstructs an erroneous frame with the latest good stationary frame, but with reversed signs of transform coefficients in sub-vectors having an accumulated number of sign changes that exceeds a predetermined threshold.

[0049] As noted above, the threshold may depend on the predetermined number of consecutive good stationary frames. For example, the threshold is assigned a first value for 2 consecutive good stationary frames and a second value for 3 consecutive good stationary frames.

[0050] Furthermore, stationarity of a received frame may be determined by determining whether it contain any transients, for example by examining the variable *isTransient_n* as described above.

[0051] A further embodiment uses three modes of switching of the sign of the transform coefficients, e.g. switch, preserve, and random, and this is realized through comparison with two different thresholds, i.e. a preserve threshold T^P and a switching threshold T^S . This means that the extrapolated sign of the transform coefficients in the first lost frame is switched in case the number of sign switches is equal to or exceeds the switching threshold T^S , and is preserved in case number of sign switches is equal to or lower than the preserve threshold T^P . Further, the signs are randomized in case the number of sign switches is larger than the preserve threshold T^P and lower than the switching threshold T^S , i.e.:

$$\left. \begin{array}{l} \text{for } b \in B \\ \text{for } i_b \in b \\ \text{sign}(\hat{x}_n(i_b)) = \begin{cases} -1 & \text{if } \Delta_n(b) \geq T^S \\ \text{rand}() & \text{if } T^P < \Delta_n(b) < T^S \\ +1 & \text{if } \Delta_n(b) \leq T^P \end{cases} \end{array} \right\} \quad (5)$$

[0052] In this scheme the sign extrapolation in the first lost frame is applied on the second and so on, as the randomization is already part of the scheme.

[0053] According to a further embodiment, a scaling factor (energy attenuation) is applied to the reconstructed coefficients, in addition to the switching of the sign:

$$\hat{x}_n = G * \hat{x}_{n-1} \quad (6)$$

[0054] In equation (6) G is a scaling factor which may be 1 if no gain prediction is used, or $G \leq 1$ in the case of gain prediction (or simple attenuation rule, like -3 dB for each consecutive lost frame).

[0055] The steps, functions, procedures, modules and/or blocks described herein may be implemented in hardware using any conventional technology, such as discrete circuit or integrated circuit technology, including both general-purpose electronic circuitry and application-specific circuitry.

[0056] Particular examples include one or more suitably configured digital signal processors and other known electronic circuits, e.g. discrete logic gates interconnected to perform a specialized function, or Application Specific Integrated Circuits (ASICs).

[0057] Alternatively, at least some of the steps, functions, procedures, modules and/or blocks described above may be implemented in software such as a computer program for execution by suitable processing circuitry including one or more processing units.

[0058] The flow diagram or diagrams presented herein may therefore be regarded as a computer flow diagram or diagrams, when performed by one or more processors. A corresponding apparatus may be defined as a group of function modules, where each step performed by the processor corresponds to a function module. In this case, the function modules are implemented as a computer program running on the processor.

[0059] Examples of processing circuitry includes, but is not limited to, one or more microprocessors, one or more Digital Signal Processors, SPs, one or more Central Processing Units, CPUs, video acceleration hardware, and/or any suitable programmable logic circuitry such as one or more Field Programmable Gate Arrays, FPGAs, or one or more Programmable Logic Controllers.

[0060] It should also be understood that it may be possible to re-use the general processing capabilities of any conventional device or unit in which the proposed technology is implemented. It may also be possible to re-use existing software, e.g. by reprogramming of the existing software or by adding new software components.

[0061] The embodiments described herein apply to a decoder for an encoded audio signal, as illustrated in Fig. 7. Thus, Fig. 7 is a schematic block diagram of a decoder 20 according to the embodiments. The decoder 20 comprises an input unit IN configured to receive an encoded audio signal. The figure illustrates the frame loss concealment by a logical frame error concealment-unit (FEC) 16, which indicates that the decoder 20 is configured to implement a concealment of a lost or corrupt audio frame, according to the above-described embodiments. The decoder 20 with its included units could be implemented in hardware. There are numerous variants of circuitry elements that can be used and combined to achieve the functions of the units of the decoder 20. Such variants are encompassed by the embodiments. Particular examples of hardware implementation of the decoder are implementation in digital signal processor (DSP) hardware and integrated circuit technology, including both general-purpose electronic circuitry and application-specific circuitry.

Fig. 8 is a block diagram of an example embodiment of a decoder 20 in accordance with the proposed technology. An input unit IN extracts transform coefficient vectors from an encoded audio signal and forwards them to the FEC unit 16 of the decoder 20. The decoder 20 includes a sign change tracker 26 configured to track sign changes between corresponding transform coefficients of predetermined sub-vectors of consecutive good stationary frames. The sign change tracker 26 is connected to a sign change accumulator 28 configured to accumulate the number of sign changes in corresponding sub-vectors of a predetermined number of consecutive good stationary frames. The sign change accumulator 28 is connected to a frame reconstructor 30 configured to reconstruct an erroneous frame with the latest good stationary frame, but with reversed signs of transform coefficients in sub-vectors having an accumulated number of sign changes that exceeds a predetermined threshold. The reconstructed transform coefficient vector is forwarded to an output unit OUT, which converts it into an audio signal.

[0062] Fig. 9 is a block diagram of an example embodiment of a decoder in accordance with the proposed technology. An input unit IN extracts transform coefficient vectors from an encoded audio signal and forwards them to the FEC unit 16 of the decoder 20. The decoder 20 includes:

- A sign change tracking module 26 for tracking sign changes between corresponding transform coefficients of predetermined sub-vectors of consecutive good stationary frames.
- A sign change accumulation module 28 for accumulating the number of sign changes in corresponding sub-vectors of a predetermined number of consecutive good stationary frames.
- A frame reconstruction module 30 for reconstructing an erroneous frame with the latest good stationary frame, but with reversed signs of transform coefficients in sub-vectors having an accumulated number of sign changes that exceeds a predetermined threshold.

[0063] The reconstructed transform coefficient vector is converted into an audio signal in an output unit OUT.

[0064] Fig. 10 is a block diagram of an example embodiment of a decoder 20 in accordance with the proposed technology. The decoder 20 described herein could alternatively be implemented e.g. by one or more of a processor 22 and adequate software with suitable storage or memory 24 therefore, in order to reconstruct the audio signal, which includes performing audio frame loss concealment according to the embodiments described herein. The incoming encoded audio signal is received by an input unit IN, to which the processor 22 and the memory 24 are connected. The decoded and reconstructed audio signal obtained from the software is outputted from the output unit OUT.

[0065] More specifically the decoder 20 includes a processor 22 and a memory 24, and the memory contains instructions executable by the processor, whereby the decoder 20 is operative to:

- Track sign changes between corresponding transform coefficients of predetermined sub-vectors of consecutive good stationary frames.
- Accumulate the number of sign changes in corresponding sub-vectors of a predetermined number of consecutive good stationary frames.
- Reconstruct an erroneous frame with the latest good stationary frame, but with reversed signs of transform coefficients in sub-vectors having an accumulated number of sign changes that exceeds a predetermined threshold.

[0066] Illustrated in Fig. 10 is also a computer program product 40 comprising a computer readable medium and a computer

program (further described below) stored on the computer readable medium. The instructions of the computer program may be transferred to the memory 24, as indicated by the dashed arrow.

[0067] Fig. 11 is a block diagram of an example embodiment of a decoder 20 in accordance with the proposed technology. This embodiment is based on a processor 22, for example a micro processor, which executes a computer program 42 for frame error concealment based on frames including transform coefficient vectors. The computer program is stored in memory 24. The processor 22 communicates with the memory over a system bus. The incoming encoded audio signal is received by an input/output (I/O) controller 26 controlling an I/O bus, to which the processor 22 and the memory 24 are connected. The audio signal obtained from the software 130 is outputted from the memory 24 by the I/O controller 26 over the I/O bus. The computer program 42 includes code 50 for tracking sign changes between corresponding transform coefficients of predetermined sub-vectors of consecutive good stationary frames, code 52 for accumulating the number of sign changes in corresponding sub-vectors of a predetermined number of consecutive good stationary frames, and code 54 for reconstructing an erroneous frame with the latest good stationary frame, but with reversed signs of transform coefficients in sub-vectors having an accumulated number of sign changes that exceeds a predetermined threshold.

[0068] The computer program residing in memory may be organized as appropriate function modules configured to perform, when executed by the processor, at least part of the steps and/or tasks described above. An example of such function modules is illustrated in Fig. 9.

[0069] As noted above, the software or computer program 42 may be realized as a computer program product 40, which is normally carried or stored on a computer-readable medium. The computer-readable medium may include one or more removable or non-removable memory devices including, but not limited to a Read-Only Memory, ROM, a Random Access Memory, RAM, a Compact Disc, CD, a Digital Versatile Disc, DVD, a Universal Serial Bus, USB, memory, a Hard Disk Drive, HDD storage device, a flash memory, or any other conventional memory device. The computer program may thus be loaded into the operating memory of a computer or equivalent processing device for execution by the processing circuitry thereof.

[0070] For example, the computer program includes instructions executable by the processing circuitry, whereby the processing circuitry is able or operative to execute the steps, functions, procedure and/or blocks described herein. The computer or processing circuitry does not have to be dedicated to only execute the steps, functions, procedure and/or blocks described herein, but may also execute other tasks.

[0071] The technology described above may be used e.g. in a receiver, which can be used in a mobile device (e.g. mobile phone, laptop) or a stationary device, such as a personal computer. This device will be referred to as a user terminal including a decoder 20 as described above. The user terminal may be a wired or wireless device.

[0072] As used herein, the term "wireless device" may refer to a User Equipment, UE, a mobile phone, a cellular phone, a Personal Digital Assistant, PDA, equipped with radio communication capabilities, a smart phone, a laptop or Personal Computer, PC, equipped with an internal or external mobile broadband modem, a tablet PC with radio communication capabilities, a portable electronic radio communication device, a sensor device equipped with radio communication capabilities or the like. In particular, the term "UE" should be interpreted as a non-limiting term comprising any device equipped with radio circuitry for wireless communication according to any relevant communication standard.

[0073] As used herein, the term "wired device" may refer to at least some of the above devices (with or without radio communication capability), for example a PC, when configured for wired connection to a network.

[0074] Fig. 12 is a block diagram of a user terminal 60. The diagram illustrates a user equipment, for example a mobile phone. A radio signal from an antenna is forwarded to a radio unit 62, and the digital signal from the radio unit is processed by a decoder 20 in accordance with the proposed frame error concealment technology (typically the decoder may perform other task, such as decoding of other parameters describing the segment, but these tasks are not described since they are well known in the art and do not form an essential part of the proposed technology). The decoded audio signal is forwarded to a digital/analog (D/A) signal conversion and amplification unit 64 connected to a loudspeaker.

[0075] Fig. 13 is a diagram illustrating another embodiment of frame error concealment. The encoder side 10 is similar to the embodiment of Fig. 1. However, the encoder side includes a decoder 20 in accordance with the proposed technology. This decoder includes an frame error concealment unit (FEC) 16 as proposed herein. This unit modifies the reconstruction step S5 of Fig 1 into a reconstruction step S5' based on the proposed technology. According to a further embodiment, the above-described error concealment algorithm may optionally be combined with another concealment algorithm on a different domain. In Fig. 13 this is illustrated by an optional frame error concealment unit FEC2 18, in which a waveform pitch-based concealment is also

performed. This will modify step S6 into S6'. Thus, in this embodiment the reconstructed waveform contains contributions from both concealment schemes.

[0076] It is to be understood that the choice of interacting units or modules, as well as the naming of the units are only for exemplary purpose, and may be configured in a plurality of alternative ways in order to be able to execute the disclosed process actions.

[0077] It should also be noted that the units or modules described in this disclosure are to be regarded as logical entities and not with necessity as separate physical entities. It will be appreciated that the scope of the technology disclosed herein fully encompasses other embodiments which may become obvious to those skilled in the art, and that the scope of this disclosure is accordingly not to be limited.

[0078] Reference to an element in the singular is not intended to mean "one and only one" unless explicitly so stated, but rather "one or more." All structural and functional equivalents to the elements of the above-described embodiments that are known to those of ordinary skill in the art are expressly incorporated herein by reference and are intended to be encompassed hereby. Moreover, it is not necessary for a device or method to address each and every problem sought to be solved by the technology disclosed herein, for it to be encompassed hereby.

[0079] In the preceding description, for purposes of explanation and not limitation, specific details are set forth such as particular architectures, interfaces, techniques, etc. in order to provide a thorough understanding of the disclosed technology. However, it will be apparent to those skilled in the art that the disclosed technology may be practiced in other embodiments and/or combinations of embodiments that depart from these specific details. That is, those skilled in the art will be able to devise various arrangements which, although not explicitly described or shown herein, embody the principles of the disclosed technology. In some instances, detailed descriptions of well-known devices, circuits, and methods are omitted so as not to obscure the description of the disclosed technology with unnecessary detail. All statements herein reciting principles, aspects, and embodiments of the disclosed technology, as well as specific examples thereof, are intended to encompass both structural and functional equivalents thereof. Additionally, it is intended that such equivalents include both currently known equivalents as well as equivalents developed in the future, e.g. any elements developed that perform the same function, regardless of structure.

[0080] Thus, for example, it will be appreciated by those skilled in the art that the figures herein can represent conceptual views of illustrative circuitry or other functional units embodying the principles of the technology, and/or various processes which may be substantially represented in computer readable medium and executed by a computer or processor, even though such computer or processor may not be explicitly shown in the figures.

The functions of the various elements including functional blocks may be provided through the use of hardware such as circuit hardware and/or hardware capable of executing software in the form of coded instructions stored on computer readable medium. Thus, such functions and illustrated functional blocks are to be understood as being either a hardware-implemented and/or a computer-implemented, and thus machine-implemented.

[0081] The embodiments described above are to be understood as a few illustrative examples of the present invention. It will be understood by those skilled in the art that various modifications, combinations and changes may be made to the embodiments without departing from the scope of the present invention. In particular, different part solutions in the different embodiments can be combined in other configurations, where technically possible.

[0082] It will be understood by those skilled in the art that various modifications and changes may be made to the proposed technology without departure from the scope thereof, which is defined by the appended claims.

REFERENCES

[0083]

1. [1] ITU-T standard G.719, section 8.6, June 2008.
2. [2] A. Ito et al, "Improvement of Packet Loss Concealment for MP3 Audio Based on Switching of Concealment method and Estimation of MDCT Signs", IEEE, 2010 Sixth International Conference on Intelligent Information Hiding and Multimedia Signal Processing, pp. 518-521.
3. [3] Sang-Uk Ryu and Kenneth Rose, "An MDCT Domain Frame-Loss Concealment Technique for MPEG Advanced Audio Coding", IEEE, ICASSP 2007, pp. 1-273 - 1-276.

4. [4] ITU-T standard G.719, section 7.1, June 2008.

ABBREVIATIONS

[0084]

ASIC	Application Specific Integrated Circuit
CPU	Central Processing Units
DSP	Digital Signal Processor
FEC	Frame Erasure Concealment
FPGA	Field Programmable Gate Array
MDCT	Modified Discrete Cosine Transform
MLT	Modulated Lapped Transform
PLC	Packet Loss Concealment

REFERENCES CITED IN THE DESCRIPTION

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Non-patent literature cited in the description

- **A. ITO et al.** Improvement of Packet Loss Concealment for MP3 Audio Based on Switching of Concealment method and Estimation of MDCT Signs IEEEE, 2010 Sixth International Conference on Intelligent Information Hiding and Multimedia Signal Processing, 518-521 [0083]
- **SANG-UK RYU KENNETH ROSE** An MDCT Domain Frame-Loss Concealment Technique for MPEG Advanced Audio Coding IEEEE, ICASSP 2007, I-273-1276- [0083]

SKJUL AF RAMMEFEJL

PATENTKRAV

1. Fremgangsmåde til at skjule rammefejl baseret på rammer indbefattende transformationskoefficientvektorer, hvilken fremgangsmåde indbefatter følgende trin:
 - 5 tracking (S11) af fortegnændringer mellem tilsvarende transformationskoefficienter af forhåndsbestemte undervektorer, der hver omfatter en flerhed af koefficienter, af konsekutive gode stationære rammer;
akkumulering (S12) af antallet af fortegnændringer i tilsvarende undervektorer af et forhåndsbestemt antal af konsekutive gode stationære rammer;
 - 10 rekonstruktion (S13) af en fejlramme med den seneste gode stationære ramme, men med omvendte fortegn for transformationskoefficienter i undervektorer med et akkumuleret antal af fortegnændringer, der overstiger en forhåndsbestemt tærskel.
2. Fremgangsmåde ifølge krav 1, hvor tærsklen afhænger af det forhåndsbestemte antal af konsekutive gode stationære rammer.
- 15 3. Fremgangsmåde ifølge krav 2, hvor tærsklen tildeles en første værdi for 2 konsekutive gode stationære rammer og en anden værdi for 3 konsekutive gode stationære rammer.
4. Fremgangsmåde ifølge et hvilket som helst af de foregående krav 1-3, hvilken fremgangsmåde indbefatter trinnet med bestemmelse af stationaritet for en modtaget ramme ved bestemmelse af, om den indeholder transienter.
- 20 5. Computerprogram (42) til at skjule rammefejl baseret på rammer indbefattende transformationskoefficientvektorer, hvilket computerprogram omfatter computerlæsbar kode (50, 52, 54), der, når det køres på en processor (22), bevirker, at processoren:
 - 25 tracker (S11; 50) fortegnændringer mellem tilsvarende transformationskoefficienter af forhåndsbestemte undervektorer, der hver omfatter en flerhed af koefficienter, af konsekutive gode stationære rammer;
akkumulerer (S12; 52) antallet af fortegnændringer i tilsvarende undervektorer af et forhåndsbestemt antal af konsekutive gode stationære rammer;
 - 30 rekonstruerer (S13; 54) en fejlramme med den seneste gode stationære ramme, men med omvendte fortegn for transformationskoefficienter i undervektorer med et akkumuleret antal af fortegnændringer, der overstiger en forhåndsbestemt tærskel.
6. Computerprogramprodukt (40), der omfatter et computerlæsbart medium og et computerprogram (42) ifølge krav 5, der er lagret på det computerlæsbare medium.
7. Dekoder (20), der er konfigureret til at skjule rammefejl baseret på rammer indbefattende transformationskoefficientvektorer, hvilken dekode indbefatter:
 - 35 en fortegnændrings-tracker (26), der er konfigureret til at tracke fortegnændringer mellem tilsvarende transformationskoefficienter af forhåndsbestemte undervektorer, der hver omfatter en flerhed af koefficienter, af konsekutive gode stationære rammer;

- 2 -

en fortegnsændringsakkumulator (28), der er konfigureret til at akkumulere antallet af fortegnsændringer i tilsvarende undervektorer af et forhåndsbestemt antal af konsekutive gode stationære rammer;

5 en rammerekonstruktionsanordning (30), der er konfigureret til at rekonstruere en fejlramme med den seneste gode stationære ramme, men med omvendte fortegn for transformationskoefficienter i undervektorer med et akkumuleret antal af fortegnsændringer, der overstiger en forhåndsbestemt tærskel.

8. Brugerterminal (60), der indbefatter en dekoder (20) ifølge krav 7.
9. Brugerterminal (60) ifølge krav 8, hvor brugerterminalen er et brugerudstyr.
10. Brugerterminal (60) ifølge krav 9, hvor brugerudstyret er en mobiltelefon.
- 10 11. Brugerterminal (60) ifølge krav 8, hvor brugerterminalen er en pc.

DRAWINGS

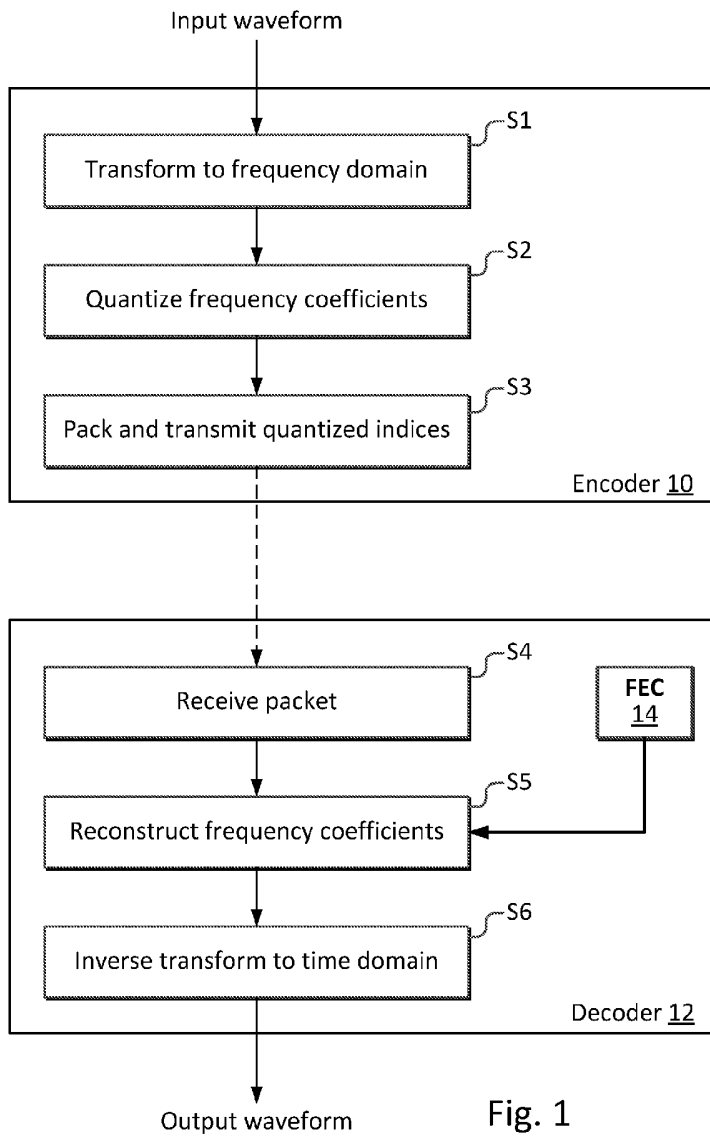


Fig. 1

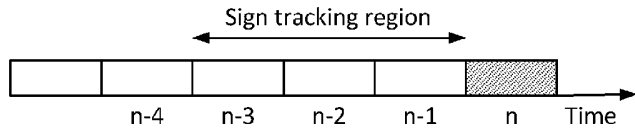


Fig. 2a

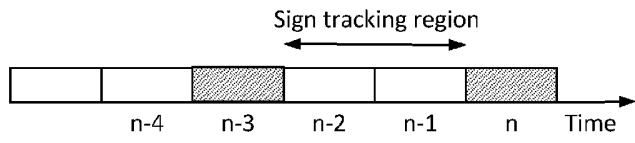


Fig. 2b

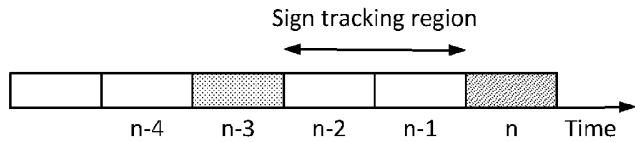


Fig. 2c

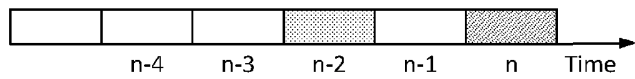


Fig. 3a

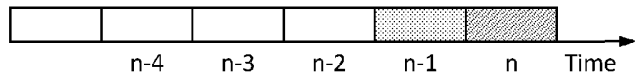


Fig. 3b

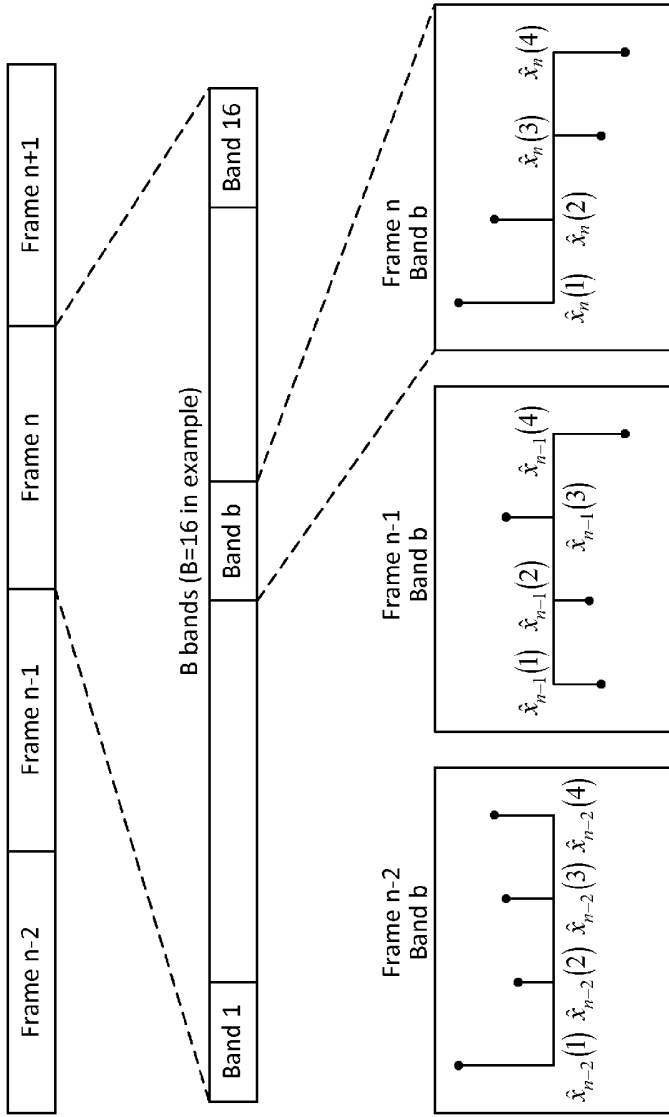


Fig. 4

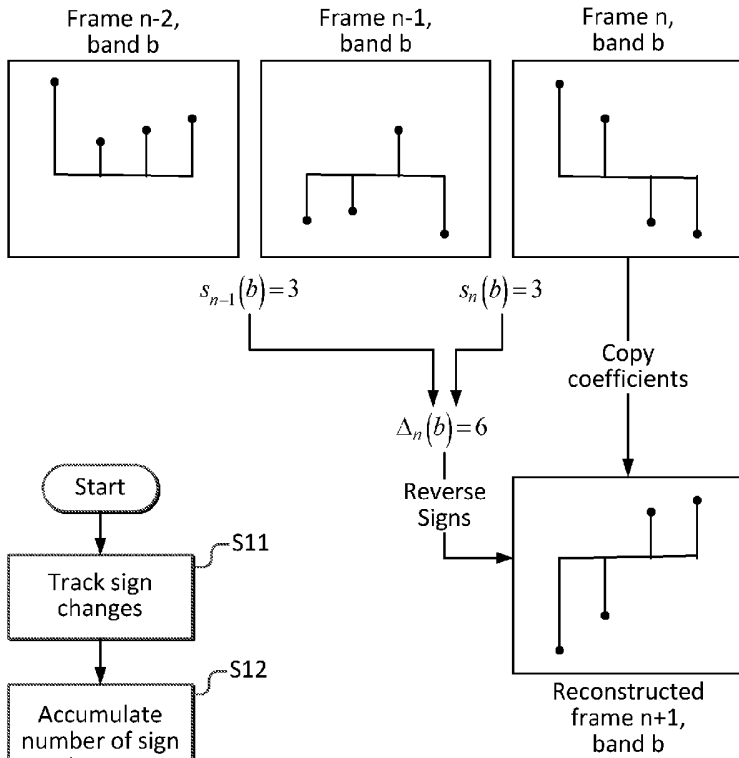


Fig. 5

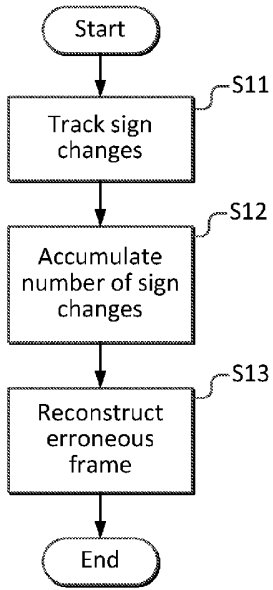


Fig. 6

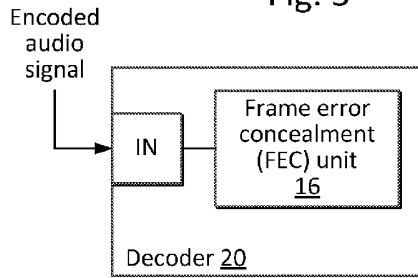
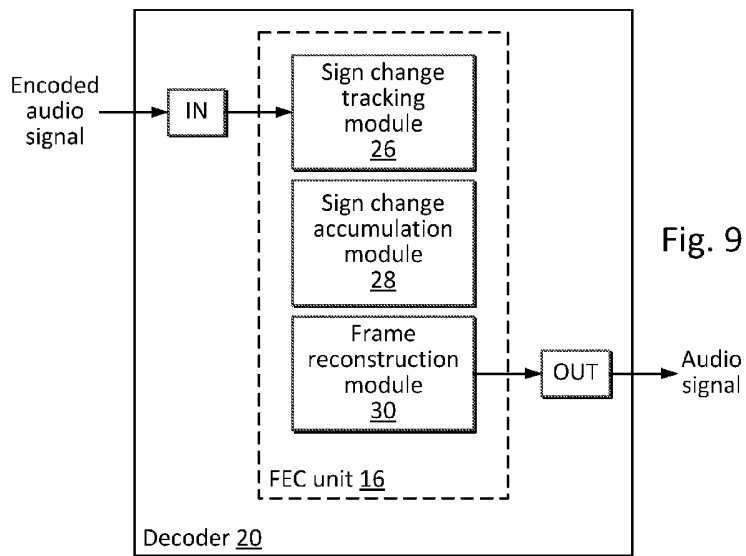
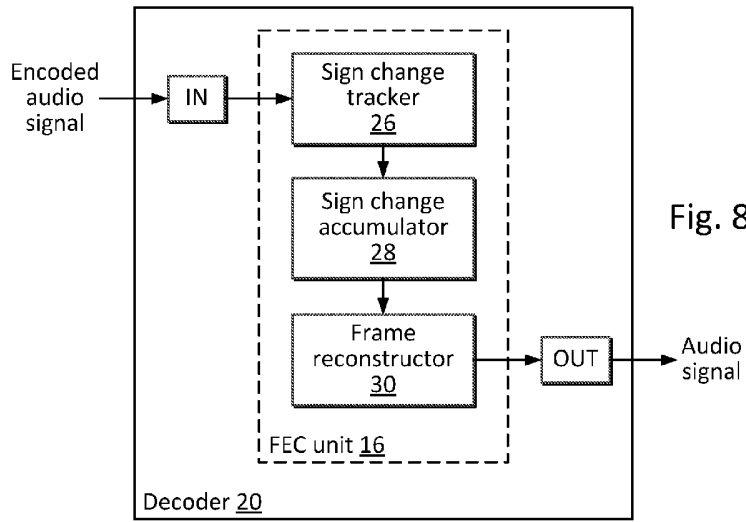


Fig. 7



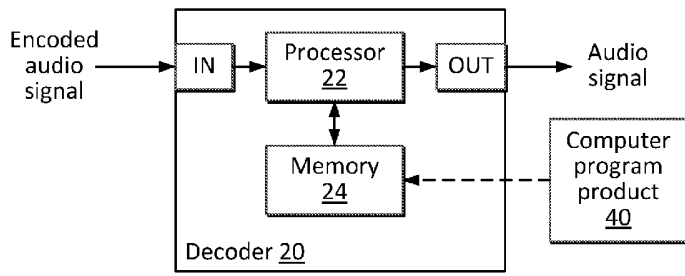


Fig. 10

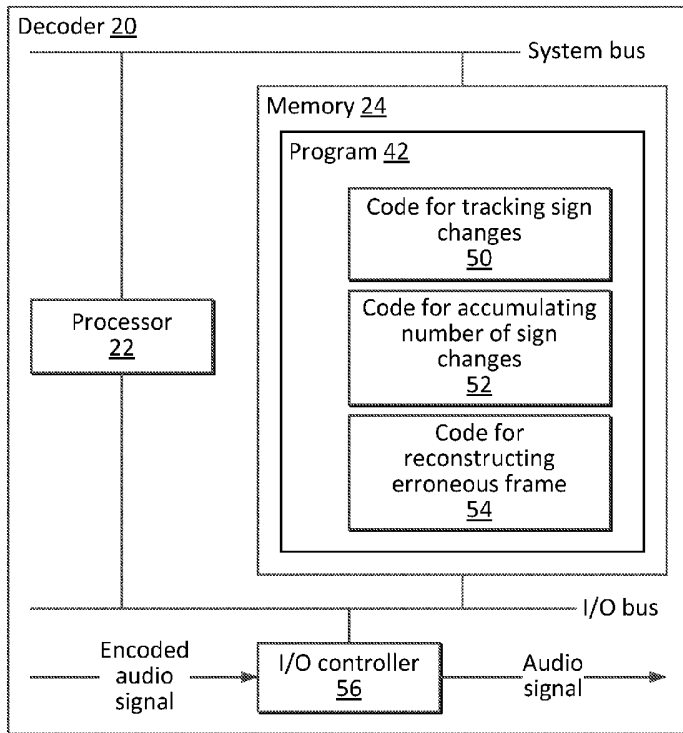


Fig. 11

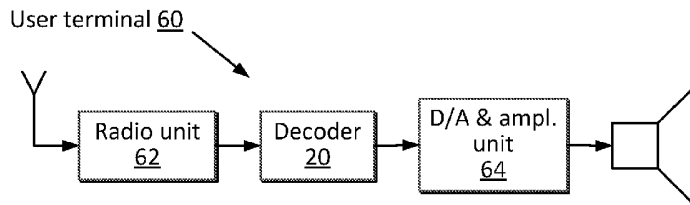


Fig. 12

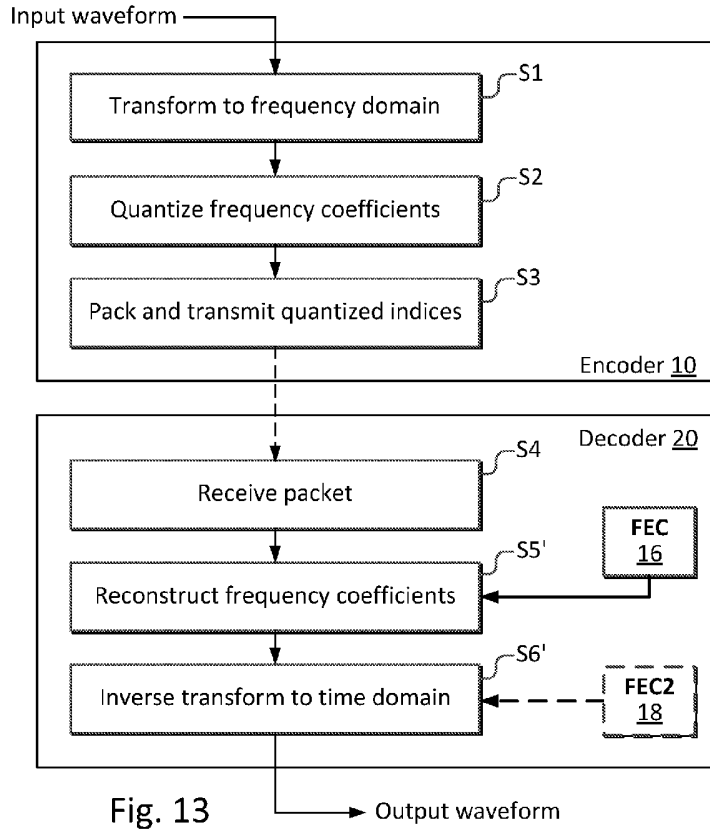


Fig. 13