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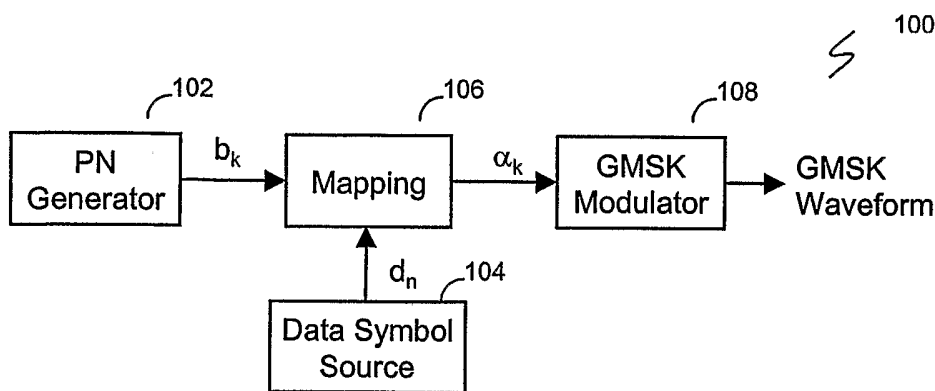
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(54) Title: GMSK SPREAD MODULATION



(57) Abstract: Spread spectrum GMSK signals are presented. Transmission may involve obtaining a sequence of data symbols, obtaining a spread spectrum code comprising a sequence of spread spectrum chips, generating a sequence of pre-modulation chips by combining the sequence of data symbols with the spread spectrum chips, wherein for each data symbol, at least one of the pre-modulation chips is generated by taking into account at least the data symbol and at least one of the spread spectrum chips, performing Gaussian Minimum Shift Keying (GMSK) modulation using the sequence of pre-modulation chips to produce a spread spectrum GMSK signal, and transmitting the spread spectrum GMSK signal.

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GMSK SPREAD MODULATION

CROSS-REFERENCES TO RELATED APPLICATIONS

[0001] This application claims priority from U.S. Provisional Application No. 5 60/515,864; filed October 29, 2003. The 60/515,864 application is incorporated herein by reference.

BACKGROUND OF THE INVENTION

[0002] The present invention relates generally to the field of digital communications 10 and more specifically to modulation and spread spectrum technologies. Spread spectrum techniques have proven to be extremely effective in building multiple access systems, combating or suppressing jamming and interference, transmitting signals at low power, and achieving message privacy from undesired listeners. Spread spectrum technology is characterized by a signal occupying an allocated bandwidth that is much 15 wider than the data rate at which information is communicated. While many of the benefits and fundamental principals of spread spectrum techniques are well-known, actual design of spread spectrum systems can vary widely and face significant challenges in terms of performance, cost, and other considerations.

[0003] Spread spectrum systems commonly involve Phase Shift Keying (PSK) 20 modulation as the modulation technique used in transmitting spread spectrum signals. PSK modulation refers to a general category of modulation schemes that may includes Binary Phase Shift Keying (BPSK), Quadrature Phase Shift Keying (QPSK), and others. PSK modulation schemes are often associated with "spill-over" of signal energy from the allocated bandwidth into adjacent bandwidths. This effect may be 25 reduced by incorporating various envelope, waveform shaping, and/or other techniques. However, the need to improve spectral efficiency of modulated signals remains a fundamental problem in spread spectrum systems. This is especially true in limited bandwidth environments such as wireless and satellite systems, where neighboring signals packed closely together in the frequency spectrum may experience significant 30 interference from one another due to unwanted "spill-over" of each signal outside of its allocated bandwidth.

[0004] Another important consideration in the design of spread spectrum systems is susceptibility to phase noise. In traditional multidimensional QPSK spread spectrum systems, the two dimensional data signals corresponding to in-phase (I) and quadrature (Q) components of the QPSK signal are each spread independently by separate spreading codes. Such independent spreading of the I and Q components means the combined complex signal does not necessarily represent each data symbol using antipodal signaling. This yields a modulation scheme characterized by degraded phase noise sensitivity as compared to antipodal modulation schemes.

[0005] U.S. Patent No. 5,020,075 presents a "chip insertion" technique that utilizes a spread spectrum pseudo-random (PN) sequence in providing input to a minimum-shift-keying (MSK) modulator. In this patent, the PN sequence of chips is periodically interrupted with chips that represent symbols from a differentially encoded stream of data symbols. The resulting pre-modulation chip stream is presented to a minimum-shift-keying (MSK) modulator. To achieve Gaussian minimum-shift-keying (GMSK) modulation, the technique replaces the MSK modulator with a Gaussian filtered MSK modulator. However, this "chip insertion" technique is prone to significant performance degradations. For example, in "Performance of DS/GMSK/PSK Modem Using Four-Phase Correlator", Yano, et. al, IEEE Spread Spectrum Techniques and Applications Proceedings, 1996., pgs. 249-253, the performance of a QPSK chip insertion system is shown to be about 2 dB away from theoretical performance bounds.

[0006] Improvements to current design that address these and other shortcomings would undoubtedly advance spread spectrum technology toward its full potential as a powerful communications methodology.

BRIEF SUMMARY OF THE INVENTION

[0007] The invention presents methods, apparatuses, and systems for transmitting and receiving spread spectrum Gaussian Minimum Shift Keying (GMSK) signals. Transmission may involve obtaining a sequence of data symbols, obtaining a spread spectrum code comprising a sequence of spread spectrum chips, generating a sequence of pre-modulation chips by combining the sequence of data symbols with the spread spectrum chips, wherein for each data symbol, at least one of the pre-modulation chips is generated by taking into account at least the data symbol and at least one of the

spread spectrum chips, performing Gaussian Minimum Shift Keying (GMSK) modulation using the sequence of pre-modulation chips to produce a spread spectrum GMSK signal, and transmitting the spread spectrum GMSK signal. The spread spectrum GMSK signal may adopt antipodal signaling to represent each data symbol.

5 As an example, for each data symbol, if the data symbol takes on a first binary value, a corresponding portion of the spread spectrum GMSK signal exhibits a select waveform, and if the data symbol takes on a second binary value, the corresponding portion of the spread spectrum GMSK signal exhibits a phase-inverted version of the select waveform.

10 **[0008]** Each data symbol may correspond to K spread spectrum chips and K pre-modulation chips, wherein K is a positive integer. A leading one of the K pre-modulation chips corresponding to a current data symbol may be generated by taking into account (i) an immediately preceding data symbol, (ii) a final one of the K spread spectrum chips corresponding to the immediately preceding data symbol, (iii) the
15 current data symbol, and (iv) a leading one of the K spread spectrum chips corresponding to the current data symbol, whereas remaining ones of the K pre-modulation chips corresponding to the current data symbol may each be generated by taking into account (i) an immediately preceding one of the K spread spectrum chips corresponding to the current data symbol, and (ii) a current one of the K spread
20 spectrum chips corresponding to the current data symbol. According to one embodiment, the spread spectrum code utilized is a pseudo-noise (PN) code.

[0009] Reception may involve receiving a spread spectrum GMSK signal corresponding to a sequence of data symbols and a spread spectrum code, processing the received spread spectrum GMSK signal using at least one matched filter to produce
25 at least one filtered signal, correlating the at least one filtered signal with the spread spectrum code corresponding to the spread spectrum GMSK signal to produce at least one correlation output, and evaluating the correlation output to estimate the sequence of data symbols.

BRIEF DESCRIPTION OF THE DRAWINGS

- [0010] Figure 1 is a basic block diagram of a system for transmitting a Gaussian Minimum Shift Keying (GMSK) modulation spread spectrum signal, in accordance with one embodiment of the present invention;
- 5 [0011] Figure 2 illustrates the pulse functions for Minimum Shift Keying (MSK) and the PAM expansion functions $h_0(t)$, $h_1(t)$, and $h_2(t)$ for GMSK;
- [0012] Figure 3 is a table showing an example the mapping of data symbols d_n and spread spectrum code b_k into a pre-modulation chip sequence α_k , in order to produce Laurent expansion PAM symbols $a_{0,k}$ that are antipodal with respect to each data
- 10 symbol d_n ;
- [0013] Figure 4 is a basic block diagram of a demodulator structure for receiving a GMSK modulation spread spectrum signal, in accordance with one embodiment of the present invention;
- [0014] Figure 5 is a plot of computer simulation results for channel BER
- 15 performance versus E_s/N_0 ;
- [0015] Figure 6 is a plot of the calculated loss versus different values of the GMSK BT product;
- [0016] Figure 7 illustrates the calculated power spectral density (PSK) of various modulated signals, including offset QPSK, QPSK, MSK, and GMSK signals of
- 20 different BT products;
- [0017] Figure 8 is a plot showing the constant β tabulated for different values of the BT product; and
- [0018] Figure 9 is a table showing values of a figure of merit (FOM) for various bandwidth definitions and BT products.

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DETAILED DESCRIPTION OF THE INVENTION

Introduction

- [0019] Figure 1 is a basic block diagram of a system 100 for transmitting a spread spectrum Gaussian Minimum Shift Keying (GMSK) signal, in accordance with one
- 30 embodiment of the present invention. As shown in the figure, a data symbol source

block 104 provides a sequence of data symbols d_n , and a pseudo-random or pseudo-noise (PN) generator block 102 provides spread spectrum code b_k to a mapping block 106. The spread spectrum code b_k is also referred to as a sequence of spread spectrum chips. The mapping block 106 utilizes the data symbols d_n and spread spectrum code b_k to produce a sequence of pre-modulation "chips" α_k , which is provided to a GMSK modulator block 108. The GMSK modulator block 108 performs GMSK modulation on the "chip" sequence α_k to produce a GMSK waveform for transmission.

[0020] According to the present embodiment, the mapping block 106 combines data symbols d_n and spectrum code b_k in such a way that the impression of the data symbols on the GMSK waveform produced by the GMSK modulation block 108 results in near antipodal signaling (BPSK). This provides a waveform characteristics of GMSK with data symbol performance and phase error sensitivity similar to that of BPSK. In addition, GMSK modulation allows spectral characteristics and Multiple Access Interference (MAI) performance to be easily traded off by adjustment of the GMSK parameter BT, or BT product, associated with the GMSK modulator block 108. This allows the GMSK waveform to be designed to meet the specific objectives of the system.

[0021] While a PN generator block 102 is shown in this figure, other codes suitable for spread spectrum applications may also be used in accordance with the present invention. Further, data symbols d_n may broadly refer to any sequence of symbols representing data. For example, data symbols d_n may represent information symbols that have not been error correction encoded. Alternatively, data symbols d_n may represent error correction encoded symbols, such as Forward Error Correction (FEC) encoded symbols.

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GMSK Description

[0022] According to the present embodiment, the input to the GMSK modulator block 108 is the "chip" sequence, α_k . Here, the chips α_k take on binary values of +1 or -1. For modulator synthesis, GMSK may be described as a Continuous Phase Modulation (CPM) technique. The GMSK waveform may be defined as:

30

$$s(t) = A \cos \left(2\pi f t + 2\pi h \sum_{k=0}^m \alpha_k q(t - kT_c) \right) \quad mT_c \leq t \leq (m+1)T_c \quad (1)$$

where f is the channel center frequency plus any requested frequency offset, T_c is the chip duration (1/chip rate), $h = 0.5$, and

$$q(t) = \int_{-\infty}^t g(x) dx \quad g(t) = \frac{1}{2T_c} [Q(\sigma t_1) - Q(\sigma t_2)] \quad (2)$$

$$5 \quad t_1 = \frac{t}{T_c} - \left(\frac{L+1}{2} \right) \quad t_2 = \frac{t}{T_c} - \left(\frac{L-1}{2} \right) \quad \sigma = \frac{2\pi B T_c}{\sqrt{\ln(2)}}$$

the function $Q(t)$ is the standard Q function and is defined by:

$$Q(t) = \frac{1}{\sqrt{2\pi}} \int_t^{\infty} e^{-x^2/2} dx \quad (3)$$

[0023] Here, the product $B T_c$ is used to correspond to the chip duration T_c . The product $B T_c$ serves as a parameter that can be varied to trade off better spectral performance (lower $B T_c$) vs. better detection efficiency (higher $B T_c$). To simplify implementation, it can be roughly assumed for purposes of calculation that the frequency pulse, $g(t)$ is only non-zero for a duration of about L chips ($0 \leq t \leq L T_c$). Similarly, the phase pulse $q(t)$ is essentially 0 for $t < 0$ and essentially equal to $1/2$ for $t > L T_c$. As an example, with $B T_c = 0.25$, the value of L is 4. For $B T_c = 0.20$, the value of L should be increased to 5.

[0024] The description of $q(t)$ in equation (2) is convenient for modulator realizations using a Frequency Modulation technique (such as a VCO or NCO). The integral in the description of $q(t)$ above can be computed to get a direct expression for $q(t)$:

$$q(t) = \frac{1}{2} \left[1 + t_1 Q(\sigma t_1) - t_2 Q(\sigma t_2) - \frac{1}{\sigma \sqrt{2\pi}} \left(e^{-\frac{1}{2}\sigma^2 t_1^2} - e^{-\frac{1}{2}\sigma^2 t_2^2} \right) \right] \quad (4)$$

20 [0025] This form is convenient for a realization using Phase Modulation synthesis techniques.

Laurent Expansion

[0026] To determine the mapping structure that gives the spread spectrum GMSK signal a BPSK nature to it, one may examine the Laurent PAM representation, described in “Exact and Approximate Construction of Digital Phase Modulations by Superposition of Amplitude Modulated Pulses” . P.A. Laurent, IEEE Transactions
 5 Communications, vol. COM-34, 1986. Specifically, the baseband CPM signal consisting of K chips can be represented as the sum of 2L-1 PAM signals:

$$s(t) = e^{j\theta(t)} = \sum_{l=0}^{2^{L-1}-1} \sum_{k=0}^{K-1} a_{l,k} h_l(t - kT_c) \tag{5}$$

[0027] For many values of the BT_c product, the main pulse (corresponding to $l=0$) carries most of the signal energy. In this case, the only relevant pulse shapes and PAM
 10 data sequences are:

$$h_0(t) = \frac{1}{\sqrt{T_c}} \prod_{i=1}^L c(t - iT_c) \tag{6}$$

$$a_{0,k} = j^{\left(\sum_{i=0}^k \alpha_i\right)} = a_{0,k-1} j^{\alpha_k} \tag{7}$$

[0028] The basic function $c(t)$ is defined by:

$$c(t) = \sin\left[\frac{\pi}{2}(1 - 2q(t))\right] \quad -LT_c \leq t \leq LT_c \tag{8}$$

[0029] Expressions for the other pulse functions and data symbols can be found in the appendix of “Simple Coherent receivers for Partial response Continuous Phase Modulation”, G.K. Kaleh, IEEE Journal on Selected Areas in Communications, Vol 7, No 9, Dec 1989. From the recursion formula in equation (7), the PAM symbols are
 20 imaginary for n even and real for n odd.

[0030] Figure 2 illustrates the pulse functions, $h_l(t)$, for Minimum Shift Keying (MSK) and GMSK. For MSK, $q(t) = \frac{1}{2} \frac{t}{T_c}$ for $0 < t < T_c$, $= 0$ for $t < 0$, and $= 1/2$ for $t > T_c$ and $L=1$. The function $h_0(t)$ has a duration of $2T_c$ and is shown in Figure 2. The result

is the standard offset QPSK view of MSK with sinusoidal pulse shaping and data mapping given by equation (7). Since the PAM data sequence $a_{0,k}$ alternates between real and imaginary values, little or no intersymbol interference is expected.

[0031] For GMSK with a BT_c of 0.25, an acceptable value of L is 4. The duration of the main Laurent pulse is $5T_c$. The pulse shapes for the $h_0(t)$, $h_1(t)$, and $h_2(t)$ Laurent pulses shown in the figure above. Only the first two pulses are significant. Also, since the duration of the main pulse is larger than $2T_c$, this waveform will produce intersymbol interference.

10 Symbol Mapping

[0032] The objective for the mapping function is to combine the data symbols d_n with the PN sequence b_k to produce a pre-modulation chip sequence α_k such that the Laurent PAM expansion results in antipodal signaling for the main PAM pulse, $h_0(t)$. In particular, it is desired that all the values of $a_{0,k}$ to be inverted during data symbol n , but only during data symbol n , if d_n is inverted. An additional objective is to have a simple relationship between the data values for the main Laurent pulse ($a_{0,k}$) and the spread spectrum chips (b_k). These objectives can be accomplished for the K chips within symbol interval n by using the following mapping:

$$\alpha_m = \alpha_{k+(n-1)K} = (-1)^m (\tilde{b}_{-1} \tilde{d}_{n-1}) \cdot (\tilde{b}_0 \tilde{d}_n) \quad k=0 \quad (9)$$

$$\alpha_m = \alpha_{k+(n-1)K} = (-1)^m \tilde{b}_{k-1} \tilde{b}_k \quad k>0$$

where $\tilde{b}_k = 2b_k - 1$ is the translation of the logic values of 0 and 1 to numerical values of -1 and 1 respectively. Likewise for $\tilde{d}_n = 2d_n - 1$. The term \tilde{b}_{-1} is the last PN chip associated with data symbol d_{n-1} (the previous symbol). For the first symbol in the packet (d_0), the previous symbol, d_{-1} , and the last PN chip of the previous symbol, b_{-1} , are both initialized to 1. The variable m is a chip counter that starts at 0 at the beginning of transmission and does not get reset at the end of any symbol. The variable k is the chip count within a symbol, ranging from 0 to $K-1$. In another embodiment of the present invention, the alternate inversion performed by the $(-1)^m$ term can be equivalently implemented in the receiver. In other words, the $(-1)^m$ term in Equation 9

may be omitted in generating the pre-modulation chips at the transmitter end. Instead the $(-1)^m$ term may be incorporated into the receiver structure.

[0033] Figure 3 is a table showing an example of such mapping of data symbols d_n and spread spectrum code b_k into a pre-modulation chip sequence α_k , in order to
5 produce Laurent expansion PAM symbols $a_{0,k}$ that are antipodal with respect to each data symbol d_n . For purposes of illustration, $K=4$ chips per symbol is shown. The table illustrates the mapping of the sequence of data symbols 0-1-1 using a particular PN sequence, then illustrates the mapping of the sequence of data symbols 1-0-1 using the same PN sequence. As shown, the Laurent expansion PAM symbols exhibit antipodal
10 signaling with respect to each data symbol. The first symbol of the two sequences are different, so their corresponding PAM expansions $\{-j, 1, -j, 1\}$ and $\{+j, -1, +j, 1\}$ are opposite. The second symbol of both sequences are also different, again yielding the two opposite PAM expansions $\{-j, 1, -j, 1\}$ and $\{+j, -1, +j, -1\}$. Finally, the third symbol of both sequences are the same, yielding the identical PAM expansion $\{+j, 1, -j, -1\}$. The simple relationship between the PN chips and the I and Q values of the
15 PAM symbols is also shown in the table. In particular, the 4 PN chips for the first data symbol are $\{1, 0, 1, 1\}$ and the four I and Q values for the PAM symbols are $\{-1, 1, -1, -1\}$. In all cases, the I and Q values for the PAM symbols are just the exclusive OR of the PN chip and the data symbol with the result shifted up to values of $+1$ or -1 by
20 multiplying by 2 and subtracting 1.

[0034] In the present embodiment of the invention, the pre-modulation chip sequence α_k is generated such that the Laurent PAM expansion results in antipodal signaling for the main PAM pulse, $h_0(t)$. Since the main PAM pulse represents most, but not all, of the energy in the baseband CPM signal, the resulting spread spectrum GMSK signal is
25 antipodal in a general sense. That is, the resulting spread spectrum GMSK signal may be antipodal to an extent that satisfies performance requirements, but it may not be perfectly antipodal. Thus, for each data symbol, if the data symbol takes on a first binary value, a corresponding portion of the spread spectrum GMSK signal exhibits a select waveform, and if the data symbol takes on a second binary value, the
30 corresponding portion of the spread spectrum GMSK signal exhibits a phase-inverted version of the select waveform. The phase-inverted version of the select waveform may be a nearly exact, but not perfectly exact, phase-inverted replica of the select waveform.

Demodulator Structure

[0035] Figure 4 is a basic block diagram of a demodulator structure 110 for receiving a spread spectrum GMSK signal, in accordance with one embodiment of the present invention. For antipodal signaling, an optimum symbol-by-symbol demodulator correlates the received signal with the expected signal under the hypothesis, $d_n=1$, and compares the real part of the output to zero (after phase rotation to remove the phase error). The correlator output is for the n^{th} symbol is,

$$Z_n = \int r(t)[x_n(t) + jy_n(t)]^* dt \quad (10)$$

[0036] The signal $r(t)$ is the received complex valued (I and Q components) baseband signal, $x(t)$ is the real part of the expected signal with $d_n=1$ and $y(t)$ is the imaginary part of the expected signal. The integration is performed over to time duration, which is occupied by one symbol of K chips. The simple demodulator structure 100 can be derived by assuming all the PAM pulses to be negligible except the main pulse $h_0(t)$. By using this assumption, the expected signal over the n^{th} symbol interval can be well approximated by

$$x_n(t) + jy_n(t) = \sum_{k=0}^{K-1} a_{0,k} h_0(t - kT_c) = \sum_{\substack{k'=0 \\ k'=\text{odd}}}^{K-1} x_{nK+k'} h_0(t - (nK + k')T_c) + j \sum_{\substack{k'=0 \\ k'=\text{even}}}^{K-1} y_{nK+k'} h_0(t - (nK + k')T_c) \quad (11)$$

$$x_k = \text{Re}\{a_{0,k}\} \quad y_k = \text{Im}\{a_{0,k}\}$$

thus the correlation in equation (10) becomes,

$$Z_n = \sum_{k'=\text{odd}} x_k r_k - j \sum_{k'=\text{even}} y_k r_k \quad k = nK + k' \quad (12)$$

where r_k is the output (sampled once per chip) of a filter matched to the main Laurent pulse,

$$r_k = \int r(t) h_0(t - kT_c) \quad (13)$$

[0037] Referring to Figure 4, received signal(s) are provided to one or more filters 112 matched to the main Laurent pulse $h_0(t)$. The sampled filtered output r_k is provided to a de-multiplexer block 114, which outputs separated even and odd samples of r_k .

The even and odd samples of r_k are provided to correlator blocks 116. A PN sequence block 118 generates the same spread spectrum code b_k originally used by the transmitter to generate the spread spectrum GMSK signal. The spread spectrum code b_k is provided to a de-multiplexer 120, which outputs separated even samples y_k and odd samples x_k . The even and odd samples y_k and x_k are also provided to the correlator blocks 116.

[0038] In this manner, correlator blocks 116 correlate the received signal with the expected signal under the hypothesis $d_n=1$. This results in correlation outputs Z_{even} and Z_{odd} , which are combined at a combiner block 122 to produce a complex result Z . The complex result Z is provided to a phase rotation block 124, which performs a phase rotation operation to compensate for any phase error. This results in an output Z_{rot} . The phase-rotated output Z_{rot} may then be used to estimate the data symbols carried in the spread spectrum GMSK signal.

[0039] When timing and phase is not known, such as during the preamble (burst acquisition), the sample rate out of the chip matched filter, $h_o(t)$, may be increased to allow several different timing hypothesis to be attempted. A sample rate of $2R_c$ may enable 4 timing hypothesis per main Laurent pulse (one I or Q channel pulse per two chips). In addition, a non-coherent metric, such as $|Z|^2$, may be used to accommodate the unknown, random phase.

20

Performance

[0040] For large channel bit error rates (BERs), the channel BER of the spread spectrum GMSK waveform can be accurately approximated (details omitted) by,

$$P_b = Q \left(\sqrt{\frac{2 \frac{E_s}{N_o} H_0(0)}{1 + 4 \frac{E_s}{N_o} \frac{\alpha}{K^2 H_0(0)} + 2 \frac{E_s}{N_o} \frac{\beta}{K \cdot H_0(0)}}} \right) \tag{14}$$

[0041] Comparing equation (14) to the standard BER expression for coherent BPSK, one can see that degradation occurs due to the term $H_0(0)$ in the numerator and the second two terms in the denominator. The term $H_0(0)$ is the energy in the first Laurent Pulse, $h_o(t)$. This value is slightly less than unity. The second term in the denominator

is due to the interference with the $h_o(t)$ pulses from the chips in the two adjacent symbols. The third term is the interference with the higher order Laurent pulses from the chips in the current symbol as well as the adjacent symbols. The terms α and β are constants which are functions of the cross correlation of the Laurent pulses.

- 5 [0042] By examining equation (14), the interference terms in the denominator get smaller as K gets larger and get larger as the interferer signal to noise ration (E_s/N_o) gets larger. Thus it is expected that the degradation to be larger at higher E_s/N_o and to be independent of E_s/N_o as K gets large. For large values of K the performance approaches that of theoretical coherent BPSK.
- 10 [0043] Figure 5 is a plot of computer simulation results for channel BER performance versus E_s/N_o . The simulations were performed for a BT product of 0.10. These results confirm the analytical observations regarding equation (14). As expected, the degradation becomes fixed for all values of E_s/N_o as the number of chips per symbol (K) increases to 24. At this point, the second two terms in the denominator are
- 15 negligible. In fact, at the E_s/N_o range of interest ($-4 \text{ dB} \leq E_s/N_o \leq 0 \text{ dB}$), the inter-chip interference terms are negligible even for the minimum value of K (4 chips per symbol). Therefore, the degradation with respect to theoretical coherent BPSK in the interesting range of E_s/N_o can be accurately approximated as just the energy in the first Laurent pulse, $h_o(t)$. This approximation is expected to hold for all values of K . Figure
- 20 6 is a plot of the calculated loss, with respect to theoretical coherent BPSK, versus different values of the GMSK BT product.

Spread Spectrum GMSK Spectral Characteristics

- [0044] Another dimension to the selection of the BT product is the spectral
- 25 occupancy. Figure 7 illustrates the calculated power spectral density (PSK) of various modulated signals, including offset QPSK, QPSK, MSK, and GMSK signals of different BT products. These results are calculated using analytical techniques. Ideally, the spectrum of the transmitted signal would have a rectangular "boxcar" shape. This would allow one to place all of the signal energy in the allocated
- 30 bandwidth and none outside of the allocated bandwidth. No known constant envelope signal has this characteristic. As demonstrated in the figure, the GMSK signal family

possesses roll-off which is much faster than that other constant envelop waveforms considered.

[0045] Spectral characteristics can be further improved by using several FDMA channels, each of smaller chip rate and consisting of, for example many Code Division
 5 Multiple Access (CDMA) waveforms, to support the network traffic instead one single channel of a larger chip rate. An example will illustrate this best. Suppose one has purchased 30 MHz of bandwidth and it is required that the PSD outside of the
 purchased bandwidth be 30 dB down. First consider the use of a single spread
 spectrum GMSK signal using a BT product of 0.25. When a BT product of 0.25 is
 10 used, the 30 dB bandwidth (double sided) of the signal is about 1.3 times the chip rate. The maximum chip rate that could be supported is 23.08 MHz.

[0046] Now consider the use of N FDMA channels in the 30 MHz purchased bandwidth. The signals in each channel uses spread spectrum GMSK modulation with
 a BT product of 0.25. The spacing between channels is set to be 0.75 times the chip
 15 rate. At this spacing the PSD's of adjacent channels cross at the -10 dB point. This is conservative and in all likelihood, a smaller spacing between channels could be used. The composite spectrum of N channels will have a 30 dB bandwidth of,

$$W = (N - 1)\Delta R_c + BW_{30} R_c \quad (15)$$

where W is in Hz, ΔR_c is the channel spacing, and BW_{30} is the 30 dB bandwidth of a
 20 single GMSK signal normalized by the chip rate. Suppose there are N=10 channels. The spacing Δ is 0.75, and $BW_{30} = 1.3$. Thus the 30 dB bandwidth of sum of all signals is 8.05 time the chip rate. The maximum chip rate for each signal will be 3.73 MHz
 (=30 MHz/8.05). Since there are 10 FDMA channels, the effective chip rate for this
 system is 37.27 MHz, a factor of 1.6 greater than the example with 1 FDMA channel.

[0047] As seen in the example above, one can gain more physical layer capacity by
 using several FDMA channels each of smaller chip rate. The gain is due to the
 resulting composite spectrum looking more like the ideal "boxcar" spectrum. The more
 FDMA channels used, the more ideal the spectrum will look. However, there is a
 diminishing return on increasing N, so the question is how many big should N be.
 25
 30 Using a normalized channel spacing (normalized by the chip rate) of Δ and a
 normalized 30 dB bandwidth of BW_{30} , the maximum chip rate that can be supported by

a single FDMA channel approach is W/BW_{30} where W is the allocated bandwidth. Using N FDMA carriers, the maximum chip rate that can be supported by each carrier is $R_c = W/[(N-1)\Delta + BW_{30}]$. The effective chip rate, due to all N channels, will be the N times as large. The ratio of the multi channel effective chip rate to the single channel
 5 chip rate determines the capacity gain (as previously shown, the capacity of a CDMA system is proportional to the chip rate)

$$Gain = \left[\left(\frac{N-1}{N} \right) \frac{\Delta}{BW_{30}} + \frac{1}{N} \right]^{-1} \quad (16)$$

[0048] As N becomes large, the gain goes to BW_{30}/Δ . This represents the maximum possible gain. Selection of $N=5$ will yield more than 85% of the maximum achievable
 10 gain. Selection of $N=10$ will more than 90% of the maximum achievable gain.

Multiple Access Interference (MAI)

[0049] The final dimension in the selection of BT product is the performance of the spread spectrum GMSK signal in the presence of MAI. GMSK with different values of
 15 BT will have different BER performance to the same levels of MAI. Intuitively, one would expect that use of GMSK with higher value BT would provide more resistance to MAI than lower of BT values since the occupied bandwidth is larger for the same chip rate. This is indeed the case, and the relationship has been quantified using the approach described below.

20 [0050] For N , asynchronous MAI interfering sources (all of the same power level), the classical equation for relating N to E_b/N_0 is

$$\frac{E_b}{I_o} = \frac{W/R_b}{N} = \frac{\beta R_c/R_b}{N} \quad (17)$$

[0051] The only ambiguity is the definition of the bandwidth W . The bandwidth may be defined as $W=\beta R_c$. The constant β is a function of the BT product. A larger value
 25 of β results in more MAI resistance since the resulting E_b/I_o is larger. The value of β is defined such that the variance of the MAI out of the GMSK matched filters is identical to the variance of the thermal noise out of the GMSK matched filters when the

E_b/N_0 is set to a level of $\beta R_c/(R_b N)$. This allows the total $E_b/(N_0+I_0)$ to be computed as the parallel combination of E_b/N_0 and E_b/I_0 .

[0052] As an example, suppose that the variance of the MAI out of the GMSK matched filter is measured to be σ^2 when R_c/R_b is 100 and N is 50. The variance out of the GMSK matched filters is also measured to be σ^2 when the E_b/N_0 is set to 2.5 dB. Thus the performance with the above MAI and no thermal noise scenario is identical (in terms of the second order statistics out of the matched filter) to that of the thermal noise only at an E_b/N_0 of 2.5 dB. To aim is to select a constant β such that the resulting E_b/I_0 is 2.5 dB. This results in value of $\beta=0.8891$. Figure 8 is a plot showing the constant β tabulated for different values of the BT product.

Optimal Selection of BT Product

[0053] Figure 9 is a showing values of a figure of merit (FOM) for various bandwidth definitions and BT products. Here, the occupied bandwidth of a single GMSK signal is $W=\alpha R_c$, where the definition of occupied bandwidth corresponds to some PSD bandwidth, such as 10 dB bandwidth or 30 dB bandwidth. As previously shown, the CDMA capacity is proportional to β/α . Thus, a good FOM for optimizing the BT selection is β/α , which is illustrated in the table. A larger FOM indicates a better waveform choice. For comparison purposes, regular MSK is also shown. The bandwidth definition is the ratio of the PSD at the edge of band to the PSD at band center.

[0054] When it is required for the PSD of the signal to be very low out of band, like 40 or 60 dB lower than center of band, a small value of the BT product of about 0.2 will optimize the capacity. When a larger out of band PSD can be tolerated, a higher value of BT, or even MSK, should be used. For a 30 dB out of band PSD, the optimum value of BT is about 0.5. For certain multi-channel applications, carriers will be spaced as close together as possible. The bandwidth of interest for these carriers is more like a -10 dB, or greater bandwidth. Thus one would think that plain MSK might be the best modulation choice. However, it may be desired that the PSD of the two edge channels roll off faster since they will determine the bandwidth occupancy of the composite return channel (all FDMA channels). Regulatory or other constraints may dictate, for example, that the relevant bandwidth definition for the two edge channels be around 30

dB, which indicates an optimum selection of $BT=0.5$. By looking at the table, it is clear that there is very little degradation in the FOM by using a BT product of 0.5 when the PSD roll off of interest is in the -6 to -20 dB range. In practice, one might want to use a BT product slightly smaller than 0.5 to compensate for some small amount
5 spectral expansion in the modulator implementation. Thus, in one embodiment of the present invention, the optimum selection of BT product will generally lie in the 0.4 to 0.5 range.

[0055] While the present invention has been described in terms of specific embodiments, it should be apparent to those skilled in the art that the scope of the
10 present invention is not limited to the described specific embodiments. The specification and drawings are, accordingly, to be regarded in an illustrative rather than a restrictive sense. It will, however, be evident that additions, subtractions, substitutions, and other modifications may be made without departing from the broader spirit and scope of the invention as set forth in the claims.

15

WHAT IS CLAIMED IS:

- 1 1. A method for transmitting a modulated signal comprising:
2 obtaining a sequence of data symbols;
3 obtaining a spread spectrum code comprising a sequence of spread
4 spectrum chips;
5 generating a sequence of pre-modulation chips by combining the
6 sequence of data symbols with the spread spectrum chips, wherein for each data
7 symbol, at least one of the pre-modulation chips is generated by taking into account at
8 least the data symbol and at least one of the spread spectrum chips;
9 performing Gaussian Minimum Shift Keying (GMSK) modulation using
10 the sequence of pre-modulation chips to produce a spread spectrum GMSK signal; and
11 transmitting the spread spectrum GMSK signal.
- 1 2. The method of claim 1 wherein the spread spectrum GMSK
2 signal adopts antipodal signaling to represent each data symbol.
- 1 3. The method of claim 2 wherein for each data symbol, if the data
2 symbol takes on a first binary value, a corresponding portion of the spread spectrum
3 GMSK signal exhibits a select waveform, and if the data symbol takes on a second
4 binary value, the corresponding portion of the spread spectrum GMSK signal exhibits a
5 phase-inverted version of the select waveform.
- 1 4. The method of claim 1 wherein each data symbol corresponds to
2 K spread spectrum chips and K pre-modulation chips, wherein K is a positive integer.
- 1 5. The method of claim 4 wherein a leading one of the K pre-
2 modulation symbols corresponding to a current data symbol is generated by taking into
3 account
4 i. an immediately preceding data symbol;
5 ii. a final one of the K spread spectrum chips corresponding to the
6 immediately preceding data symbol;
7 iii. the current data symbol; and
8 iv. a leading one of the K spread spectrum chips corresponding to
9 the current data symbol; and

10 wherein remaining ones of the K pre-modulation symbols corresponding
11 to the current data symbol are each generated by taking into account

12 i. an immediately preceding one of the K spread spectrum chips
13 corresponding to the current data symbol; and

14 ii. a current one of the K spread spectrum chips corresponding to
15 the current data symbol.

1 6. The method of claim 5 wherein even ones and odd ones of the
2 remaining pre-modulation symbols corresponding to the current data symbol are
3 generated using opposite signs.

1 7. The method of claim 1 wherein the spread spectrum GMSK
2 signal is capable of being approximated as a pulse amplitude modulation (PAM) signal.

1 8. The method of claim 7 wherein even ones and odd ones of the
2 sequence of pre-modulation chips separately correspond to in-phase (I) and quadrature
3 (Q) components of the PAM signal.

1 9. The method of claim 1 wherein the spread spectrum code is a
2 pseudo-noise (PN) code.

1 10. The method of claim 1 wherein the GMSK modulation adopts a
2 BT product within a range from 0.4 to 0.5.

1 11. A method for receiving a modulated signal comprising:
2 receiving a spread spectrum GMSK signal corresponding to a sequence
3 of data symbols and a spread spectrum code;
4 processing the received spread spectrum GMSK signal using at least one
5 matched filter to produce at least one filtered signal;
6 correlating the at least one filtered signal with the spread spectrum code
7 corresponding to the spread spectrum GMSK signal to produce at least one correlation
8 output; and
9 evaluating the correlation output to estimate the sequence of data
10 symbols.

1 12. The method of claim 11 wherein the at least one matched filter is
2 matched to a main Laurent pulse corresponding to an approximation of the spread
3 spectrum GMSK signal.

1 13. The method of claim 11 further comprising a step of sampling
2 the at least one filtered signal before the correlating step.

1 14. The method of claim 13 wherein even ones and odd ones of the
2 at least one filtered and sampled signals are separately correlated with even and odd
3 portions of the spread spectrum code corresponding to the spread spectrum GMSK
4 signal to produce even and odd correlated signals.

1 15. The method of claim 14 wherein the even and odd correlated
2 signals are combined into a complex signal before the evaluating step.

1 16. The method of claim 15 wherein the complex signal is phase-
2 adjusted to compensate for a phase differential before the evaluating step.

1 17. An apparatus for transmitting a modulated signal comprising:
2 a data processing unit capable of receiving a sequence of data symbols
3 and combining the sequence of data symbols with a spread spectrum code comprising a
4 sequence of spread spectrum chips to produce a sequence of pre-modulation chips,
5 wherein for each data symbol, at least one of the pre-modulation chips is generated by
6 taking into account at least the data symbol and at least one of the spread spectrum
7 chips; and
8 a modulator coupled to the data processing unit and configured to
9 perform Gaussian Minimum Shift Keying (GMSK) modulation on the sequence of pre-
10 modulation chips to produce a spread spectrum GMSK signal.

1 18. The apparatus of claim 17 wherein the spread spectrum GMSK
2 signal adopts antipodal signaling to represent each data symbol.

3 19. The apparatus of claim 18 wherein for each data symbol, if the
4 data symbol takes on a first binary value, a corresponding portion of the spread
5 spectrum GMSK signal exhibits a select waveform, and if the data symbol takes on a

6 second binary value, the corresponding portion of the spread spectrum GMSK signal
7 exhibits a phase-inverted version of the select waveform.

1 20. The apparatus of claim 17 wherein each data symbol corresponds
2 to K spread spectrum chips and K pre-modulation chips, wherein K is a positive
3 integer.

1 21. The apparatus of claim 20 wherein a leading one of the K pre-
2 modulation chips corresponding to a current data symbol is generated by taking into
3 account

- 4 i. an immediately preceding data symbol;
- 5 ii. a final one of the K spread spectrum chips corresponding to the
6 immediately preceding data symbol;
- 7 iii. the current data symbol; and
- 8 iv. a leading one of the K spread spectrum chips corresponding to
9 the current data symbol; and

10 wherein remaining ones of the K pre-modulation chips corresponding to
11 the current data symbol are each generated by taking into account

- 12 i. an immediately preceding one of the K spread spectrum chips
13 corresponding to the current data symbol; and
- 14 ii. a current one of the K spread spectrum chips corresponding to
15 the current data symbol.

1 22. The apparatus of claim 21 wherein even ones and odd ones of the
2 remaining pre-modulation chips corresponding to the current data symbol are generated
3 using opposite signs.

1 23. The apparatus of claim 17 wherein the spread spectrum GMSK
2 signal is capable of being approximated as a pulse amplitude modulation (PAM) signal.

1 24. The apparatus of claim 23 wherein even ones and odd ones of the
2 sequence of pre-modulation chips separately correspond to in-phase (I) and quadrature
3 (Q) components of the PAM signal.

1 25. The apparatus of claim 17 wherein the spread spectrum code is a
2 pseudo-noise (PN) code.

1 26. The apparatus of claim 17 wherein the GMSK modulation adopts
2 a BT product within a range from 0.4 to 0.5.

1 27. An apparatus for receiving a modulated signal comprising:
2 at least one matched filter configured to receive a spread spectrum
3 GMSK signal corresponding to a sequence of data symbols and a spread spectrum code
4 and produce at least one filtered signal;
5 at least one correlator for correlating the at least one filtered signal with
6 the spread spectrum code corresponding to the spread spectrum GMSK signal to
7 produce at least one correlation output; and
8 a decision unit for evaluating the correlation output to estimate the
9 sequence of data symbols.

1 28. The apparatus of claim 27 wherein the at least one matched filter
2 is matched to a main Laurent pulse corresponding to an approximation of the spread
3 spectrum GMSK signal.

1 29. The apparatus of claim 27 further comprising a sampler for
2 sampling the at least one filtered signal before the correlating step.

1 30. The apparatus of claim 29 wherein even ones and odd ones of the
2 at least one filtered and sampled signals are separately correlated with even and odd
3 portions of the spread spectrum code corresponding to the spread spectrum GMSK
4 signal to produce even and odd correlated signals.

1 31. The apparatus of claim 30 wherein the even and odd correlated
2 signals are combined into a complex signal before the evaluating step.

1 32. The apparatus of claim 31 wherein the complex signal is phase-
2 adjusted to compensate for a phase differential before the evaluating step.

1 33. A system for transmitting a modulated signal comprising:
2 means for obtaining a sequence of data symbols;
3 means for obtaining a spread spectrum code comprising a sequence of
4 spread spectrum chips;

5 means for generating a sequence of pre-modulation chips by combining
6 the sequence of data symbols with the spread spectrum chips, wherein for each data
7 symbol, at least one of the pre-modulation chips is generated by taking into account at
8 least the data symbol and at least one of the spread spectrum chips;
9 means for performing Gaussian Minimum Shift Keying (GMSK)
10 modulation using the sequence of pre-modulation chips to produce a spread spectrum
11 GMSK signal; and
12 means for transmitting the spread spectrum GMSK signal.

1 34. A system for receiving a modulated signal comprising:
2 means for receiving a spread spectrum GMSK signal corresponding to a
3 sequence of data symbols and a spread spectrum code;
4 means for processing the received spread spectrum GMSK signal using
5 at least one matched filter to produce at least one filtered signal;
6 means for correlating the at least one filtered signal with the spread
7 spectrum code corresponding to the spread spectrum GMSK signal to produce at least
8 one correlation output; and
9 means for evaluating the correlation output to estimate the sequence of
10 data symbols.

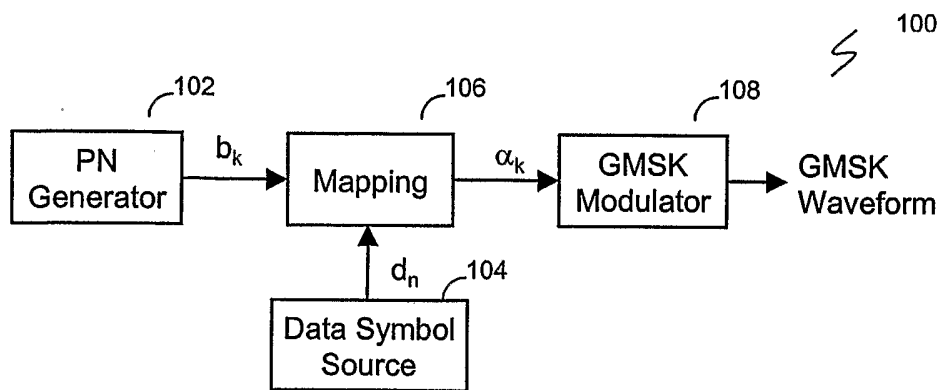


Figure 1

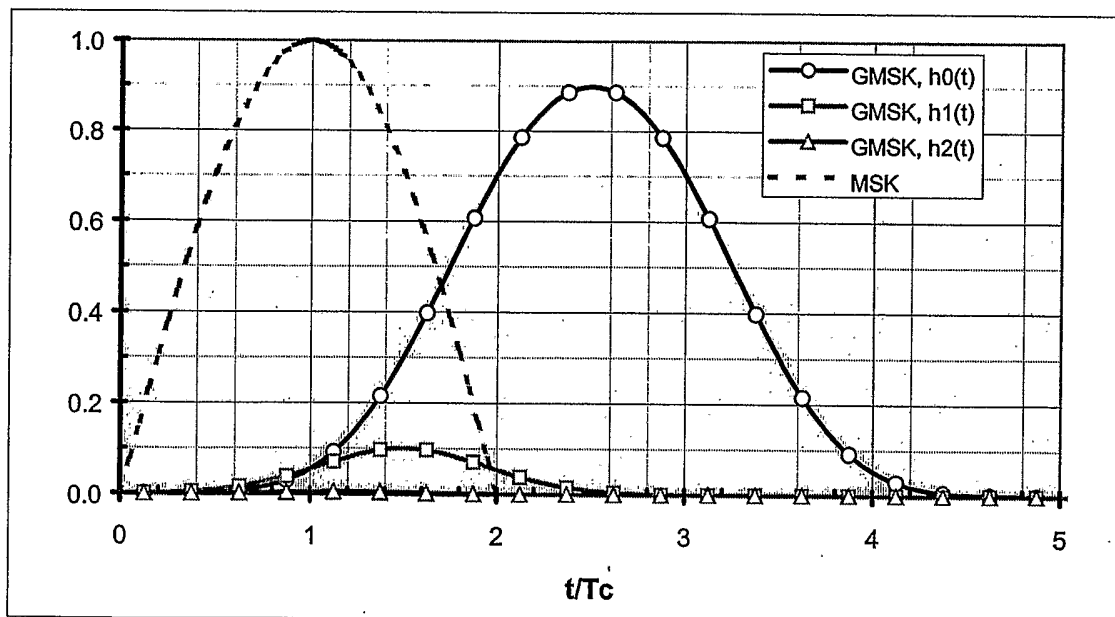


Figure 2

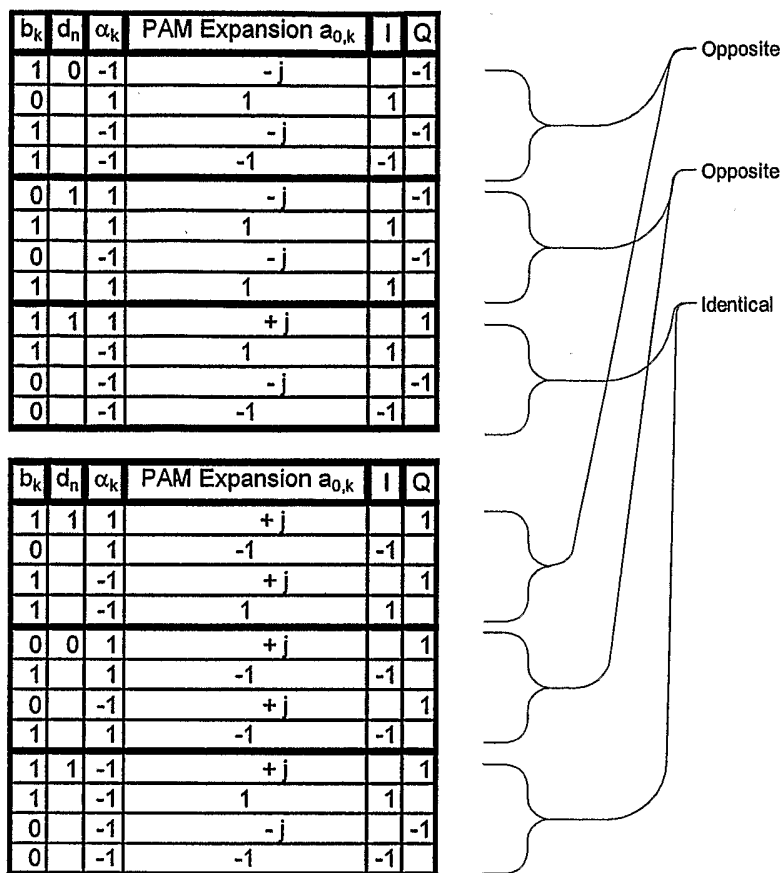


Figure 3

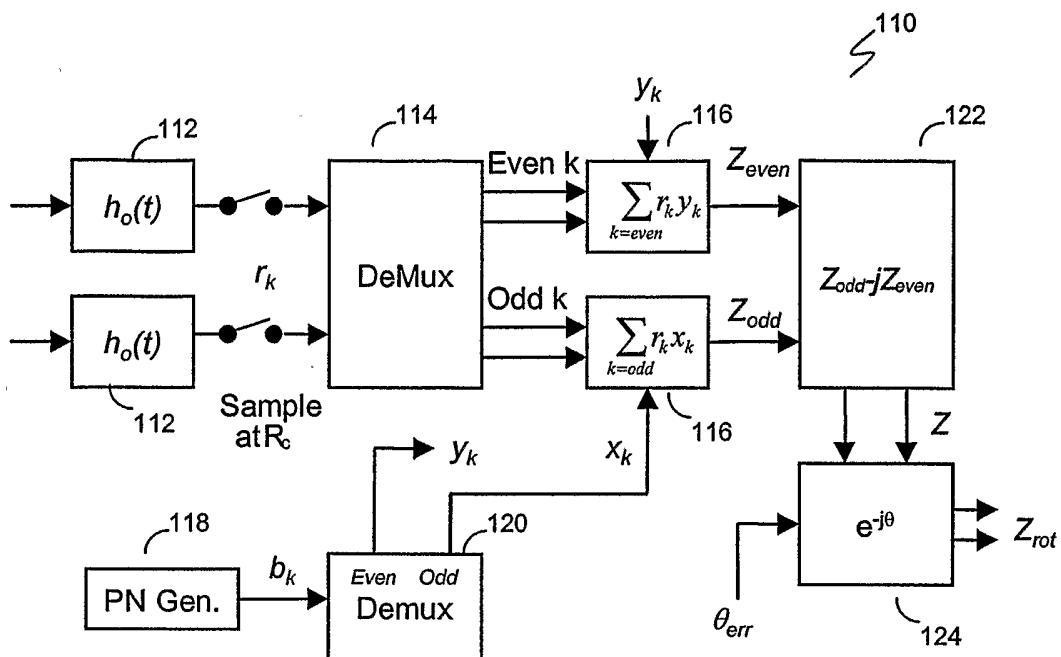


Figure 4

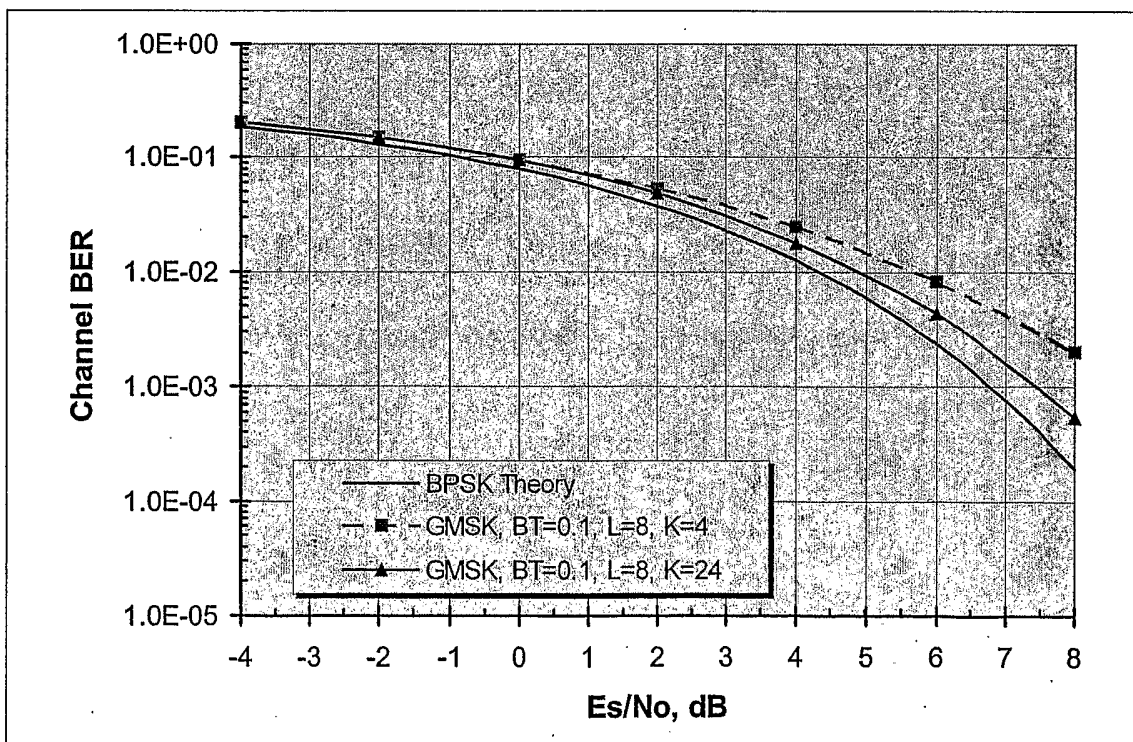


Figure 5

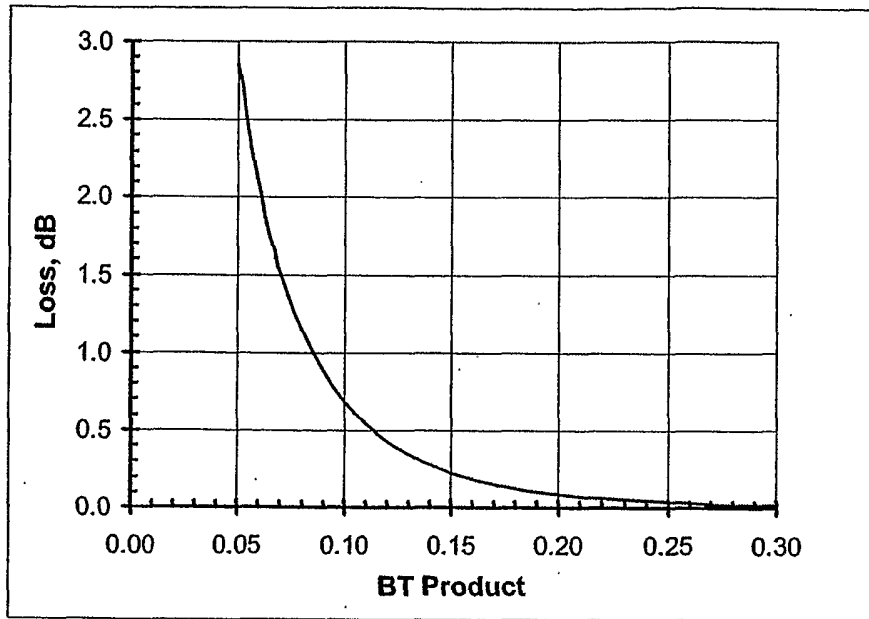


Figure 6

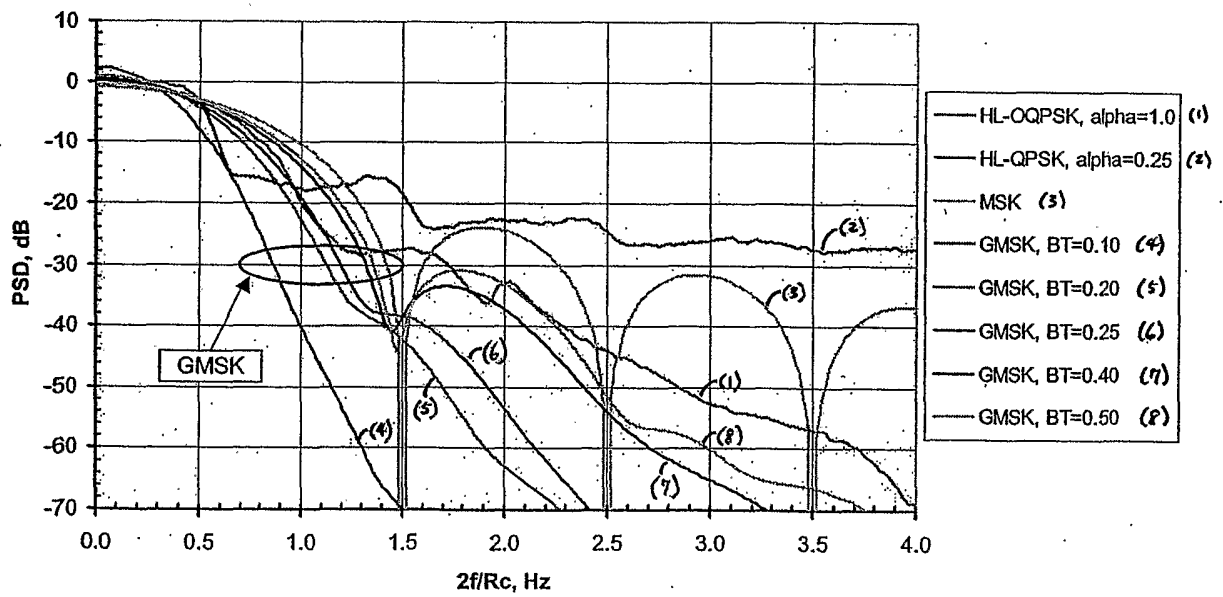


Figure 7

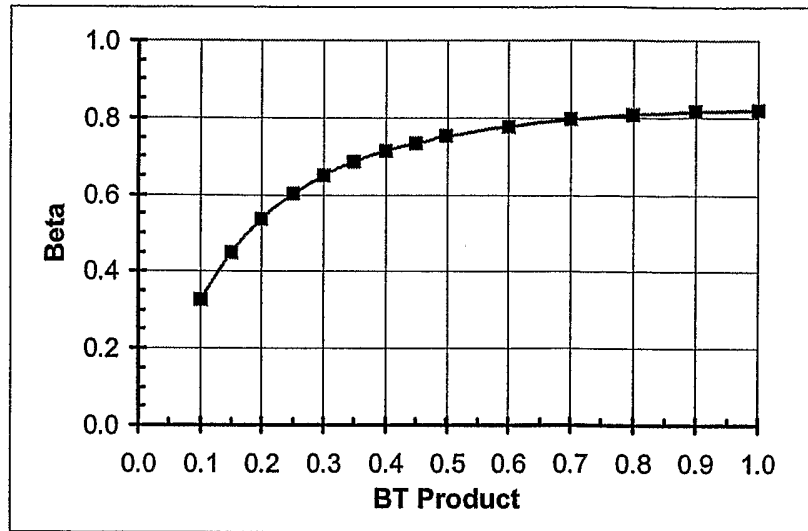


Figure 8

BT Product	Bandwidth Definition					
	-6 dB	-10 dB	-20 dB	-30 dB	-40 dB	-60 dB
0.1	0.7247	0.5929	0.4529	0.4235	0.3261	0.2508
0.2	1.0335	0.8397	0.6322	0.5269	0.4221	0.3317
0.4	1.0627	0.8184	0.6191	0.5394	0.3390	0.2967
0.5	1.0884	0.8344	0.6258	0.5563	0.3338	0.2512
0.6	1.1090	0.8438	0.6363	0.3961	0.3346	0.2389
0.8	1.1188	0.8569	0.6444	0.3836	0.3399	0.1873
1	1.0944	0.8550	0.6463	0.3800	0.2648	0.1865
MSK	1.1211	0.8694	0.6605	0.3770	0.2024	0.0568

Figure 9