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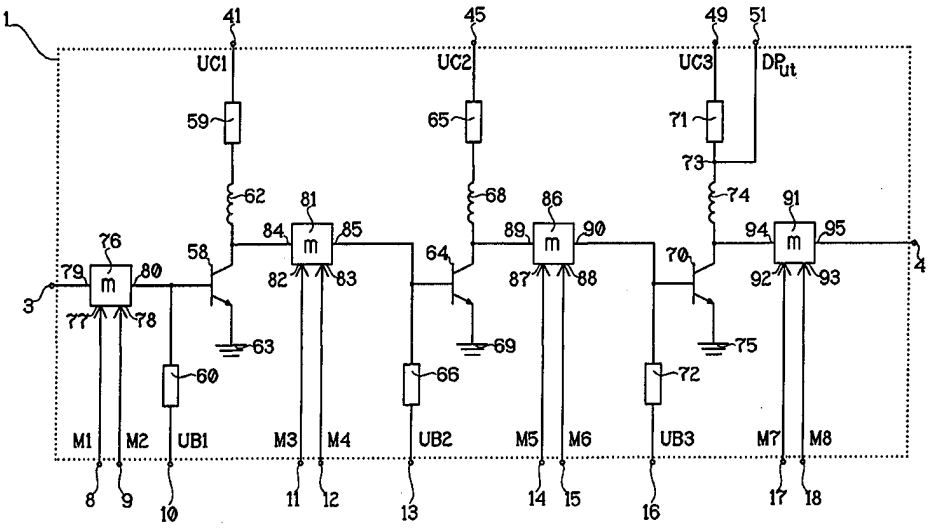

(57) Abstract

The present invention relates to arrangements and procedures in the power amplification of a signal. The power of an input signal is amplified and the power-amplified input signal forms an output signal whose power shall be in agreement with a reference level. The power of the signal is amplified with a power amplification circuit (5) that includes a power amplifier (1) and a control unit (2). The power amplification circuit (5) also includes means for measuring the power of the output signal. The control unit (2) generates supply voltages for the transistors (58, 64, 70) of the power amplifier (1) in relation to said reference level, so as to obtain a power amplifier (1) of good efficiency. The control unit (2) regulates the power of the output signal in relation to the reference level and in relation to the power of the output signal, so that the power of the output signal will be in general agreement with said reference level. The power amplifier (1) may also include controllable matching circuits that are controlled by controlled signals generated in relation to said reference level, so as to further improve the efficiency of the power amplifier (2).

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INTERNATIONAL SEARCH REPORT

International application No.
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A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H03G 3/20, H03F 3/24
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H03G, H04B, H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPIL, EDOC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0481741 A2 (HITACHI, LTD.), 22 April 1992 (22.04.92), column 6, line 55 - column 7, line 51; column 10, line 18 - line 27; column 11, line 48 - line 55, figures 1,4,6,9-11, column 13, line 49 - column 14, line 44; column 14, line 9 - line 44	1,14,15-21
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X	US 5126688 A (EIICHI NAKANISHI ET AL), 30 June 1992 (30.06.92), column 14, line 62 - column 15, line 68	1,14
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Date of the actual completion of the international search	Date of mailing of the international search report
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Name and mailing address of the ISA/ Swedish Patent Office Box 5055, S-102 42 STOCKHOLM Facsimile No. +46 8 666 02 86	Authorized officer Eva Jedermark Telephone No. +46 8 782 25 00

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