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# United States Patent [19]

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Sakaguchi et al.

[45] Date of Patent: **\*\* Nov. 23, 1993**

[54] **MEMORANDUM INPUT AND OUTPUT TERMINAL**

D. 325,571 4/1992 Sakaguchi et al. .... D18/7 X  
D. 328,089 7/1992 Haneda ..... D18/7 X

[75] Inventors: **Hiroshi Sakaguchi**, Kyouto; **Shinsaku Hino**, Nara; **Makiko Yoshimura**, Nara; **Kouji Nishida**, Nara, all of Japan

*Primary Examiner*—Wallace R. Burke  
*Assistant Examiner*—Freda S. Nunn  
*Attorney, Agent, or Firm*—Nixon & Vanderhye

[73] Assignee: **Sharp Kabushiki Kaisha**, Osaka, Japan

[57] **CLAIM**

The ornamental design for a memorandum input and output terminal, as shown and described.

[\*\*] Term: **14 Years**

**DESCRIPTION**

[21] Appl. No.: **821,553**

[22] Filed: **Jan. 17, 1992**

[30] **Foreign Application Priority Data**

Jul. 17, 1991 [JP] Japan ..... 3-21535

[52] U.S. Cl. .... **D14/100; D18/7**

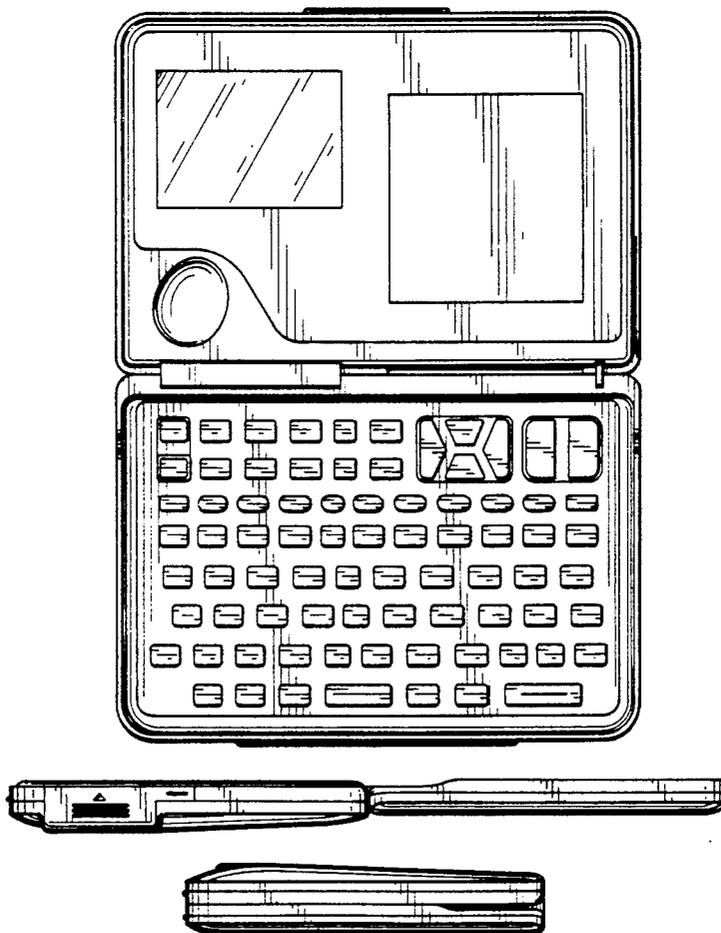
[58] Field of Search ..... D14/100, 106, 115;  
D18/7; 340/700, 706; 341/22, 23; 364/708,  
709.04

FIG. 1 is a top plan view of a memorandum input and output terminal according to the present design in an open condition;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a right side elevational view thereof;  
FIG. 4 is a rear elevational view thereof;  
FIG. 5 is a left side elevational view thereof;  
FIG. 6 is a bottom plan view thereof;  
FIG. 7 is a bottom plan view of the memory input and output terminal hereof in the closed position;  
FIG. 8 is a front elevational view thereof;  
FIG. 9 is a right side elevational view thereof;  
FIG. 10 is a rear elevational view thereof;  
FIG. 11 is a left side elevational view thereof; and,  
FIG. 12 is a bottom plan view thereof.

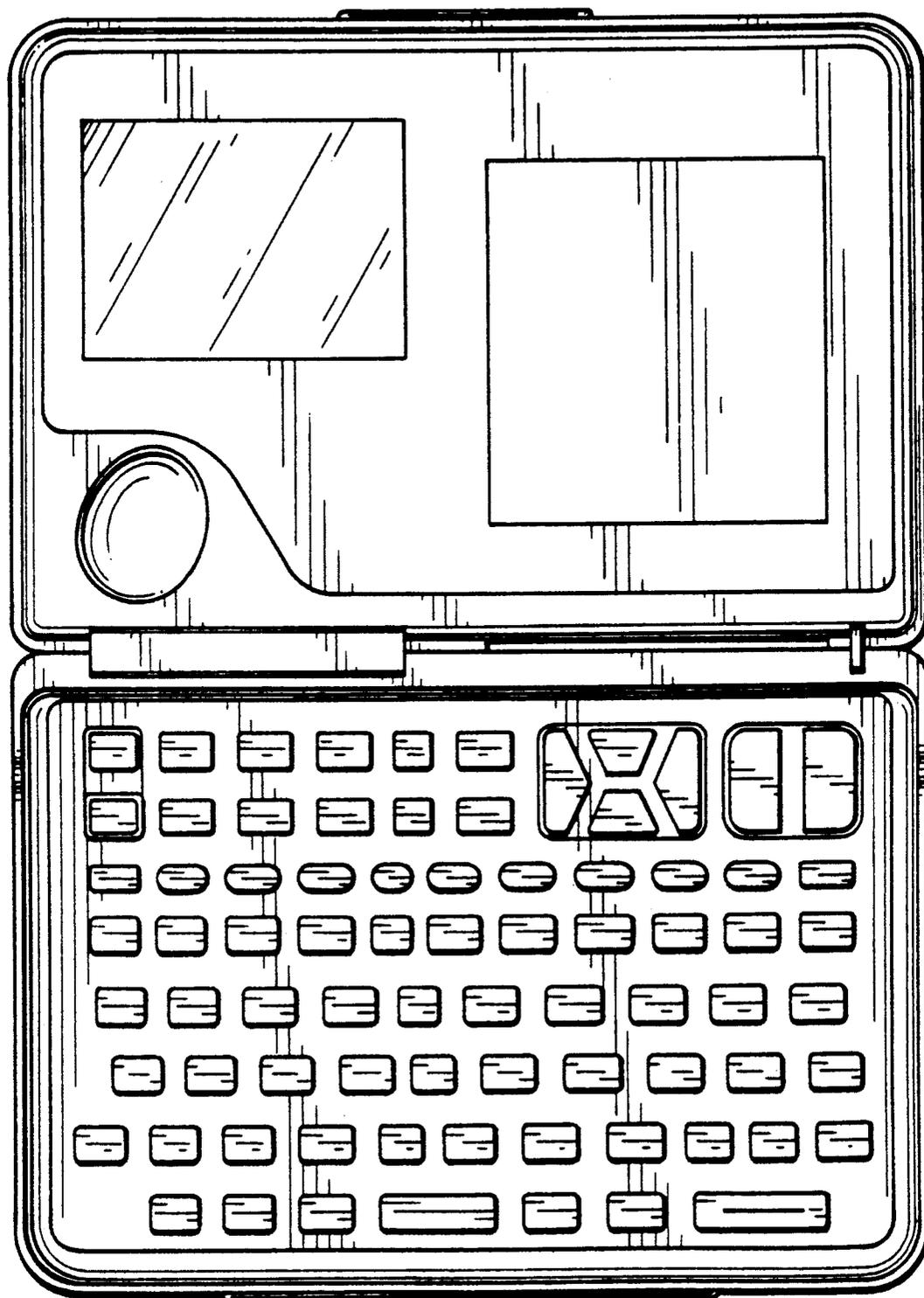
[56] **References Cited**

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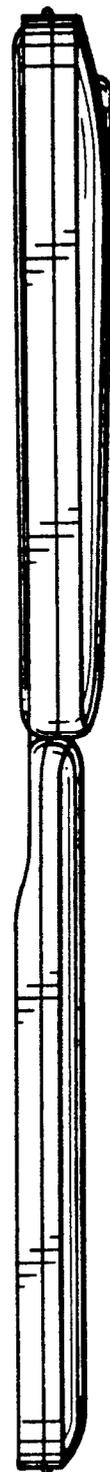


**FIG. 1**





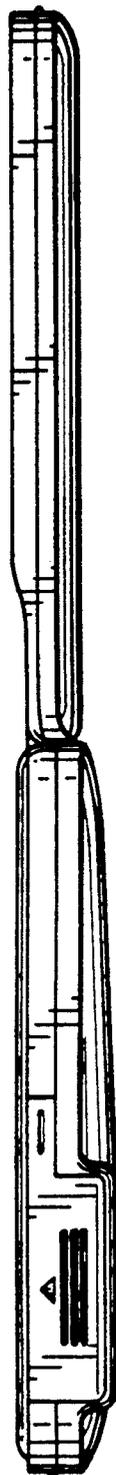
**FIG. 2**



**FIG. 3**

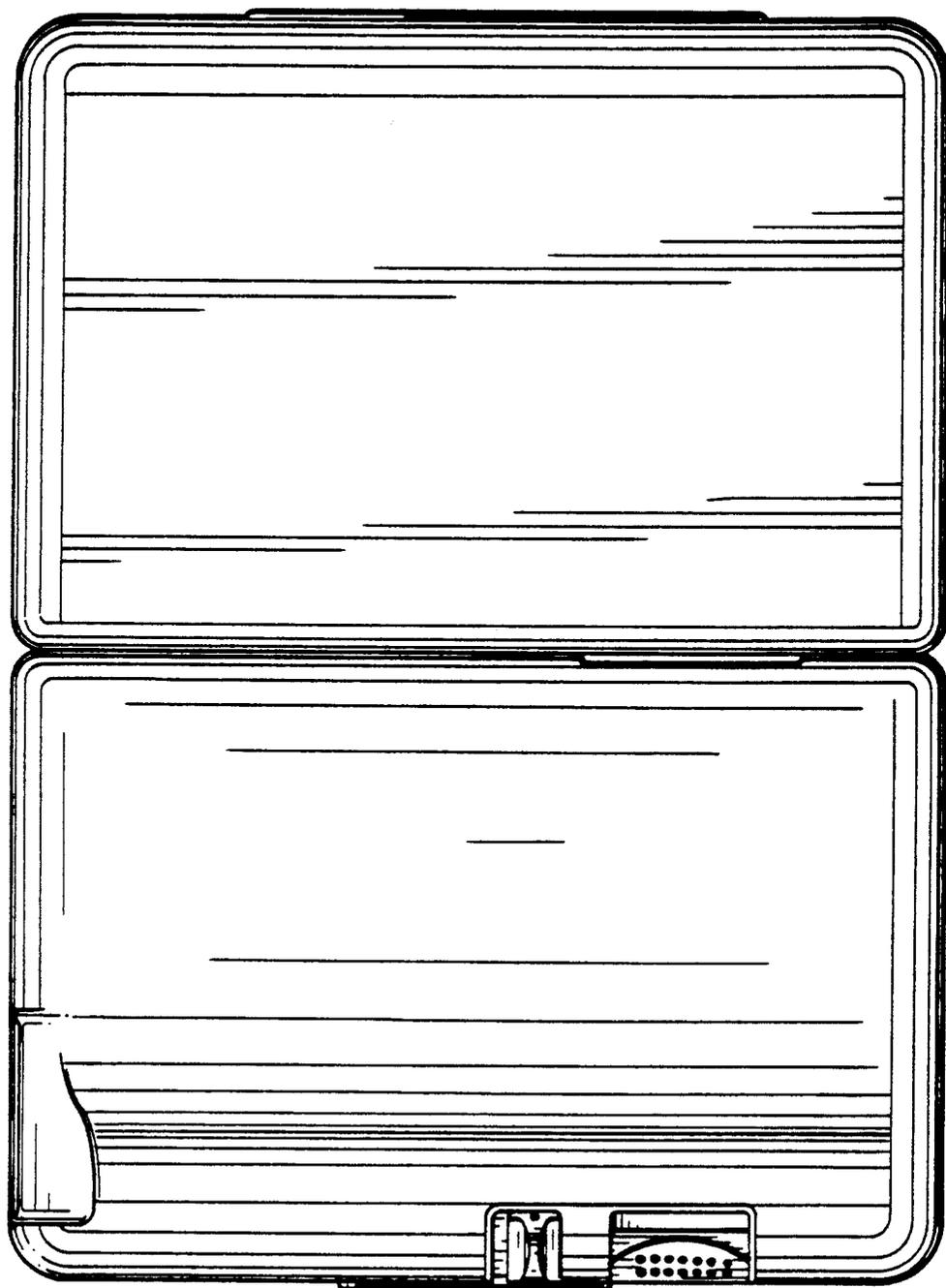


**FIG. 4**

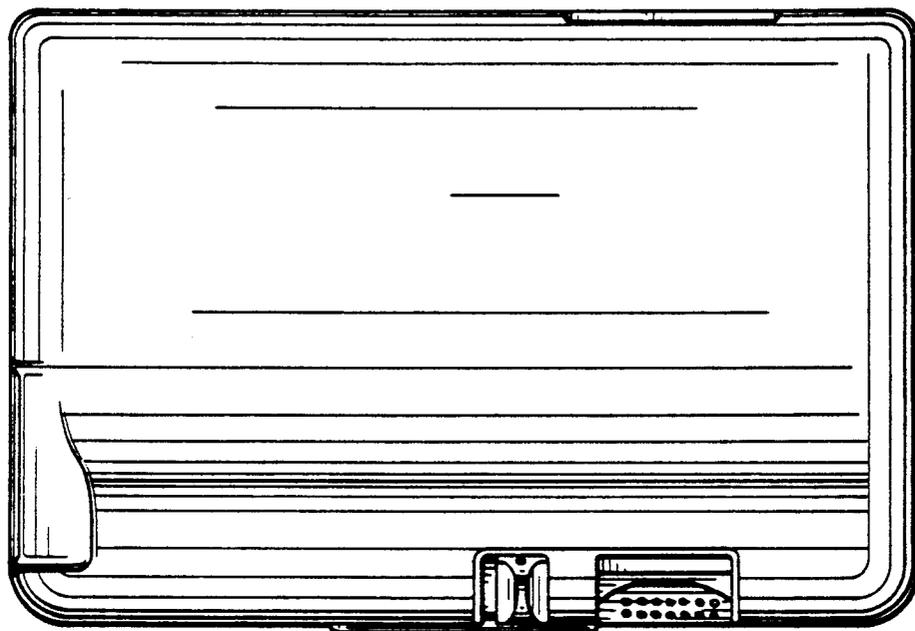


**FIG. 5**

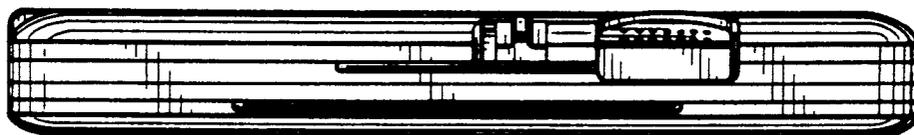
**FIG. 6**



**FIG. 7**



**FIG. 8**



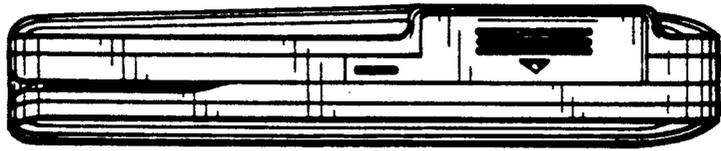
**FIG. 9**



**FIG. 10**



**FIG. 11**



**FIG. 12**

