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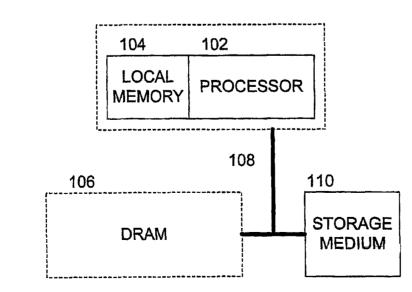
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(54) Title: METHODS AND APPARATUS FOR FACILITATING A SECURE PROCESSOR FUNCTIONAL TRANSITION

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program to the authentication program.

(57) Abstract: A processor and associated local memory are capable of operative connection to a main memory such that data may be read from the main memory for use in the local memory, and the processor is operable to carry out actions, including: entering a secure mode of operation where externally initiated requests to read data from or write data into the processor are not serviced but internally initiated data transfers are serviced; reading a decryption program from a storage medium into the local memory of the processor; reading an encrypted authentication program into the local memory of the processor; decrypting the encrypted authentication program using a decryption program; and transitioning functionality of the processor from the decryption



## INTERNATIONAL SEARCH REPORT

International application No PCT/JP2006/302099

A. CLASSI	FICATION OF SUBJECT MATTER G06F21/00				
According to	o International Patent Classification (IPC) or to both national classifica	tion and IPC			
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	ocumentation searched (classification system followed by classification	n symbols)			
Documental	tion searched other than minimum documentation to the extent that su	uch documents are included in the fields se	arched		
Electronic d	lata base consulted during the international search (name of data bas	e and, where practical, search terms used	)		
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C. DOCUM	ENTS CONSIDERED TO BE RELEVANT				
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Name and mailing address of the ISA/  European Patent Office, P.B. 5818 Patentlaan 2  NL – 2280 HV Rijswijk  Tel. (+31–70) 340–2040, Tx. 31 651 epo nl,  Fax: (+31–70) 340–3016		Authorized officer  Kleiber, M			

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