



- (51) International Patent Classification:
H05B 33/02 (2006.01) H05B 37/02 (2006.01)
- (21) International Application Number:
PCT/US2009/042110
- (22) International Filing Date:
29 April 2009 (29.04.2009)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
61/048,711 29 April 2008 (29.04.2008) US
- (71) Applicant (for all designated States except US): **IVUS INDUSTRIES, INC.** [US/US]; 610 N. Almon Street, Ste. 130, Moscow, ID 83843 (US).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **CEGNAR, Erik, J.** [US/US]; 610 N. Almon Street, Ste. 130, Moscow, ID 83843 (US). **JESSUP, Fred** [US/US]; 610 N. Almon Street, Ste. 130, Moscow, ID 83843 (US). **MAUGHAN, Mike** [US/US]; 610 N. Almon Street, Ste. 130, Moscow, ID 83843 (US). **ALEXANDER, David, G.** [US/US]; 610 N. Almon Street, Ste. 130, Moscow, ID 83843 (US).
- (74) Agent: **NIPPER, Stephen, M.**; P.O.Box 877, Boise, ID 83701 (US).

- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: WIDE VOLTAGE, HIGH EFFICIENCY LED DRIVER CIRCUIT

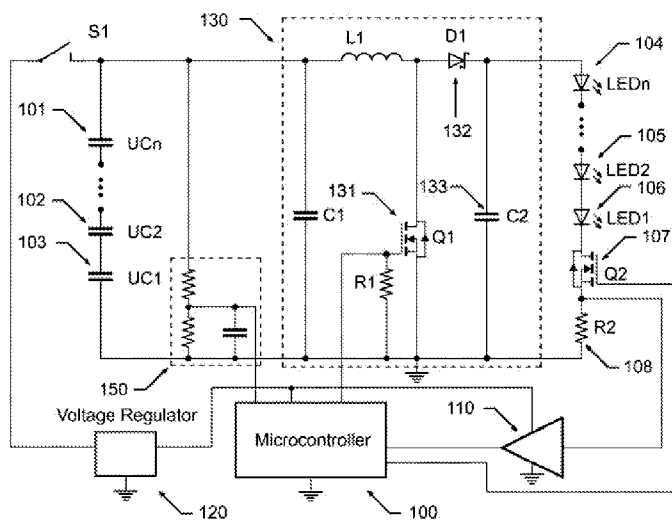


FIG. 1

(57) Abstract: An electrical circuit and method for driving light emitting diodes with a constant current via a high efficiency DC-DC converter controlled by a digital controller through pulse width modulation (PWM). The light emitting diodes may be powered by a variety of power sources including batteries, supercapacitors or ultracapacitors.

WO 2009/134885 A1

TITLE

Wide Voltage, High Efficiency LED Driver Circuit

FIELD OF THE INVENTION

[0001] The invention generally relates to driver circuits for light emitting diodes (LEDs) which can be powered by batteries or ultracapacitors, and in particular relates to a LED driver circuit which is powered by ultracapacitors.

DEFINITIONS

[0002] As used herein, the following terms have the following meanings:

- a. The term "LED" refers to a light emitting diode.
- b. The term "ultracapacitor" refers to a capacitor exhibiting a very high energy density ($> 0.5 \text{ Wh/l}$), including double layer capacitors, supercapacitors, pseudocapacitors, and hybrid capacitors.
- c. The term "microcontroller" refers to a device with electrical inputs and outputs that performs a digital process (e.g., digital signal controllers, microprocessors, digital controllers, digital signal processors).
- d. The term "energy storage system" ("energy source") refers to anything that stores energy and provides power to the system, including but not limited to ultracapacitors and batteries.

BACKGROUND OF THE INVENTION

[0003] Most power output systems are designed to operate at relatively constant voltage because this is typical of the discharge characteristics of most battery chemistries. In comparison to battery chemistries, state of the art ultracapacitor devices store less energy per volume and weight. Also, ultracapacitor discharge curves are significantly different than battery discharge curves. Battery discharge curves are relatively flat as most of the energy is dissipated from the devices. Most systems are designed to operate in this relatively flat portion of the curve. Ultracapacitors, on the other hand, do not have a flat voltage region. Instead, the voltage varies approximately linearly with a constant current discharge.

[0004] Ultracapacitors are commonly viewed or modeled as an ideal capacitor. In fact, the device is considerably more complex. However, for the purposes of this discussion the ideal capacitor model will be used. Equation 1 describes the relationship between voltage, current, and capacitance of an ideal ultracapacitor.

$$i(t) = C \frac{dv}{dt} \quad (\text{Equation 1})$$

[0005] From this equation it is known that for a constant discharge current, the voltage of an ultracapacitor varies linearly with a slope of dv/dt being equal in magnitude to $I(t)/C$. Also, the amount of stored energy that can be used from an ultracapacitor is dependant on the amount of voltage swing a system can allow. For an ultracapacitor with a given capacitance C , and an allowable voltage

swing from V_{high} to V_{low} the amount of usable energy can be calculated from Equation 2.

$$E_{uc} = \frac{1}{2} C (V_{high}^2 - V_{low}^2) \quad (\text{Equation 2})$$

[0006] From Equation 2, it is clear that the larger the allowable voltage swing of an ultracapacitor cell, the larger the amount of stored energy that can be utilized. Therefore, a system that best utilizes the energy storage capabilities of an ultracapacitor is a system that can allow for the largest voltage swing possible.

[0007] Primary and secondary battery powered systems can also benefit from systems that allow for a large voltage swing. However, because a smaller percentage of a battery's usable energy is utilized by a wide voltage swing, the gain is less significant with a battery than it is with an ultracapacitor.

[0008] Recently, white and color LED technology has improved significantly. The color quality, efficacy, and total light output per device continue to improve. Because of these recent advancements LEDs are being used more frequently in consumer and commercial applications.

[0009] LEDs exhibit a nonlinear voltage to current relationship and the voltage for a given current will vary slightly from device to device. The amount of light emitted from an LED at a given temperature is based on current. Therefore, in order to achieve a consistent and predictable light output it is best to drive the LED with a constant current.

Currently there exist many methods of driving LEDs. Many of these circuits drive LEDs with a constant current, but the current regulation is poor and therefore the light output varies as the input voltage to the circuit goes down. The input voltage of ultracapacitors and batteries go down during discharge. Furthermore, existing circuits have a limited input voltage range in comparison to the disclosed technology. And over this limited range the efficiency may be very low. For ultracapacitor systems, the efficiency is critical because the energy density is typically lower for state of the art ultracapacitors vs. state of the art batteries. However, efficiency is still important for battery-powered systems as well as other sources of electrical power.

[0010] Digital controllers can provide unique functionality to consumer products. In the case of hand-held lighting the use of a digital controller can provide, for example, unique light output profiles based on input voltage, unique types of user interface and unique flash patterns. State of charge and other calculations can easily be performed. Digital controllers can also operate down to very low voltages, which make them advantageous in control systems over alternative methods.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011] FIG. 1 is a high level schematic of a circuit for driving high power LEDs.

[0012] FIG. 2 is a block diagram of a control system representing the microcontroller, DC/DC Converter, and current feedback circuit.

[0013] FIG. 3 is a graph of efficiency of one embodiment of the system/DC-DC boost converter.

[0014] FIG. 4 is a graph of lux vs. time as produced by one embodiment of the disclosed invention as measured with a lux meter.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0015] While the invention is susceptible of various modifications and alternative constructions, certain illustrated embodiments thereof have been shown in the drawings and will be described below in detail. It should be understood, however, that there is no intention to limit the invention to the specific form disclosed, but, on the contrary, the invention is to cover all modifications, alternative constructions, and equivalents falling within the spirit and scope of the invention as defined in the claims.

[0016] In the following description and in the figures, like elements are identified with like reference numerals. The use of "e.g.," "etc.," and "or" indicates non-exclusive alternatives without limitation unless otherwise noted.

The use of “including” means “including, but not limited to,” unless otherwise noted.

[0017] Referring initially to FIG. 1, shown is a high level schematic of the circuit for driving high power LEDs. The circuit includes ultracapacitors (101-103) for energy storage from and series connected LEDs (104-106). The ultracapacitors could be connected in series, parallel or combinations of series and parallel.

[0018] FIG. 2 shows a block diagram of the control system representing the microcontroller (100), DC/DC Converter (130), and current feedback circuit. The feedback circuit represents a measurement resistor (108), a filter (109), and an operational amplifier circuit (110) to provide gain to the current feedback signal.

[0019] The LED driver circuit powers high-powered LEDs by controlling the current through them. The preferred system uses closed-loop proportional-integral-derivative (PID) control to ensure a well regulated constant current over a very wide range of input voltages. Alternatively, integral control, proportional control, or proportional-integral control could be used. In this embodiment the derivative gain is set to zero. The current from the output of the DC-DC boost converter (130) is controlled by a pulse width modulation (PWM) signal from the microcontroller (100).

[0020] The main microcontroller program (90) generates an internal reference current (I_{ref}) to the PID control loop. The reference current (I_{ref}) may be a constant or a function based on a discharge profile or various other

inputs and parameters. The current from the DC/DC boost converter (130) is measured by a resistor (108) connected in series with the LEDs. The small value of the 0.2Ω measurement resistor (108) results in a dissipation that is a very small percentage of less than 1% of the total output of power. The voltage over the measurement resistor (108) is filtered by the filter (109) and amplified by an operational amplifier circuit (110). The microcontroller (100) then converts the amplified signal to a digital number by use of an analog to digital converter (ADC) (88).

[0021] Closed loop control is performed within the microcontroller (100) and is based on the measured current and the program generated reference current. Within the PID loop, the digital value representing the measured current is subtracted from the program-generated reference current. The difference between the two is the error. Three terms are generated based on the error. A proportional term is generated by multiplying the error by the proportional gain (K_p). An integral term is generated by integrating with the error with respect to time and multiplying it by the integral gain (K_i). A derivative term is generated by taking the derivative of the error with respect to time and multiplying it by the derivative term (K_d). In this embodiment the derivative gain is set to zero.

[0022] The proportional gain, the integral gain and the derivative gain are summed to generate a digital value for the PWM signal. The microcontroller's built-in PWM generator uses the PWM value to generate a PWM signal for the DC-DC boost converter. The use of a PID control loop ensures that the generated

PWM signal is such that the DC-DC boost converter outputs the commanded current to a very high degree of accuracy.

[0023] FIG. 3 is a graph of efficiency of the system/DC-DC boost converter powering three white LEDs over the range of input voltages from roughly 4.0 to 8.15 volts. The efficiency is over 90% for this range.

[0024] FIG. 4 is a graph of lux vs. time as produced by the disclosed invention and measured with a lux meter. The circuit is powered with ultracapacitors during data collection. The voltage of the ultracapacitors decreases from 8.1 to 1.8 volts during this operation. The graph has two distinct operating modes where a first mode has a high light output and a second mode has a low light output. FIG. 4 illustrates clearly a very well regulated flat light output curve with two distinct operating modes during the ultracapacitor discharge.

[0025] The DC-DC converter transfers energy to the output based on the PWM signal. The PWM signal is modulated by changing the period of time when the signal is high versus when the signal is low. When the signal is high the mosfet (131) turns on and conducts current. When it is low the mosfet is off and not conducting current. When the mosfet is on, current is increasing in the inductor and the diode (132) is reverse biased and not conducting. When the mosfet turns off the diode becomes forward biased and current flows from the source through the inductor and the diode and into the bulk capacitor (133) and the LEDs (104-106). During this time, the current through the inductor is decreasing. This configuration contributes to a high efficiency because the

voltage drop over the diode (132) is proportionally less than the total output voltage when the diode is forward biased. In this embodiment, the output voltage is approximately 10V and the voltage drop over the diode while it is conducting is approximately 0.3V.

[0026] A turn-off transistor (107) prevents current from flowing from the energy system to the LEDs when the system is not operating. Said turn-off transistor is controlled by the microcontroller (100) by means of a digital signal. Said turn-off transistor also provides the circuit with the capability of turning the LEDs on and off rapidly. This function is important for strobe type flashing modes of operation.

[0027] Beyond the closed-loop control the microcontroller performs other various functions. As discussed above, the microcontroller generates an internal reference current. The dc-dc converter follows this current. The internal reference current is a function of the mode of operation and the voltage of the energy storage system. The mode of operation may or may not be user selectable. The reference current may also be based on other inputs such as user input buttons, temperature and time.

[0028] Ultracapacitors provide unique advantages to systems such as long life and quick recharge. In order to take advantage of these characteristics a unique system is needed. The system must have a wide input voltage range, a very high efficiency and a very well regulated output.

[0029] The disclosed invention provides these necessary characteristics to make ultracapacitors a viable source to power LEDs in hand-held products and other applications.

[0030] In the disclosed invention, a high efficiency dc-dc converter (130) is controlled by a digital controller (100) through pulse width modulation (PWM). A low-dropout linear regulator (120) prevents the input voltage to the digital controller from exceeding its maximum voltage. A very low power consumption measurement circuit provides current feedback to said digital controller. Said digital controller performs closed-loop current control.

[0031] One example embodiment: An electrical circuit for driving high output LEDs with a constant current is disclosed. The circuit is configured in a manner that lends itself to a very wide input voltage range with high efficiency over that wide operating range. The circuit can achieve a peak efficiency of greater than 96% with an operating range from 10 volts down to 1.5 volts. This embodiment provides an operating range of up to 10 volts; however it is not limited to 10 volts. Because of this wide voltage range and high efficiency the circuit is particularly beneficial to ultracapacitor-powered systems. However, it also provides benefit to battery powered systems because it operates at a very high efficiency and allows the battery voltage to decrease significantly below its nominal voltage while still providing a regulated output. Closed loop current control is provided by a microcontroller. The current through the LEDs is measured by amplifying the voltage over a measurement resistor. The use of a

microcontroller to provide closed loop control provides the system with the ability to operate to a very low voltage (1.5 volts) and provides unique custom control and functionality. The system provides a very constant light output as the batteries or ultracapacitors discharge. Figure 4 shows two distinct operating modes where a first mode has a high light output and a second mode has a low light output as measured with a lux meter. At approximately one hour, the driver distinctly switches to a lower output mode. These two “flat” output modes are uncommon in most existing LED drivers and light output systems.

[0032] The purpose of the Abstract is to enable the public, and especially the scientists, engineers, and practitioners in the art who are not familiar with patent or legal terms or phraseology, to determine quickly from a cursory inspection, the nature and essence of the technical disclosure of the application. The Abstract is neither intended to define the invention of the application, which is measured by the claims, nor is it intended to be limiting as to the scope of the invention in any way.

[0033] Still other features and advantages of the claimed invention will become readily apparent to those skilled in this art from the following detailed description describing preferred embodiments of the invention, simply by way of illustration of the best mode contemplated by carrying out my invention. As will be realized, the invention is capable of modification in various obvious respects all without departing from the invention. Accordingly, the drawings and

description of the preferred embodiments are to be regarded as illustrative in nature, and not as restrictive in nature.

[0034] While there is shown and described the present preferred embodiment of the invention, it is to be distinctly understood that this invention is not limited thereto but may be variously embodied to practice within the scope of the following claims. From the foregoing description, it will be apparent that various changes may be made without departing from the spirit and scope of the invention as defined by the following claims.

CLAIMS

What is claimed is:

1. A method of driving at least one light emitting diode, said method comprising the steps of:

generating an internal reference current;

measuring the current from a DC-DC boost converter powered by a power source, said DC-DC boost converter driving said at least one light emitting diode;

supplying said measured current and said internal reference current to a closed loop control;

using said closed loop control to generate a PWM signal;

controlling the output current of said DC-DC boost converter using said PWM signal; and

driving said at least one light emitting diode with said output current.

2. The method of claim 1, wherein the said measured current is determined by measuring the voltage across a measurement resistor, filtering the measured voltage to create a filtered voltage, amplifying the filtered voltage to create an amplified signal, and converting the amplified signal to create said measured current.

3. The method of claim 2, wherein said amplification step is accomplished using an operational amplifier.
4. The method of claim 1, wherein the power source is at least one ultracapacitor.
5. The method of claim 1, wherein said power source is a plurality ultracapacitors connected in series, parallel or combinations of series and parallel.
6. The method of claim 1, wherein said internal reference current can be changed based upon user input.
7. The method of claim 1, wherein the power source is at least one battery.
8. The method of claim 1, wherein said power source is a plurality batteries connected in series, parallel or combinations of series and parallel.
9. The method of claim 1, wherein said internal reference current can be changed based upon temperature.
10. The method of claim 1, wherein said internal reference current can be changed based upon time.

11. The method of claim 1, wherein said closed loop control is a proportional-integral-derivative control.

12. The method of claim 1, wherein said closed loop control is a proportional-integral control.

13. The method of claim 1, wherein said closed loop control is a proportional derivative control.

14. The method of claim 1, wherein said closed loop control is an integral control.

15. A LED driver circuit for powering a plurality of light emitting diodes with a power source, said circuit comprising:

a DC-DC boost converter powered by said power source, said DC-DC boost converter for providing current to said light emitting diodes, said DC-DC boost converter controlled by a microcontroller through pulse width modulation (PWM);

a current feedback circuit for measuring the output of said DC-DC boost converter, said current feedback circuit comprising a measurement resistor connected in series with said light emitting

diodes, wherein voltage measured across said measurement resistor can be filtered by a filter and amplified by an operational amplifier to create an amplified signal; and
said microcontroller, said microcontroller for generating a reference current, said microcontroller comprising a PWM generator for generating a PWM signal based on the difference between said measured current and said reference current, said PWM signal for controlling current output of said DC-DC boost converter.

16. The LED driver circuit of claim 15, wherein said amplified signal is analog, and wherein said microcontroller further comprises an analog to digital converter for converting said analog amplified signal to said measured current which is digital .

17. The LED driver circuit of claim 15, wherein said at least one light emitting diode comprises a plurality of series connected light emitting diodes.

18. The LED driver circuit of claim 15, wherein said power source is at least one ultracapacitor or at least one battery.

19. The LED driver circuit of claim 18, wherein said power source is a plurality of ultracapacitors connected in series, parallel or a combination of series and parallel.

20. A LED driver circuit for powering a plurality of series connected light emitting diodes with a plurality of series connected ultracapacitors, said circuit comprising:

a DC-DC boost converter powered by said power source, said DC-DC boost converter for providing current to said light emitting diodes, said DC-DC boost converter controlled by a microcontroller through pulse width modulation (PWM);

a current feedback circuit for measuring the output of said DC-DC boost converter, said current feedback circuit comprising a measurement resistor connected in series with said light emitting diodes, wherein voltage measured across said measurement resistor can be filtered by a filter and amplified by an operational amplifier to create an analog amplified signal; and

said microcontroller, said microcontroller for generating a digital reference current, said microcontroller comprising an analog to digital converter for converting said analog amplified signal to a digital measured current, said microcontroller comprising a PWM generator for generating a PWM signal based on the difference between said measured current and said reference current, said PWM signal for controlling current output of said DC-DC boost converter.

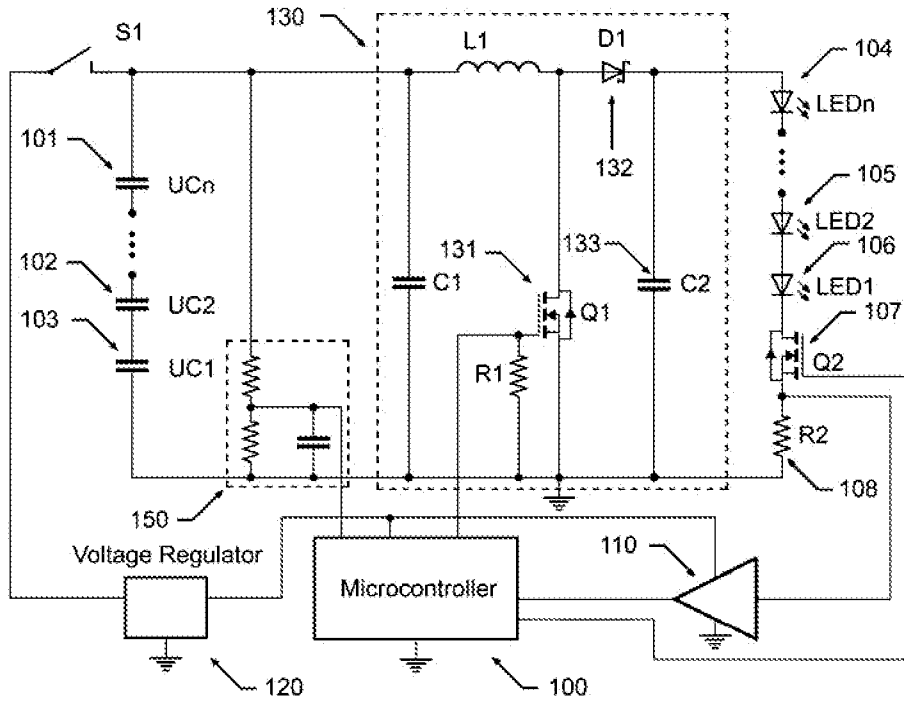


FIG. 1

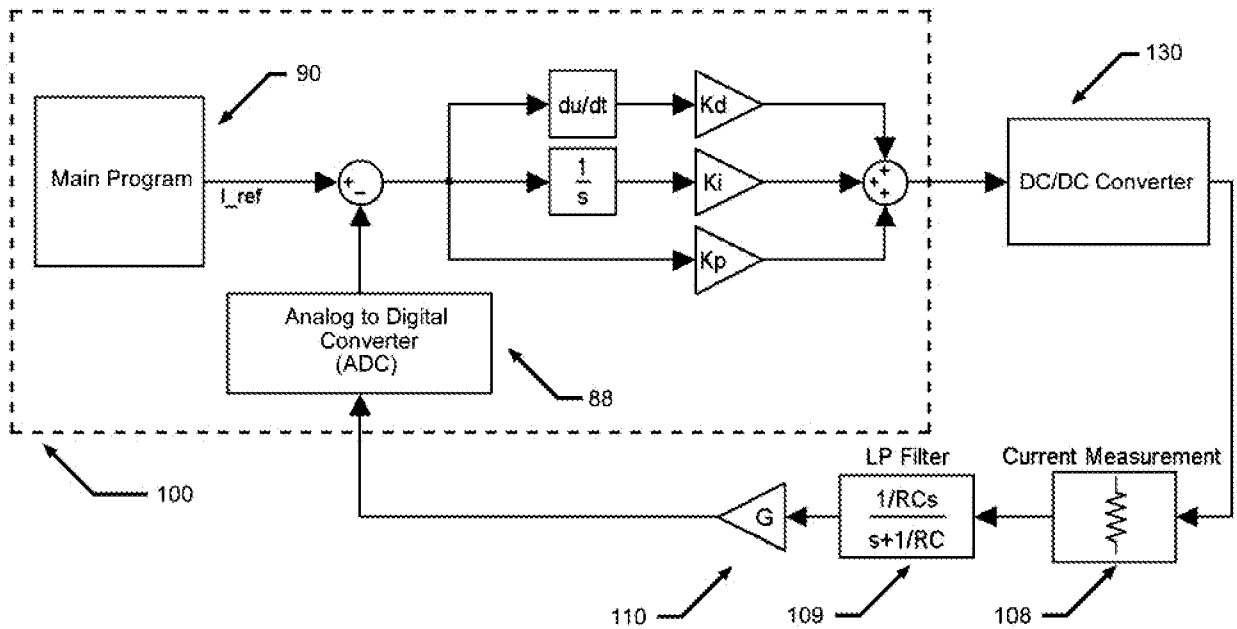


FIG. 2

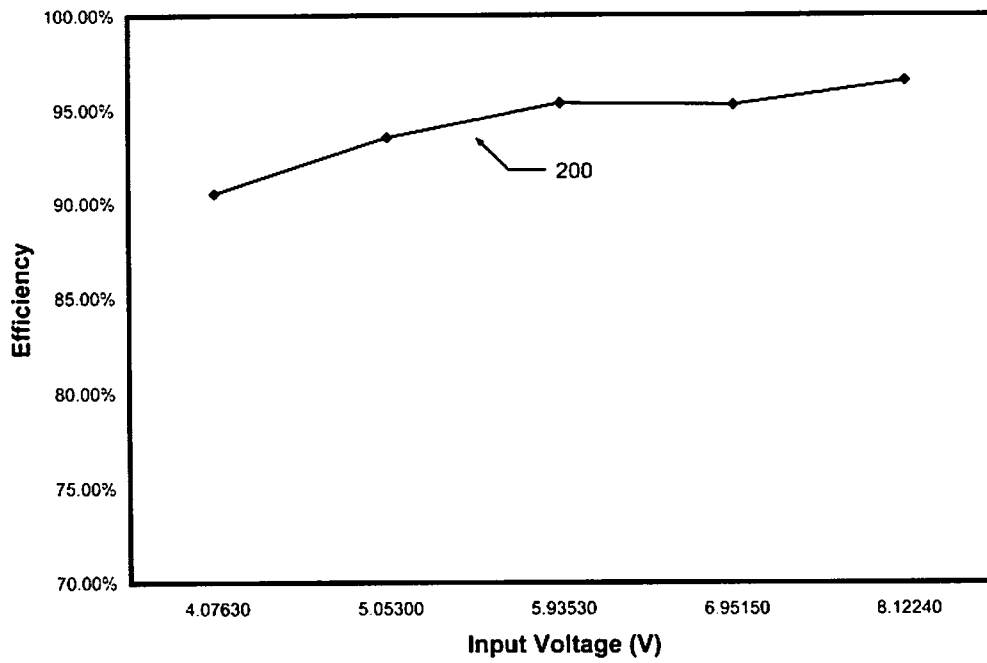


FIG. 3

Standard Mode Lux Output

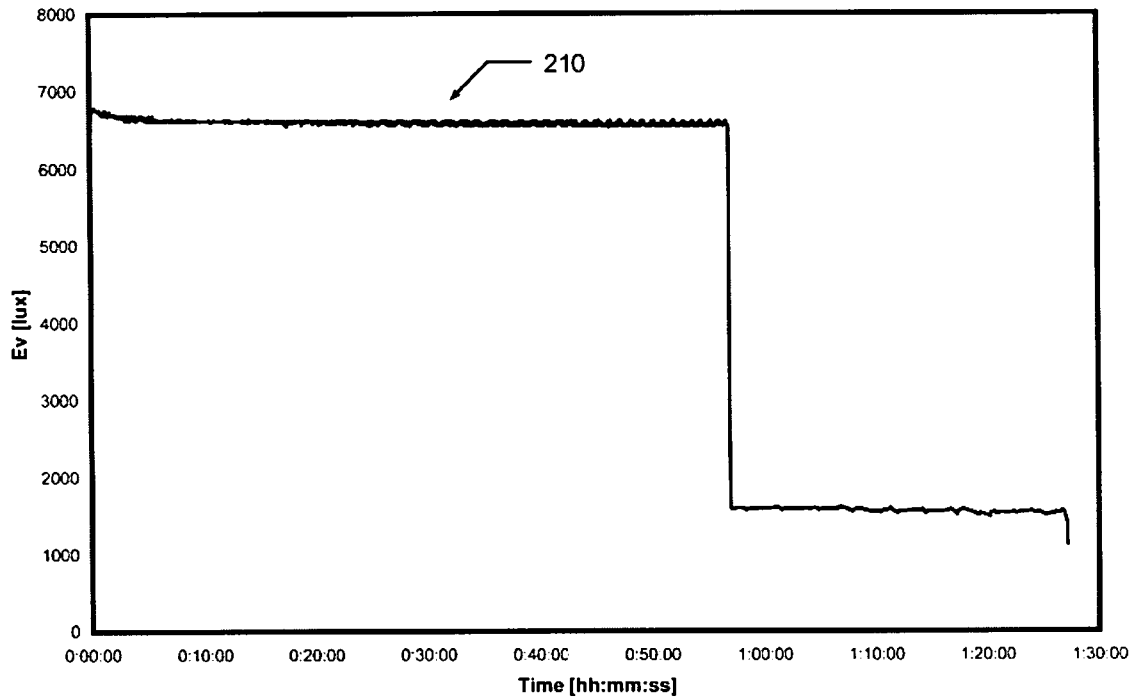


FIG. 4

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US2009/042110

A. CLASSIFICATION OF SUBJECT MATTER

Int. Cl.

H05B 33/08 (2006.01) **H05B 33/02** (2006.01) **H05B 37/02** (2006.01)

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
 TXTUS1, TXTUS2, TXTUS3, TXTEP1, TXTGB1, TXTWO1, TXTAU1, TXTCA1, Espacenet, Patentscope, Google Patents, INSPEC, Google Scholar and keywords (LED, light emitting diode, laser diode, driver, driven, driving, reference current, boost converter, DC_DC, current regulat+, PWM, closed_loop, microcontroller, DSP, ultracapacitor, supercapacitor and like words)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 7102340 B1 (FERGUSON) 5 September 2006 Abstract; figure 1; column 1, lines 19-60; column 2, line 35, to column 3, line 54; column 5, line 12-48; column 6, line 26	1
Y		4-5
X	US 2005/0218838 A1 (LYS) 6 October 2005 Abstract; figure 7; paragraphs 0003-0006, 0009, 0015-0025, 0031, 0051, 0088-0093, 0213, 0230-0233, 0245, 0254 and 0267	1-3, 6-18
Y		4-5; 19-20
X	Bhattacharya et al., 'Digital sliding mode pulsed current averaging IC drivers for high brightness light emitting diodes', 10th IEEE Workshop on computers in Power Electronics, 16-19 July 2006, IEEE 2006, ISBN 0-7803-9724-X, pages 136-141	1-3, 15, 17
Y	Abstract; figures 1, 7(a), 7(b) and 8; columns 1-4, 6-8	4-5, 19-20



Further documents are listed in the continuation of Box C



See patent family annex

* Special categories of cited documents:		
"A" document defining the general state of the art which is not considered to be of particular relevance	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"E" earlier application or patent but published on or after the international filing date	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	"&"	document member of the same patent family
"P" document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search
25 June 2009

Date of mailing of the international search report
- 1 JUL 2009

Name and mailing address of the ISA/AU
 AUSTRALIAN PATENT OFFICE
 PO BOX 200, WODEN ACT 2606, AUSTRALIA
 E-mail address: pct@ipaaustralia.gov.au
 Facsimile No. +61 2 6283 7999

Authorized officer
STEPHEN HARKER
 AUSTRALIAN PATENT OFFICE
 (ISO 9001 Quality Certified Service)
 Telephone No : +61 2 6283 7962

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US2009/042110

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2007/0120506 A1 (GRANT) 31 May 2007 Abstract; figure 1; paragraphs 0003-0013, 0021-0031 and 0037	1
X	US 2006/0261752 A1 (LEE) 23 November 2006 Abstract; figures 2-3; paragraphs 0003, 0007-0026, 0036-0046 and 0053	1
X	Lee et al., 'New design and application of high efficiency LED driving system for RGB-LED backlight in LCD display', 2006 IEEE Power Electronics Specialists Conference, 18-22 June 2006, IEEE, 2006, ISBN 1-4244-9717-7 Abstract; figures 1-4; columns 1-5	1
Y	Calleja et al., 'Evaluation of power LEDs drivers with supercapacitors and digital control', Proceedings of the 42nd IAS Annual Meeting, IEEE, September, 2007, pages 1129-1134, DOI: 10.1109/07IAS.2007.17 Abstract; figures 2-3; columns 1-4; sections VI and VII	4-5, 19-20
	Note on Y indications: Calleja et al. can be combined with any one of US 7102340, US 2005/0218838 or Bhattacharya et al. with reference to the claims as shown.	

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2009/042110

This Annex lists the known "A" publication level patent family members relating to the patent documents cited in the above-mentioned international search report. The Australian Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

Patent Document Cited in Search Report		Patent Family Member			
US	7102340	US	7102339		
US	2005218838	AU	17825/01	AU	19241/99
		AU	41484/02	AU	53129/99
		AU	63473/00	AU	70018/01
		AU	77185/01	AU	77308/00
		AU	85408/01	AU	88659/01
		AU	92060/98	AU	2002360721
		AU	2003237827	AU	2003265764
		AU	2003279157	AU	2004200183
		AU	2005222987	AU	2006202217
		CA	2302227	CA	2314163
		CA	2466717	CA	2552683
		CA	2579196	CA	2591133
		CN	1809867	EP	1016062
		EP	1090459	EP	1172748
		EP	1224843	EP	1224845
		EP	1287724	EP	1295515
		EP	1340412	EP	1356610
		EP	1393599	EP	1395975
		EP	1428415	EP	1459600
		EP	1501763	EP	1502483
		EP	1610593	EP	1620843
		EP	1687692	EP	1704752
		EP	1731004	EP	1754121
		EP	1831866	EP	1849152
		HK	1025416	JP	2004006253
		JP	2008078162	JP	2009070832
		KR	20060131985	KR	20080099352
		US	6150774	US	6166496
		US	6292901	US	6340868
		US	6528954	US	6548967
		US	6608453	US	6624597
				AU	39470/02
				AU	59134/01
				AU	73694/01
				AU	85398/01
				AU	91111/01
				AU	2003210890
				AU	2003268540
				AU	2004300444
				AU	2007216901
				CA	2336184
				CA	2559718
				CA	2591205
				EP	1040398
				EP	1195740
				EP	1234140
				EP	1337784
				EP	1388276
				EP	1422975
				EP	1474633
				EP	1535495
				EP	1624728
				EP	1729615
				EP	1800054
				EP	1887836
				JP	2008034385
				KR	20060108757
				US	6016038
				US	6211626
				US	6459919
				US	6577080
				US	6717376

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2009/042110

US	6720745	US	6774584	US	6777891
US	6781329	US	6788011	US	6801003
US	6806659	US	6869204	US	6888322
US	6897624	US	6936978	US	6965205
US	6967448	US	6969954	US	6975079
US	7014336	US	7031920	US	7038398
US	7038399	US	7042172	US	7064498
US	7113541	US	7132785	US	7132804
US	7135824	US	7139617	US	7161311
US	7161313	US	7161556	US	7180252
US	7186003	US	7187141	US	7202613
US	7204622	US	7221104	US	7228190
US	7231060	US	7233115	US	7233831
US	7242152	US	7248239	US	7253566
US	7255457	US	7256554	US	7274160
US	7300192	US	7303300	US	7308296
US	7309965	US	7344279	US	7350936
US	7352138	US	7352339	US	7353071
US	7354172	US	7358679	US	7358706
US	7358929	US	7385359	US	7387405
US	7427840	US	7449847	US	7453217
US	7459864	US	7462997	US	7482565
US	7482764	US	7495671	US	7502034
US	7515128	US	7520634	US	7525254
US	2001028227	US	2002038157	US	2002043938
US	2002044066	US	2002047569	US	2002047628
US	2002047646	US	2002048169	US	2002057061
US	2002070688	US	2002074559	US	2002078221
US	2002101197	US	2002101200	US	2002113555
US	2002130627	US	2002145394	US	2002152045
US	2002153851	US	2002158583	US	2002163316
US	2002171365	US	2002171377	US	2002171378
US	2002176259	US	2002195975	US	2003011538
US	2003028260	US	2003057884	US	2003057886
US	2003057887	US	2003057890	US	2003076281
US	2003100837	US	2003133292	US	2003137258
US	2003206411	US	2003214259	US	2003222587

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2009/042110

US	2004032226	US	2004052076	US	2004090191
US	2004090787	US	2004105261	US	2004113568
US	2004130909	US	2004155609	US	2004160199
US	2004178751	US	2004212320	US	2004212321
US	2004212993	US	2004240890	US	2004257007
US	2005030744	US	2005035728	US	2005036300
US	2005040774	US	2005041161	US	2005041424
US	2005044617	US	2005047132	US	2005047134
US	2005062440	US	2005063194	US	2005099824
US	2005116667	US	2005151489	US	2005174473
US	2005213352	US	2005213353	US	2005218870
US	2005219872	US	2005231133	US	2005236998
US	2005248299	US	2005253533	US	2005275626
US	2005276053	US	2005285547	US	2006002110
US	2006012987	US	2006016960	US	2006022214
US	2006050509	US	2006076908	US	2006098077
US	2006104058	US	2006109649	US	2006152172
US	2006158881	US	2006208667	US	2006221606
US	2006262516	US	2006285325	US	2007047227
US	2007086754	US	2007086912	US	2007115658
US	2007115665	US	2007153514	US	2007188427
US	2007189026	US	2007195526	US	2007206375
US	2007236156	US	2007237284	US	2007258240
US	2007291483	US	2008012502	US	2008012506
US	2008130267	US	2008140231	US	2008183081
US	2008204268	US	2008215391	WO	0001067
WO	0105195	WO	0124584	WO	0136864
WO	0182657	WO	0199475	WO	0210847
WO	0211497	WO	0212127	WO	0213490
WO	0218913	WO	0225842	WO	0240921
WO	0245467	WO	9910867	WO	9931560
WO	02061330	WO	02069306	WO	02091805
WO	02098182	WO	02098183	WO	02099780
WO	02101702	WO	03024269	WO	03026358
WO	03055273	WO	03067934	WO	03096761
WO	2004021747	WO	2004023850	WO	2004032572
WO	2004080291	WO	2004094896	WO	2005012997

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2009/042110

WO	2005052751	WO	2005060309	WO	2005084339
WO	2005089293	WO	2005089309	WO	2006023149
WO	2006031753	WO	2006069002	WO	2006069117
WO	2006071628				
US	2007120506	US	7265504	WO	2007064694
US	2006261752	JP	2006325396	KR	20060119018
				US	7358685
Due to data integration issues this family listing may not include 10 digit Australian applications filed since May 2001.					
END OF ANNEX					