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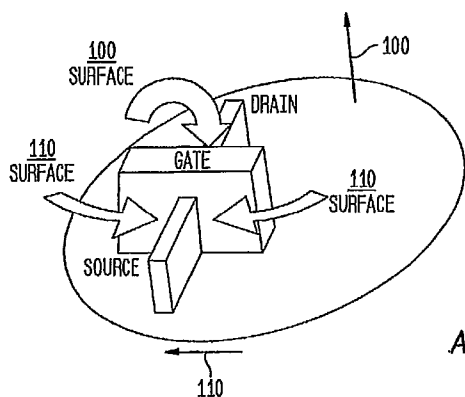
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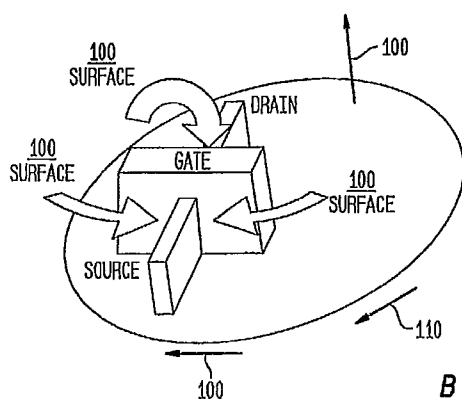
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(54) Title: HYBRID SUBSTRATE TECHNOLOGY FOR HIGH-MOBILITY PLANAR AND MULTIPLE-GATE MOSFETS



(57) Abstract: A hybrid substrate having a high-mobility surface for use with planar and/or multiple-gate metal oxide semiconductor field effect transistors (MOSFETs) is provided. The hybrid substrate has a first surface portion that is optimal for n-type devices, and a second surface portion that is optimal for p-type devices. Due to proper surface and wafer flat orientations in each semiconductor layers of the hybrid substrate, all gates of the devices are oriented in the same direction and all channels are located on the high mobility surface. The present invention also provides for a method of fabricating the hybrid substrate as well as a method of integrating at least one planar or multiple-gate MOSFET thereon.





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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6,204,098 B1 (ANCEAU) 20 March 2001 (20.03.2001), Figures 1-6 and its association.	1-3,6-7,10-12
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Y	Figures 1-6, whole document.	4-5,8-9,13-30,43
☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.		
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"O"	document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
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