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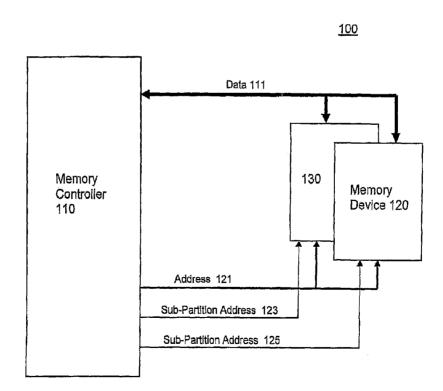
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(54) Title: MEMORY DEVICE ROW AND/OR COLUMN ACCESS EFFICIENCY



(57) Abstract: Embodiments for retrieving data from memory devices using sub-partitioned addresses are disclosed.



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C. DOCU	JMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where appropriate, of the relevant passages			Relevant to claim No.
Х	US 2003/0182513 A1 (DODD et al) 25 September 2003 (25.09.2003), see entire document.			1-2, 8-10, 16-17
Y	3-			3-6,11-14, 18-19
Y	US 2002/0093870A1 (NODA et al) 18 July 2002 (18.07.2002), see entire document.			1-19
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