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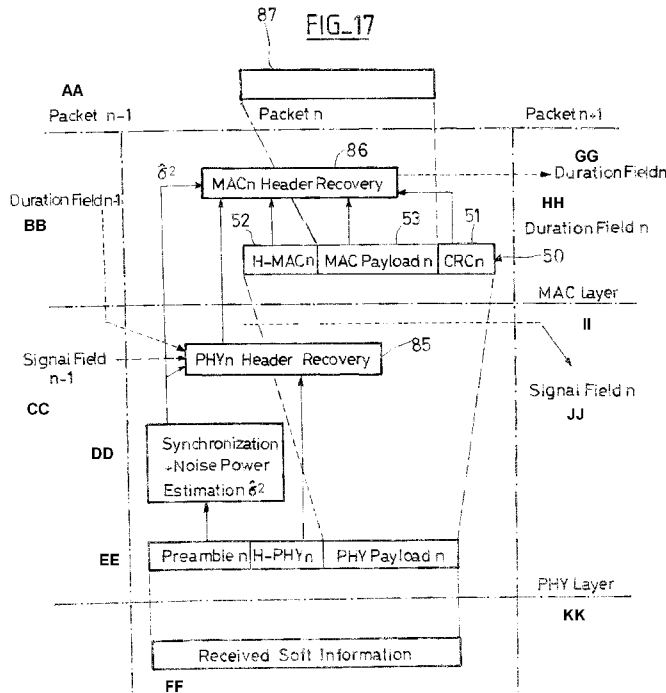
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(54) Title: RECOVERY OF TRANSMISSION ERRORS



(57) Abstract: A method for recovering transmission errors, comprising: receiving a data packet (50) comprising an error detection code (51) associated to data contained in the packet, wherein the data associated to the error detection code comprises primary data (52) and secondary data (53), checking the error detection code of the received packet to detect an erroneous state of the associated data, when the erroneous state is detected, (86) determining a finite set of candidate values for the primary data and, for each candidate values of the set: determining a marginal likelihood of the candidate value as a function of the error detection code of the received packet, determining a first correlation between the primary data of the received packet and the candidate value, and selecting a corrected value for the primary data among the set of candidate values as a function of said marginal likelihoods and said first correlations.

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## Recovery of transmission errors

### Field of the invention

The invention relates to the field of data transmission and recovery of  
5 transmission errors, especially using protocol data units corresponding to a protocol  
or a stack of several protocol layers.

### Background of the invention

Most networking communication processes are modeled in layers. This  
10 layered representation leads to the term protocol stack, which refers to the stack of  
layers in the protocol suite. By dividing the communication processes into layers, the  
protocol stack allows for division of labor, ease of implementation, e.g. through  
software, ease of software code testing, and the ability to develop alternative layer  
implementations. Layers communicate with those above and below via concise  
15 interfaces. In this regard, a layer provides services for the layer directly above it and  
makes use of services, e.g. a transport service, provided by the layer directly below it.  
At each layer, the protocol refers to a set of rules necessary to provide the services of  
the layer.

In a communication network, network devices implementing a given  
20 protocol layer send or receive data under the form of Protocol Data Units (PDU). The  
coding syntax of the PDUs is an attribute of the protocol layer. A PDU includes  
control data of the protocol layer and service data. The service data is the client  
data, i.e. data coming from an interface of the protocol layer and for which the  
services of the layer are used. Oftentimes, the service data is referred to as payload.  
25 The control data is data necessary to specify and control the services provided by the  
protocol layer. Usually, the control data is disposed in a header of the PDU.

### Summary of the invention

According to a first object, an embodiment of the invention provides a  
30 method for recovering transmission errors, comprising the steps of:  
receiving a protocol data unit corresponding to a protocol layer, wherein a format of  
said protocol data unit includes at least one control field for control data, at least one  
error detection field for an error detection code associated to said control data, and  
at least one service field for service data,

checking the error detection code of the protocol data unit to detect an erroneous state of the control data,  
when the erroneous state is detected, determining a finite set of candidate values for the control data and determining error detection code values associated to the  
5 candidate values of the set,  
determining first correlations i.e. similarities between the control data of the received protocol data unit and the respective candidate values,  
determining second correlations i.e. similarities between the error detection code of the received protocol data unit and error detection code values associated to the  
10 respective candidate values,  
and selecting a corrected value for the control data among the set of candidate values as a function of said first and second correlations.

In particular embodiments, the step of determining candidate values comprises determining a single candidate value for a first portion of the control field  
15 and determining multiple candidate values for a second portion of the control field and combining the single candidate value of the first portion with each candidate value of the second portion of the control field. In embodiments, the first portion of the control field comprises one or more subfields of the control field. Such subfields may be referred to as known or predictable fields. In embodiments, the second  
20 portion of the control field comprises one or more subfields of the control field. Such subfields may be referred to as unknown fields.

In accordance with the first object, an embodiment of the invention provides also a device for recovering control data in a protocol data unit corresponding to a protocol layer, wherein a format of said protocol data unit includes at least one  
25 control field for control data, at least one error detection field for an error detection code associated to said control data, and at least one service field for service data, the device comprising:

an input means for receiving a protocol data unit,  
an error detection code checking means for checking the error detection code of the  
30 protocol data unit to detect an erroneous state of the control data,  
a control data correction means operable to determine a finite set of candidate values for the control data, to determine error detection code values associated to the candidate values of the set, to determine first correlations between the control data of

the received protocol data unit and the respective candidate values, to determine second correlations between the error detection code of the received protocol data unit and error detection code values associated to the respective candidate values, and to select a corrected value of the control data among the set of candidate values  
5 as a function of said first and second correlations.

In accordance with a second object, an embodiment of the invention provides a method for recovering transmission errors, comprising:  
receiving a data packet comprising an error detection code associated to at least some of the data contained in the packet, wherein the data associated to the error  
10 detection code comprises primary data and secondary data,  
checking the error detection code of the received packet to detect an erroneous state of the associated data,  
when the erroneous state is detected, determining a finite set of candidate values for the primary data and, for each candidate values of the set:  
15 determining a marginal likelihood of the candidate value as a function of the error detection code of the received packet,  
determining a first correlation between the primary data of the received packet and the candidate value, and  
selecting a corrected value for the primary data among the set of candidate values as  
20 a function of said marginal likelihoods and said first correlations.

This method may be used for correcting errors found in the primary data, if any, without necessarily attempting to correct errors found in the secondary data, if any. In embodiments, the primary data includes data with a higher importance or higher priority or higher reliability-constraint than the secondary data. Such a  
25 method can be applied in a protocol layer decoder for recovering the control data necessary for the processing of a PDU, whereas the service data is treated as secondary data.

In an embodiment, the step of determining a marginal likelihood of the candidate value comprises:  
30 determining a finite set of potential values for the secondary data,  
sorting the potential values of the secondary data into subsets associated to respective error detection code values, wherein all the potential values of the secondary data within a subset are selected to yield the associated error detection code value when

combined with the candidate value of the primary data,  
for each subset, determining a marginal likelihood of the secondary data belonging  
to the subset as a function of the secondary data of the received packet and the error  
detection code of the received packet,

- 5 determining said marginal likelihood of the candidate value by cumulating said  
marginal likelihoods for all said subsets.

In an embodiment, the step of determining the marginal likelihood of the  
secondary data belonging to a subset comprises:

- determining a second correlation between the error detection code value associated  
10 to the subset and the error detection code of the received packet,  
determining third correlations between the potential values of the secondary data  
within the subset and the secondary data of the received packet, and  
determining the marginal likelihood of the secondary data belonging to the subset as  
a function of said second correlation and said third correlations.

- 15 In an advantageous embodiment, the step of determining a marginal  
likelihood of the candidate value comprises:

splitting the error detection code of the received packet into a plurality of blocks,

- for each block of the error detection code of the received packet, determining a  
20 partial marginal likelihood of the candidate value as a function of said block, and  
determining said marginal likelihood of the candidate value as a function of the  
partial marginal likelihood associated to all blocks of the error detection code.

In an embodiment, the step of determining a partial marginal likelihood of  
the candidate value comprises:

- 25 determining a finite set of potential values for the secondary data,  
sorting the potential values of the secondary data into subsets associated to respective  
values of the block of the error detection code, wherein all the potential values of the  
secondary data within a subset are selected to yield the associated block value when  
combined with the candidate value of the primary data,  
30 for each subset, determining a partial marginal likelihood of the secondary data  
belonging to the subset as a function of the secondary data of the received packet  
and the block of the error detection code of the received packet, and

determining said partial marginal likelihood of the candidate value by cumulating said partial marginal likelihoods for all said subsets.

In an embodiment, the step of determining the partial marginal likelihood of the secondary data belonging to a subset comprises:

- 5 determining a second correlation between the block value associated to the subset and the block of the error detection code of the received packet, determining third correlations between the potential values of the secondary data within the subset and the secondary data of the received packet, and determining the partial marginal likelihood of the secondary data belonging to the
- 10 subset as a function of said second correlation and said third correlations.

In an embodiment, the step of sorting the potential values of the secondary data into subsets associated to respective error detection code values or respective block values comprises constructing a trellis representation of the potential values of the secondary data.

- 15 In an embodiment, the trellis representation is constructed from a lower depth value to a higher depth value, wherein the depth of the trellis represents a number of bits of secondary data already determined.

- 20 In another embodiment, the trellis representation is constructed from a higher depth value to a lower depth value, wherein the depth of the trellis represents a number of bits of secondary data already determined.

In an embodiment, the same set of secondary data potential values is used for all candidate values of the primary data. Then this set may be determined only once. In other embodiments a different set of secondary data potential values may be used for different candidate values of the primary data.

- 25 In an embodiment, the finite set of potential values for the secondary data comprises all possible combinations of binary values for each and every bit of data of a field of the packet reserved for secondary data.

- 30 In an embodiment, the packet comprises a protocol data unit, wherein the primary data includes control data of the protocol data unit and the secondary data includes service data of the protocol data unit. The method may comprise the step of processing the service data of the received protocol data unit as a function of the corrected value of the control data.

In another embodiment, the packet comprises video data coded with a scalable video codec, wherein the primary data includes video data corresponding to a base layer and the secondary data includes video data corresponding to an enhancement layer.

5 In accordance with the second object, an embodiment of the invention provides a device for recovering transmission errors, comprising:  
an input means for receiving a data packet comprising an error detection code associated to at least some of the data contained in the packet, wherein the data associated to the error detection code comprises primary data and secondary data,

10

an error detection code checking means for checking the error detection code of the received packet to detect an erroneous state of the associated data,

and a primary data correction means operable to determine a finite set of candidate values for the primary data and, for each candidate values of the set, to determine a

15

marginal likelihood of the candidate value as a function of the error detection code of the received packet, to determine a first correlation i.e. similarities between the primary data of the received packet and the candidate value, and to select a corrected value for the primary data among the set of candidate values as a function of said marginal likelihoods and said first correlations.

20

In accordance with a third object, an embodiment of the invention provides a method for transmitting video data, comprising the steps of:

encoding a sequence of pictures with a scalable video codec to produce a first bitstream corresponding to a base layer and a second bitstream corresponding to an enhancement layer,

25

producing a sequence of data packets, wherein each packet comprises primary data belonging to the first bitstream, secondary data belonging to the second bitstream and an error detection code associated the primary and the secondary data, and transmitting the sequence of data packets through a communication channel, e.g. using any appropriate transmission protocol or stack of protocols.

30

The subclaims define further embodiments of the invention. Other embodiments result from combinations of the claims.

Embodiments of the invention are based on the idea of using an error detection code for detecting and recovering transmission errors in selected portions of

a PDU or packet, while disregarding potential errors affecting other portions of the PDU or packet. Embodiments of the invention can be applied to permeable protocol layer decoders operable to deliver a payload of the PDU regardless of its validity.

## 5           **Brief description of the drawings**

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter, by way of example, with reference to the drawings.

Figure 1 is a diagrammatic representation of a decoder module in  
10 accordance with an embodiment of the invention.

Figure 2 is a diagrammatic representation of a method for operating a permeable protocol layer, which may be executed by the decoder module of Figure 1.

Figure 3 represents an embodiment of a protocol stack and corresponding  
15 encapsulation scheme, in which the method of Figure 2 may be used.

Figure 4 is a diagrammatic representation of a communication network, which may use the protocol stack of Figure 3.

Figure 5 represents a data receiver in accordance with an embodiment of the invention, which may be used in the communication network of Figure 4.

20           Figures 6 and 7 represent packet formats corresponding to standard 802.11 PHY and MAC protocol layers respectively.

Figure 8 represents a transmission method, which may be used in the communication network of Figure 4.

25           Figure 9 is a diagrammatic representation of a method for operating a permeable PHY protocol layer in accordance with an embodiment of the invention.

Figure 10 is a graph showing the performance of a PHY layer decoder in accordance with an embodiment of the invention.

Figure 11 is a graph showing the performance of a MAC-Lite layer decoder in accordance with an embodiment of the invention.

30           Figures 12 and 13 are trellis diagrams.

Figure 14 is a graph showing the performance of an embodiment of a MAC layer decoder.

Figure 15 is a diagrammatic representation of a transmission system in accordance with an embodiment.

Figure 16 shows an embodiment of a decoder module, which may be used in the transmission system of Figure 15.

Figure 17 is a diagrammatic representation of a method for combining a permeable PHY protocol layer and a permeable MAC protocol layer in accordance with an embodiment of the invention.

### **Detailed description of illustrative embodiments**

Figure 1 is a diagrammatic representation of a decoder module 10 for decoding a protocol data unit corresponding to a predefined protocol. The decoder module 10 is intended to be used in a receiver, which receives data encoded in accordance with the protocol under consideration or a corresponding protocol stack from a transmitter over a communication channel. In the protocol under consideration, a format of the protocol data unit includes at least one control field for control data, at least one error detection field for an error detection code associated to all or part of said control data, and at least one service field for service data. In an embodiment, the PDU is a packet and the control field is a header of the packet.

The error detection code can be any type of redundant information computed at the transmitter from the data to be covered, i.e. all or part of the content of the control field, in order to allow detecting transmission errors at the receiver. In embodiments, the error detection code is a checksum or a Cyclic Redundancy Check (CRC) or a Forward Error Correction code (FEC).

The decoder module 10 comprises an input module 1 for receiving the protocol data unit. The protocol data unit may be obtained in diverse manners, depending on the protocol layer under consideration. In an embodiment, e.g. corresponding to a physical layer decoder, the input module receives the PDU from a detection module, not shown, which scans a received sequence of data to detect the PDU. The PDU can be recognized by detecting a predefined sequence of data, e.g. a known preamble. In another embodiment, the input module receives the PDU from another decoder module corresponding to a lower protocol layer.

Having received the PDU, the input module 1 identifies the control field and error detection field in accordance with the predefined format of the PDU and passes the content of those fields to an error detection code-checking module 2, which uses the error detection code of the received PDU to check the integrity of the corresponding control data. If the error detection code check is successful, i.e.

reveals no transmission error, the control data of the protocol data unit is assumed to be correct. Then the PDU is passed to a processing module 3, which processes the PDU in accordance with the functions of the protocol and the control data of the PDU.

5           The functions of the processing module 3 depend on the services being implemented by the protocol layer. Functions of the processing module 3 may include e.g. re-sequencing several PDUs, e.g. as a function of a sequence number included in the control data, delimiting and decapsulating the service data, e.g. as a function of a length included in the control data, assembling or concatenating the  
10 service data of several PDUs, segmenting the service data of a PDU, selecting a destination where the service data must be outputted, e.g. for protocol demultiplexing as a function of a protocol identifier included in the control data. Other types of services will be apparent to those skilled in the art. The processing performed by the processing module 3 causes the service data of the PDU to be sent, in full or in part,  
15 or in a concatenated or segmented form as the case may be, to an output 4 of the decoder module 10. In an embodiment, e.g. where the received data corresponds to a protocol stack, the output 4 delivers the service data of the PDU to a higher protocol layer decoder and the processing module 3 operates to put the service data in a form adapted to this higher protocol layer decoder. Although a single output is  
20 shown, the decoder module 10 may comprise several outputs, e.g. for outputting different types of payloads. The processing module may select an output as a function of the control data of the PDU.

In an embodiment, the processing module 3 stores selected portions of the control data of the PDU being processed, e.g. selected subfields of the header, in a  
25 caching module 5 to make this data available for use in intralayer or interlayer redundancy techniques, as will be explained below.

When the error detection code check performed by error detection code checking module 2 is unsuccessful, i.e. reveals transmission errors in the control data of the PDU, the PDU is passed to a control data correction module 6, which attempts  
30 to recover the control data. The control data correction module 6 determines a finite set of candidate values for the erroneous control data and determines error detection code values associated to the candidate values of the set. Then, the control data correction module 6 uses the error detection code of the received PDU to select the

best candidate among the previously determined candidate values. To do so, it compares, on the one hand, the control data and error detection code of the received protocol data unit with, on the other hand, the respective candidate values and associated error detection code values, and selects a corrected value for the control  
5 data among the set of candidate values as a function of the comparison results i.e. correlations between the compared data.

Once a corrected value of the control data has been selected, the control data correction module 6 sends the corrected PDU to the processing module 3. The processing module 3 operates as described above, yet as a function of the corrected  
10 control data of the protocol data unit in that case.

The set of candidate values could be simply constructed as an exhaustive list of all possible combinations of binary values for each and every bit of data of the control field. This would lead to a computational complexity with an order of magnitude  $2^{*\ell}$ , where  $\ell$  represents the length of the control field in number of bits  
15 and  $^{**}$  means 'power'.

In a preferred embodiment, the control data correction module 6 relies on prior knowledge about the control data, i.e. information external to the detected protocol data unit, to construct a more limited set of candidate values. Depending on the control fields, such prior knowledge can be obtained from different sources, e.g.  
20 specifications of the protocol layer, interlayer or intralayer redundancy, or a context of the communication in which the sequence of data is received.

Intralayer redundancy refers to the existence of consistent, deterministic relations between several identified items of information transmitted at the same protocol layer, e.g. relations between several fields of a given PDU and/or relations  
25 between fields of PDUs transmitted successively at the same protocol layer. Intralayer redundancy may serve to determine a candidate value or set of candidates values for control data of a received protocol data unit as a function of data that was received in an earlier protocol data unit, especially in the control field of the earlier PDU, at the same protocol layer.

30 Interlayer redundancy refers to the existence of consistent, deterministic relations between several identified items of information transmitted at different protocol layer, e.g. relations between one or more fields of a PDU at a first protocol layer and one or more fields of a PDU at a second protocol layer. Interlayer

redundancy may serve to determine a candidate value or set of candidates values for control data of a received protocol data unit at a given protocol layer as a function of data that was received in a protocol data unit, especially in the control field of the PDU, at a different, e.g. higher or lower, protocol layer. To implement intralayer  
5 redundancy techniques, the control data correction module 6 may retrieve data from the caching module 5. To implement interlayer redundancy techniques, the control data correction module 6 may retrieve data from similar caching modules at a higher or lower protocol layer, as shown by arrows 7 and 8.

The context of the communication refers to parameters that are known  
10 throughout a communication session between a transmitter and a receiver. Such parameters can be fixedly configured in the transmitter and receiver or negotiated at an early stage of the establishment of the communication channel, e.g. in an attachment procedure between a wireless terminal and a wireless base station or access point. Static or dynamic context parameters may also be transmitted in the  
15 signaling messages sent by the wireless base station or access point. In an embodiment, the control data correction module 6 may retrieve data from a memory 9 of the receiver where such parameters have been stored, e.g. in configuration files. As a consequence, candidate values for a subfield corresponding to such a parameter may be determined by accessing the memory 9.

20 In an embodiment, the control data correction module 6 integrates the specifications of the protocol layer under consideration in order to reduce as much as possible the set of candidate values for the control field of the PDU. For example, the specification of the protocol layer may provide a predefined constant value for a subfield of the control field. Accordingly, the control data correction module 6  
25 considers only candidate values in which this subfield is set to the predefined value. For another subfield, the specification of the protocol layer may provide a limited list of possible values. Accordingly, the control data correction module 6 considers only candidate values in which this subfield is set to the values selected in this limited list. Here "limited" means that the list contains fewer values than all combinations of  
30 binary values for each and every bit of data of the subfield.

In an embodiment, the control data correction module 6 relies on intralayer redundancy to determine a candidate value for a subfield of the control field, which corresponds to a relatively static parameter i.e. parameter unlikely to change from

one received PDU to the next. Intralayer redundancy can also be used to determine a candidate value for a subfield containing a parameter that evolves in a predefined manner from one received PDU to the next, e.g. by incrementing or decrementing the previously received value. For such subfields, the control data correction module 6  
5 determines candidate values for the subfield of the protocol data unit as a function of control data disposed in said subfield in a previously detected protocol data unit.

In some cases, a protocol stack may comprise a plurality of fields at the same or at different protocol layers, which contain information items that are similar or correlated through a predefined correlation law. In a corresponding embodiment,  
10 the control data correction module 6 uses such a predefined correlation law in combination with intralayer or interlayer redundancy techniques to determine one or more candidate values for a subfield of the control field of the PDU. For instance, a PDU at a first protocol layer encapsulates a PDU of a second protocol layer, i.e. carries the second layer PDU in the service field. A format of the PDU at the first layer  
15 comprises a first subfield representing a length of the service data and a second subfield representing a transmission rate of the service data. A format of the PDU at the second layer comprises a third subfield representing duration for transmitting the next second layer PDU. Hence, the duration received in one second layer PDU makes it possible to determine a relation between the length of the next second layer  
20 PDU to be received and the value of the first field in the PDU encapsulating it. Other examples of interlayer redundancy, e.g. similar information items that are transmitted redundantly at different layers, can be found in an IP/UDP/RTP protocol stack.

In a further embodiment, the service field at the first layer is used to carry exactly one PDU of the second layer. A simple relation is thus obtained between the  
25 first subfield and the second subfield of a subsequent PDU to be received at the first layer. By retaining only candidate values that satisfy that relation, the control data correction module 6 can heavily reduce the set of candidate values to be considered for correcting the control data of that subsequent PDU, if it has errors.

In a preferred embodiment, the service data of the PDU is not checked for  
30 transmission errors by the decoder module 10, in contrast with the control data. Hence, the decoder module 10 implements a so-called permeable protocol layer able to output the payload of a PDU, be it correct or damaged. By allowing correction of erroneous control data in the received PDUs, the decoder module 10 is

able to interpret and process a higher proportion of the received PDUs at the corresponding protocol layer, hence increasing the quantity of service data, e.g. number of payloads, reaching the next protocol layer or application layer. Preferably, the decoder module 10 receives the service data of the protocol data unit  
5 as soft information and processes it under that form, so that soft information corresponding to the service data is outputted to the next protocol layer or application layer. Here, soft information refers to a sampled signal which comprises several levels between an 'L' level indicative of a logic '0' and an 'H' level indicative of a logic '1'. By contrast, hard information refers to a sampled signal, which is only  
10 quantified in terms of logic '0's and logic '1's. Passing the soft information of the service data to the next layer makes it possible to use efficient decoding techniques or error correction techniques at the next layer. In an embodiment, application data in the form of soft information is passed through one or more permeable protocol layers of the receiver to an application layer implementing a joint source-channel  
15 decoding technique, which allows many errors to be corrected. Various robust joint source-channel decoders are known in the art, e.g. R. Bauer and J. Hagenauer. "On Variable Length Codes for Iterative Source/Channel Decoding", in Proceedings of DCC, pages 272–282, Snowbird, UT, 1998.

In a preferred embodiment, the decoder module 10 receives part or all of  
20 the PDU in the form of soft information and the control data correction module 6 uses the soft information of the control data to select the best-matching candidate value. In that embodiment, the control data correction module 6 computes the correlations with the control data and error detection code of the received protocol data unit provided as soft information. Using soft information in the computation  
25 improves the accuracy of the selection step.

In a modified embodiment, the error detection code checking module may be suppressed so that the control data correction module 6 receives every PDU from the input module 1 and handles them similarly.

#### Enhanced permeable layer mechanism

30 Figure 2 is a diagrammatic representation of a method for sending soft information from a protocol layer  $L-1$  to a protocol layer  $L+1$  through a permeable protocol layer  $L$ . A decoder module as shown on Figure 1 can implement the processing at permeable protocol layer  $L$ .

In the method of Figure 2, the protocol layer L receives a sequence of soft information 100, which includes a PDU of layer L 11. For example, the sequence of soft information 100 is provided by the lower protocol layer L-1 that obtained it by decapsulating the payload of one or more PDUs of layer L-1. In this embodiment, the control field of the PDU 11 is a header 12 and the error detection code field is a trailer 13 containing a CRC or checksum associated to the header. In Figure 2, 'n' is an index that refers to a PDU being currently processed, whereas 'n-1' refers to previously processed PDU. The header 12 and trailer 13 are converted to hard information to perform a CRC check, in order to detect whether the received header is erroneous. If not, the PDU is processed as a function of the information contained in the header 12. As a result, the payload 14 of the PDU is decapsulated and passed as soft information to its intended destination, i.e. protocol layer L+1.

If the CRC check reveals errors, a header recovery step 15 is performed. The header recovery step involves two main principles. Firstly, intra-layer and/or inter-layer redundancy is used to build some a priori information on candidate values for the header. Arrows 16 and 17 represent various sources of redundancy, which can be used to facilitate the header recovery at layer L. Secondly, the CRC or checksum of PDU 11 is used as an error-correcting code to select a best-matching corrected value. Hence, the header recovery step combines soft information provided by the lower protocol layer, properties of the CRC or checksum, as well as previously introduced a priori information. Although the header 12 is removed after decoding, its information fields are necessary to deliver the payload 14 to layer L+1, for further processing. In the embodiment shown, the payload 14 comprises a PDU 18 of layer L+1.

#### 25      MAP estimator for robust header recovery

Next, a generic model of a PDU, e.g. a packet, and of a corresponding header recovery method will be described. This model enables one skilled in the art to implement the method of Figure 2 with any given protocol.

At a given layer L, incoming packets include one or more control fields, e.g. a header, one or more service fields, e.g. a payload, and an error detection field, e.g. a CRC. Information protected by the CRC  $\mathbf{c}$  of  $\ell c$  bits can be split into four parts. The constant fields, represented by the vector  $\mathbf{k}$  of  $\ell k$  bits, are assumed to be known, e.g. as constant values of the protocol specification. The predictable fields are

embedded in the vector  $\mathbf{p}$  of  $\ell p$  bits. Contrary to known fields, the predictable fields are estimated by exploiting the intra-layer and inter-layer redundancy represented by  $R$ . They are predicted by using information contained in the previous received packets. The predictable fields are assumed to be entirely determined if the preceding  
 5 packets have been correctly received. The important unknown fields are collected in the vector  $\mathbf{u}$  of  $\ell u$  bits. These parameters are either completely unknown or limited to a finite set of values  $\Omega_u(\mathbf{k}, \mathbf{p}, R)$ , which content may be a function of the values of  $\mathbf{k}$ ,  $\mathbf{p}$  and  $R$ . Finally, the vector  $\mathbf{o}$  of  $\ell o$  bits contains the other fields covered by the CRC, if any. This last part contains unknown data, which are not required for the  
 10 processing of the packet at layer  $L$ , but which may be important at layer  $L + 1$ .

All these fields are collected in a vector  $\mathbf{r} = [\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{o}]$  of  $\ell r = \ell k + \ell p + \ell u + \ell o$  bits. The order of the bits in vector  $\mathbf{r}$  may not correspond to the order in which data is transmitted in a packet.

The CRC  $\mathbf{c}$  associated to  $\mathbf{r}$  is evaluated as  $\mathbf{c} = F(\mathbf{r})$ , where  $F$  is a generic  
 15 encoding function. More precisely, the evaluation of  $\mathbf{c}$  depends on a generator polynomial characterizing the CRC :

$$g(x) = \sum_{i=0}^{\ell_c} g_i x^i$$

A systematic generator matrix  $\mathbf{G} = [\mathbf{I}, \mathbf{\Pi}]$  can be associated to  $g(x)$ . Using  $\mathbf{G}$ ,  $\mathbf{c}$  may be obtained iteratively as follows:

$$20 \quad \begin{cases} \mathbf{c}^0 = \mathbf{0}, \\ \mathbf{c}^{j+1} = \mathcal{F}(\mathbf{r}^{j+1}) = \mathbf{c}^j \oplus (r_{j+1} \cdot \pi(j+1)). \end{cases} \quad \text{Eq. (1)}$$

where 'j' is an index denoting the number of iterations.

In Eq. (1),  $\mathbf{r}^j = [r_1 \dots r_j, 0 \dots 0]$  and  $\oplus$  is the XOR operator.  $\pi(j)$  represents the parity vector related to bit  $r_j$  and does not necessarily correspond to the  $j$ -th line of  $\mathbf{\Pi}$ . After  $\ell r$  iterations,  $\mathbf{c}^{\ell r} = F(\mathbf{r})$ .

25 Additionally, we consider that data have been transmitted over an Average White Gaussian Noise channel introducing a noise of zero mean and variance  $\sigma^2$ . Noisy data and CRC coming from layer  $L-1$  are denoted by

$$30 \quad \mathbf{y} = [\mathbf{y}_k, \mathbf{y}_p, \mathbf{y}_u, \mathbf{y}_o, \mathbf{y}_c], \quad \text{Eq. (2)}$$

which includes observations of  $\mathbf{k}$ ,  $\mathbf{p}$ ,  $\mathbf{u}$ ,  $\mathbf{o}$  and  $\mathbf{c}$ . Since  $\mathbf{k}$  and  $\mathbf{p}$  are known or may be predicted, only  $\mathbf{u}$  remains to be estimated. A maximum a posteriori

estimator (MAP estimator) is developed, taking into account the observations  $\mathbf{y}$ , the knowledge of  $\mathbf{k}$  and  $\mathbf{p}$ , the redundancy  $R$  as well as the CRC properties useful to estimate  $\mathbf{u}$ :

$$\hat{\mathbf{u}}_{\text{MAP}} = \arg \max_{\mathbf{u}} P(\mathbf{u}|\mathbf{k}, \mathbf{p}, R, \mathbf{y}_u, \mathbf{y}_o, \mathbf{y}_c) \quad \text{Eq. (3)}$$

5 Here, the notation  $P(\cdot|\cdot)$  denotes a conditional probability. After some derivations, one obtains:

$$\hat{\mathbf{u}}_{\text{MAP}} = \arg \max_{\mathbf{u}} P(\mathbf{u}, \mathbf{y}_u, \mathbf{y}_o, \mathbf{y}_c|\mathbf{k}, \mathbf{p}, R). \quad \text{Eq. (4)}$$

The channel is memory less. Assuming that  $\mathbf{o}$  is independent of  $R$ , one may write:

$$\begin{aligned} & P(\mathbf{u}, \mathbf{o}, \mathbf{y}_u, \mathbf{y}_o, \mathbf{y}_c|\mathbf{k}, \mathbf{p}, R) \\ 10 \quad & = P(\mathbf{u}|\mathbf{k}, \mathbf{p}, R)P(\mathbf{y}_u|\mathbf{u})P(\mathbf{o}, \mathbf{y}_o, \mathbf{y}_c|\mathbf{k}, \mathbf{p}, \mathbf{u}). \quad \text{Eq. (5)} \end{aligned}$$

In Eq. (5),  $P(\mathbf{u}|\mathbf{k}, \mathbf{p}, R)$  represents the a priori probability of  $\mathbf{u}$  and  $P(\mathbf{y}_u|\mathbf{u}) \sim \mathcal{N}(\mathbf{u}, \sigma^2 I_{\ell_u})$ , where  $\mathcal{N}(\cdot, \cdot)$  denotes a Gaussian law with the first argument being the mean value and the second argument being the variance.

15 The possible values of  $\mathbf{u}$ , i.e. those retained as candidate values, are gathered in:

$$\Omega_u = \Omega_u(\mathbf{k}, \mathbf{p}, R)$$

Assuming that combinations of  $\mathbf{u}$  are equally likely, one gets

$$P(\mathbf{u}|\mathbf{k}, \mathbf{p}, R) = P(\mathbf{u}|\Omega_u) = \frac{1}{\text{card}(\Omega_u)}$$

20 Marginalizing Eq. (5) over the  $2^{**\ell_o}$  combinations of  $\mathbf{o}$ , one obtains:

$$\begin{aligned} & P(\mathbf{u}, \mathbf{y}_u, \mathbf{y}_o, \mathbf{y}_c|\mathbf{k}, \mathbf{p}, R) \\ & = P(\mathbf{u}|\Omega_u)P(\mathbf{y}_u|\mathbf{u}) \sum_{\mathbf{o}} P(\mathbf{o}, \mathbf{y}_o, \mathbf{y}_c|\mathbf{k}, \mathbf{p}, \mathbf{u}) \quad \text{Eq. (6)}. \end{aligned}$$

The properties of the CRC are involved in:

$$\sum_{\mathbf{o}} P(\mathbf{o}, \mathbf{y}_o, \mathbf{y}_c|\mathbf{k}, \mathbf{p}, \mathbf{u})$$

Finally, combining Eq. (6) in Eq. (3), the MAP estimator is expressed as:

$$25 \quad \hat{\mathbf{u}}_{\text{MAP}} = \arg \max_{\mathbf{u} \in \Omega_u} P(\mathbf{y}_u|\mathbf{u})\Psi(\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{y}_o, \mathbf{y}_c) \quad \text{Eq. (7)}$$

where

$$\Psi(\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{y}_o, \mathbf{y}_c) = \sum_{\mathbf{o}} P(\mathbf{o}, \mathbf{y}_o, \mathbf{y}_c | \mathbf{k}, \mathbf{p}, \mathbf{u})$$

represents a marginal likelihood of the vector  $[\mathbf{k}, \mathbf{p}, \mathbf{u}]$  as a function of the observations of the error detection code  $\mathbf{y}_c$  and the other data  $\mathbf{y}_o$ .

5 The above categorization of the fields is intended to encompass most transmission protocols. Some field categories may not apply to a specific protocol under consideration. In other words, in specific instances, some of the field categories are considered as empty, e.g. known fields  $\mathbf{k}$ , predictable fields  $\mathbf{p}$  and/or other fields  $\mathbf{o}$ .

#### 10 First embodiment of the MAP estimator

In a preferred embodiment, the CRC covers only the control data at layer L, i.e. the header. Then,  $\mathbf{o}$  is empty and the sum in Eq. (6) becomes:

$$\sum_{\mathbf{o}=\emptyset} P(\mathbf{o}, \mathbf{y}_o, \mathbf{y}_c | \mathbf{k}, \mathbf{p}, \mathbf{u}) = P(\mathbf{y}_c | \mathcal{F}([\mathbf{k}, \mathbf{p}, \mathbf{u}])).$$

In this case, Eq. (7) simplifies to

$$15 \quad \hat{\mathbf{u}}_{\text{MAP}} = \arg \max_{\mathbf{u} \in \Omega_{\mathbf{u}}} P(\mathbf{y}_u | \mathbf{u}) P(\mathbf{y}_c | \mathcal{F}([\mathbf{k}, \mathbf{p}, \mathbf{u}])) \quad \text{Eq. (8)}$$

In Eq. (8),  $[\mathbf{k}, \mathbf{p}, \mathbf{u}]$  vectors represent a set of candidate values for the erroneous control data and  $\mathcal{F}([\mathbf{k}, \mathbf{p}, \mathbf{u}])$  vectors represent error detection code values associated to the candidate values of the set, which can be directly evaluated by Eq. (1), e.g. an elementary CRC computation. Since a sum over all possible values of vector  $\mathbf{o}$  is not needed in that case, the computational complexity is heavily reduced  
 20 in comparison to Eq. (7). Whereas vectors  $\mathbf{k}$  and  $\mathbf{p}$  refer to one or more fields of the header that are fixed for a given PDU,  $\mathbf{u}$  refers to one or more fields that may take multiple values, i.e. any value selected in the finite set  $\Omega_{\mathbf{u}}$ . The conditional probabilities in Eq. (7) can be evaluated by computing corresponding correlation  
 25 functions, i.e. a correlation between the observed value of the unknown field and the respective candidate values for the unknown field for the first one, and a correlation between the observed value of the error detection code field of the PDU and the error detection code values associated to the respective candidate values for the control data.

Such correlations may be computed as a Euclidian distance between hard decision data. When the observations  $\mathbf{y}$  are provided as soft data, a soft channel decoder can be used to determine a likelihood parameter for each possible state of a bit, i.e. a likelihood parameter for the bit being a '1' and a likelihood parameter for the bit being a '0'. Then a correlation between the hard data of the candidate value and the soft data of the observation may be computed as a weighted Euclidian distance, where the likelihood parameters are used as weight factors.

In general, solving Eq. (8) allows selecting a single corrected value for the control data among the set of candidate values. In embodiments, transmission errors affecting only the error detection code can also be corrected by this method.

In Eq. (8), the known and predictable fields are not taken into consideration in the first conditional probability. However, they may be taken into consideration in a modified embodiment. Since these fields are single-valued, they do not contribute to discriminate between the respective candidate values for the control data anyway.

As mentioned before, deterministic relation laws between fields at a given layer, i.e. intralayer relations or at different layers, i.e. interlayer relations facilitate the recovery of erroneous headers, since they allow reducing the size of the  $\Omega_u$  set.

#### Application to 802.11 standard

Embodiments of an IEEE 802.11 (WiFi) network will now be described, in which header recovery methods are implemented at a PHY layer and/or at a MAC layer.

Figure 3 illustrates an example of an RTP/UDP/IP protocol stack, which can be used for multimedia packet transmission in combination with the 802.11 standard (WiFi). 'H' denotes a header of the respective PDUs and APL refers to an application layer. At PHY layer, a CRC protects the header fields. At MAC layer, a CRC protects the header and the payload. At IPv4 layer, the header fields are protected by a checksum. At UDP layer, a checksum protects the header and the payload.

The 802.11 standard can be combined with other protocols. The protocol stack of Figure 3 is not limitative. The segmentation and encapsulation mechanisms depicted at each protocol layer in Figure 3 are purely illustrative and not limitative.

Figure 4 represents an embodiment of a network architecture, in which the protocol stack of Figure 3 can be used. In this embodiment, we consider a downlink multimedia transmission over the 802.11 radio interface between an access point AP

and a terminal T1. The multimedia stream 25 comes from a media server 20, e.g. through a wide area network 21 and a router R1.

Figure 5 is a diagrammatic representation of a receiver device 30 comprised in the terminal T1 to receive and decode the media stream. The receiver device 30 5 comprises a radio front-end 31 and a series of decoder modules 32 to 37 corresponding to the respective protocol layers. The radio front-end 31 demodulates the received radio signal and passes the baseband signal, preferably in the form of soft information, to the PHY decoder module 32.

With reference to Figures 6 and 7, PHY and MAC layer specifications are 10 now briefly recalled.

#### DSSS PHY Layer Description

At PHY layer, the 802.11 standard provides 1 or 2 Mbps transmission rate in the 2.4 GHz band using either Frequency Hopping Spread Spectrum (FHSS) or Direct Sequence Spread Spectrum (DSSS). In DSSS, an 11 chip Barker code sequence is 15 used for spreading the 1 Mbps bit stream. The coded flow thus represents an 11 MHz baseband signal. A Differential BPSK (DBPSK) or Differential QPSK (DQPSK) modulation is applied to provide the 1 Mbps or 2 Mbps bit rate respectively.

The DSSS PHY packet format 40 is illustrated in Figure 6. The preamble 41 and the header 42 are transmitted by using the 1 Mbps DBPSK modulation while the 20 payload 43 is modulated either in 1 Mbps DBPSK or 2 Mbps DQPSK. In such PHY packets, the SYNC field 44 and SFD field 45 consist of 144 known bits, which are not protected by the CRC. These fields may be used to estimate the variance of the noise introduced by the channel.

The CCITT CRC-16 field 46 of 2 bytes protects the Signal field 47, Service 25 field 48, and Length field 49. Using the notations of the generic model above, the field 46 will be denoted  $c^{PHY}$  and its associated encoding function is denoted by  $F^{PHY}$ . The payload 43, assumed to contain only one MAC packet in this example, is not protected at this layer. Service field 48 is reserved for future recommendation. It is set to  $00_{16}$ , and included in  $k^{PHY}$ , according to the notations of the generic model. 30 The value of the Signal field 47 indicates the payload modulation and may only be equal to  $0A_{16}$  or  $14_{16}$  for 1 or 2 Mbps bit rate respectively. The value of the Length field 49 indicates on 2 bytes the number of microseconds required to transmit the payload 43. It depends on both the bit rate and the payload size. It may range from

16 to  $(2^{**}16-1)$ . Using the notations of the generic model above, Signal 47 and Length 49 form thus the vector  $\mathbf{u}^{\text{PHY}}$ . At this layer, the vectors  $\mathbf{p}^{\text{PHY}}$  and  $\mathbf{o}^{\text{PHY}}$  are empty.

#### MAC Layer Description

5           The MAC packet format 50 is depicted in Figure 7. In this packet, the CRC field 51 of 4 bytes, denoted  $\mathbf{c}^{\text{MAC}}$ , protects both the header field 52 and the payload 53. Its encoding function is denoted  $F^{\text{MAC}}$ .

          Considering a specific communication context, e.g. a non-encrypted downlink transmission of ordered MAC data packets with deactivated retransmission and power-save mode, all the subfields of the 2 byte Frame Control field 54 are  
10 either known or easily predicted, except the More Frag flag 55 which will vary more dynamically. The 6 byte Address field 56 contains the MAC address of the terminal T1 and is thus known. The last field 57 of the MAC header is reserved for local wireless networks. It is composed of 6 bytes of zeros when not used. Using the  
15 notations of the generic model, all the previously mentioned fields may thus be embedded in  $\mathbf{k}^{\text{MAC}}$  or  $\mathbf{p}^{\text{MAC}}$ .

          The 6 byte Address field 58 contains the MAC address of the access point AP. This address is transmitted to the terminal T1 during the medium reservation procedure (RTS-CTS) and will thus remain known by the receiver during the whole  
20 communication session. The 6 byte Address field 59 corresponds to the MAC address of the router R1. As soon as the access point AP has already received the router address in one information packet, the Address field 59 may be easily predicted by the receiver for subsequent packets if the access point AP is connected to a single router.

25           The 2-byte Sequence Control field 60 contains two parameters: a sequence number and a fragment number. The sequence number represents the value of the current IP packet counter. The fragment number indicates the value of the current MAC packet counter. Assuming that packets are transmitted in order, these parameters can be easily determined: the sequence number is incremented by one  
30 for each RTS-CTS and the fragment number is incremented by one for each received MAC packet. Thus, the receiver may estimate the Sequence Control field 60 by incrementing the previously received value. All these predictable single-valued fields may thus be treated as belonging to  $\mathbf{p}^{\text{MAC}}$ .

The More Frag flag 55 specifies if the current MAC packet is the last one for a sequence of fragments of an IP packet. The 2 bytes of Duration field 61 indicates the number of microseconds required to transmit the next MAC packet and some control parameters. Its value depends on the current modulation and the size of the next MAC packet. These two fields are embedded in the vector  $\mathbf{u}^{\text{MAC}}$  of the generic model. Finally, the payload contains the data to be transmitted and its size is between 0 and 2312 bytes. It is represented by  $\mathbf{o}^{\text{MAC}}$ .

In an embodiment, in order to allow for some packet loss and/or some degree of packet re-ordering, the receiver can build a limited set of candidate values for the Sequence Control field 60 by considering all the values that fall within a limited range from the last received value, e.g. between -10 and +10. Then this field must be considered in vector  $\mathbf{u}^{\text{MAC}}$  instead of  $\mathbf{p}^{\text{MAC}}$ .

In an embodiment, in order to allow for several routers, the receiver can build a limited set of candidate values for the Address field 59 by accessing a list of most recent or most frequent values. Then this field must be considered in vector  $\mathbf{u}^{\text{MAC}}$  instead of  $\mathbf{p}^{\text{MAC}}$ .

#### Interlayer Correlations

To explain correlations between the 802.11 PHY and MAC layers, the transactions at MAC layer will now be described. Figure 8 illustrates the 802.11 MAC transmission protocol when two MAC data packets have to be delivered. Transmission is initialized by medium reservation procedure consisting in an RTS-CTS exchange between the access point AP and terminal T1. Data packets are then transmitted to terminal T1, which acknowledges them (ACK). Control packets such as RTS, CTS, and ACK are assumed to be correctly received by terminal T1. Only errors in the data packets will be considered. A Short Inter-Frame Space (SIFS) of  $10 \mu\text{s}$  separates each packet to avoid collision. When all the packets are received by terminal T1, the transmission is finished and a Distributed Inter-Frame space (DIFS) of  $50 \mu\text{s}$  precedes the next medium reservation procedure. A Duration field 61 is included in each packet and its value indicates the number of microseconds required to transmit the next packet. The Duration value allows to adjust the Network Allocation Vector (NAV) for the other terminals. The other stations cannot communicate during the NAV period to avoid interferences.

Next,  $D_n^{\text{MAC}}$  and  $B_n^{\text{PHY}}$  denote the value of Duration field 61 and of the transmission bit rate coded in Signal field 47 associated to the  $n$ -th packet transmitted by the access point AP (either RTS or data packets). Following the MAC layer specifications of 802.11 standard,  $D_n^{\text{MAC}}$  is defined as:

$$D_n^{\text{MAC}} = 3T_{\text{SIFS}} + 3T_{\text{OVH}} + 2\frac{\ell_{\text{C-A}}}{B_n^{\text{PHY}}} + \frac{\ell_{x,n+1}^{\text{PHY}}}{B_n^{\text{PHY}}} \quad \text{Eq. (12)}$$

5 except for the last packet of a sequence of fragments of an IP packet, i.e., when the value of More Frag  $M_n^{\text{MAC}} = 0$ . In this case, one has:

$$D_n^{\text{MAC}} = T_{\text{SIFS}} + T_{\text{OVH}} + \frac{\ell_{\text{C-A}}}{B_n^{\text{PHY}}} \quad \text{Eq. (13)}$$

In Eqs. (12) and (13),  $T_{\text{SIFS}}$  denotes the duration of a SIFS and  $T_{\text{OVH}}$  represents  
 10 the duration for transmitting at 1 Mbps the PHY overhead of constant size composed of the preamble and the header. The other terms of Eq. (12) depend on the current bit rate  $B_n^{\text{PHY}}$ . CTS and ACK have the same constant size  $\ell_{\text{C-A}}$ , and  $\ell_{\text{C-A}}/B_n^{\text{PHY}}$  corresponds to the duration for sending one of these packets. Finally,  $\ell_{x,n+1}^{\text{PHY}}/B_n^{\text{PHY}}$  refers to the transmission duration of the next PHY payload of  $\ell_{x,n+1}^{\text{PHY}}$  bits.

#### 15 PHY Header Recovery

For a given packet ' $n$ ' at PHY layer, the observations associated to  $\mathbf{k}_n^{\text{PHY}}$ ,  $\mathbf{u}_n^{\text{PHY}}$ , and  $\mathbf{c}_n^{\text{PHY}}$  defined above are collected in:

$$\mathbf{y}_n^{\text{PHY}} = [\mathbf{y}_{k,n}^{\text{PHY}}, \mathbf{y}_{u,n}^{\text{PHY}}, \mathbf{y}_{c,n}^{\text{PHY}}]$$

In addition,  $\mathbf{y}_{x,n}^{\text{PHY}}$  denotes the observations related to the  $\ell_{x,n}^{\text{PHY}}$  bits of the  
 20 payload. The number of values that  $\mathbf{u}^{\text{PHY}}$  may take is significantly reduced when exploiting the Duration field contained in the previously received MAC packet (either an RTS or a data packet). Using  $B_{n-1}^{\text{PHY}}$  for the previous packet at PHY layer and  $D_{n-1}^{\text{MAC}}$  for the previous packet at MAC layer, one may deduce  $\ell_{x,n}^{\text{PHY}}$  from Eq. (12) as:

$$\ell_{x,n}^{\text{PHY}} = \left( D_{n-1}^{\text{MAC}} - 3T_{\text{SIFS}} - 3T_{\text{OVH}} - 2\frac{\ell_{\text{C-A}}}{B_{n-1}^{\text{PHY}}} \right) B_{n-1}^{\text{PHY}} \quad \text{Eq. (14)}$$

25

Then, the duration  $L_n^{\text{PHY}}$  coded in the Length field 49 of the current PHY packet  $n$  may be computed using:

$$L_n^{\text{PHY}} = \frac{\ell_{x,n}^{\text{PHY}}}{B_n^{\text{PHY}}} \quad \text{Eq. (15)}$$

In Eq. (14),  $\ell_{x,n}^{\text{PHY}}$  is totally determined assuming correct estimation of the header of the previous packet 'n-1' at PHY and MAC layers. Then, according to Eq. (15),  $L_n^{\text{PHY}}$  may only take two values depending on the value of  $B_n^{\text{PHY}}$ . Integrating these structural correlations in Eq. (8), one obtains the definition of a MAP estimator specific to the PHY layer:

$$\hat{u}_n^{\text{PHY}} = \arg \max_{u_n^{\text{PHY}} \in \Omega_{u,n}^{\text{PHY}}} P(y_{u,n}^{\text{PHY}} | u_n^{\text{PHY}}) P(y_{c,n}^{\text{PHY}} | c_n^{\text{PHY}}) \quad \text{Eq. (16)}$$

$$\text{where } c_n^{\text{PHY}} = \mathcal{F}^{\text{PHY}} ([k_n^{\text{PHY}}, u_n^{\text{PHY}}])$$

Thus, only values of the fields Signal 47 and Length 49, which satisfy Eq. (15) are stored in  $\Omega_{u,n}^{\text{PHY}}(\mathbf{k}, \mathbf{p}, R)$  and taken into consideration to correct the header.

The intra-layer and inter-layer redundancy described above can be exploited in an enhanced PHY layer decoder module 32 constructed similarly to the decoder module 10 of Figure 1. In that embodiment, the control data correction module 6 accesses information about the previously received packet in order to determine values of the Length field 49, which satisfy Eq. (15). For example, the control data correction module 6 computes  $\ell_{x,n}^{\text{PHY}}$  through Eq. (14) by reading the parameter  $B_{n-1}^{\text{PHY}}$  in caching module 5, where it was previously written by processing module 3. Similarly, parameter  $D_{n-1}^{\text{MAC}}$  may be obtained from the MAC decoder module 33, e.g. through a temporary data storage.

In that embodiment, the control data correction module 6 constructs a set of two candidate values for the PHY header 42 of packet 'n', which correspond to the only two values of the Signal field 47 allowed by the protocol specification, i.e.  $0A_{16}$  and  $14_{16}$ . The resulting CRC values for the field 46 are also computed. Then the control data correction module 6 evaluates Eq. (16) to select the candidate value that best matches the observation, i.e. the received value of the header field 42 and CRC field 46. This allows recovering the correct header value in many cases. Then the processing module 3 of this embodiment uses the recovered value of the Length field 49 to correctly delimit and decapsulate the payload of the PHY packet 'n'.

The above embodiment assumes that the service field 48 is constantly set to  $00_{16}$ . However, if a particular implementation of the protocol allows several values for the service field 48, then a corresponding PHY layer decoder module can be implemented based on the same principle. In order to construct a set of candidate values for header recovery, all possible values of the service field 48 are then taken into consideration in combination with the two possible values of the Signal field 47.

Figure 9 illustrates an embodiment of a decoding method for the PHY layer, which may be executed by the PHY layer decoder module 32 at the receiver. Arrows 62 show the exchange of information between layers and between consecutive packets. The header recovery step 66 is performed in accordance with the above.

In addition,  $\mathbf{y}_{s,n}^{\text{PHY}}$  denotes the observations of the known preamble  $\mathbf{s}^{\text{PHY}}$  of  $\ell_s^{\text{PHY}}$  bits. As explained above, the receiver synchronization is performed with  $\mathbf{s}^{\text{PHY}}$ . In an embodiment, the noise power  $\sigma^2$  is estimated from  $\mathbf{s}^{\text{PHY}}$  and  $\mathbf{y}_{s,n}^{\text{PHY}}$ . This measure is useful for working with the soft information, as it allows the evaluation of all likelihoods. The noise power estimator is given by:

$$\hat{\sigma}^2 = \frac{1}{\ell_s^{\text{PHY}}} \|\mathbf{y}_{s,n}^{\text{PHY}} - \mathbf{s}^{\text{PHY}}\|^2 \quad \text{Eq. (19)}$$

where  $\|\cdot\|$  denotes the Euclidian distance.

Bypassing the header recovery step 66 when the normal CRC check is successful may minimize computational complexity. In an embodiment, the header recovery processing is deactivated when the quality of the soft information provided by the lower layer is too poor, i.e., when the signal power is lower than a pre-defined threshold. In such a case, the packet is discarded.

In Fig. 9, the MAC decoding step 63 may or may not include a MAC header recovery method.

#### PHY Simulation results

A transmission system 64 shown on Fig. 4 consisting of a transmitter AP, an AWGN channel 65, and a receiver T1 has been numerically simulated. The AP generates PHY and MAC packets following the formats defined in Figs. 6 and 7. The MAC payloads consist of a variable amount of randomly generated bytes. The transmitter modulates data in DBPSK for all the simulations. Three types of PHY header recovery methods are considered in T1. A standard decoder performs hard decisions on the data at channel output. A robust decoder exploits only the intra-

layer and inter-layer redundancy through a soft decoding algorithm, neglecting the information provided by the CRC. Finally, a CRC robust decoder combines the intra-layer and inter-layer redundancy together with the information provided by the CRC through the header recovery method presented in the previous section using Eq. (16).

5 Performance analysis in terms of EHR (Erroneous Header Rate) versus Signal to Noise Ratio is presented in Fig. 10. In Figure 10, the standard, robust, and CRC-robust PHY decoders are compared. The robust decoders outperform the standard one. An EHR of less than  $10^{-5}$  is obtained with the robust decoder for an SNR above 4 dB and for an SNR above 2 dB for CRC-robust decoder. With the standard  
10 decoder, an SNR of at least 15 dB is required to get a comparable EHR. At PHY layer, considerable coding gains for a relatively low additional complexity are thus observed.

#### MAC-lite Header Recovery

In an embodiment of the transmission system of Fig. 4, the MAC layer is  
15 implemented with a modified packet format, in which the CRC field 51 is modified to protect only the header field 52 of the MAC packet. This modified MAC protocol will be referred to as MAC-Lite by analogy to the UDP-Lite protocol described in Request For Comment 3828 by the Internet Engineering Task Force. In this case, since vector  $\mathbf{o}^{\text{MAC}}$  is empty, a MAC-Lite decoder module can implement a header recovery method  
20 using a MAP estimator defined as:

$$\hat{\mathbf{u}}_n^{\text{MAC}} = \arg \max_{\mathbf{u}_n^{\text{MAC}} \in \Omega_{\mathbf{u},n}^{\text{MAC}}} P(\mathbf{y}_{u,n}^{\text{MAC}} | \mathbf{u}_n^{\text{MAC}}) P(\mathbf{y}_{c,n}^{\text{MAC}} | \mathbf{c}_n^{\text{MAC}}) \quad \text{Eq. (20)}$$

where  $\mathbf{c}_n^{\text{MAC}} = \mathcal{F}^{\text{MAC}}([\mathbf{k}_n^{\text{MAC}}, \mathbf{p}_n^{\text{MAC}}, \mathbf{u}_n^{\text{MAC}}])$

Thus at a MAC-Lite layer, a header recovery method can be used with steps similar to the above described PHY layer. To construct the candidate values, the  
25 known and predictable fields defined in the MAC layer description are set to their respective single predicted values. In this embodiment, the MAC-lite decoder module uses intralayer redundancy to determine those values.

The PHY packet payload  $\mathbf{y}_{x,n}^{\text{PHY}}$  enters the MAC layer and contains the observations associated to  $\mathbf{k}_n^{\text{MAC}}$ ,  $\mathbf{p}_n^{\text{MAC}}$ ,  $\mathbf{u}_n^{\text{MAC}}$ ,  $\mathbf{o}_n^{\text{MAC}}$ , and  $\mathbf{c}_n^{\text{MAC}}$  specified in the  
30 MAC layer description. Using the conventions defined in the generic model, one may write:

$$y_n^{\text{MAC}} = y_{r,n}^{\text{PHY}} = [y_{k,n}^{\text{MAC}}, y_{p,n}^{\text{MAC}}, y_{u,n}^{\text{MAC}}, y_{o,n}^{\text{MAC}}, y_{e,n}^{\text{MAC}}]$$

The number of possible combinations for  $\mathbf{u}^{\text{MAC}}_n$  may be significantly reduced when exploiting properties of Eqs. (12) and (13). Indeed, the value of Duration  $D^{\text{MAC}}_n$  is totally determined when  $M^{\text{MAC}}_n = 0$ . When  $M^{\text{MAC}}_n = 1$ , the value of Duration field  
 5 61 depends on the next PHY payload size. The number of combinations is a function of the range of MAC payload size. Considering that the payload contains an entire number of bytes, the possible values of  $\ell_{r,n+1}^{\text{PHY}}$  in Eq. (12) are given by:

$$\ell_{r,n+1}^{\text{PHY}} = \ell_{\text{HDR}} + 8i \quad \text{Eq. (17)}$$

where  $i = 1, 2 \dots 2312$ .

10 In Eq.(17),  $\ell_{\text{HDR}}$  specifies the known size of the header in a MAC data packet. Then, using Eqs. (12), (13), and (17),  $\mathbf{u}^{\text{MAC}}_n$  is limited to 2313 combinations, which are inserted in the set of candidate values  $\Omega^{\text{MAC}}_{u,n}$ .

Standard, robust, and CRC-robust MAC-Lite decoders are compared in Figure 11. Three types of MAC-Lite header recovery methods are considered in T1.  
 15 A standard decoder performs hard decisions on the data provide by the PHY layer. A robust decoder exploits only the intra-layer redundancy through a soft decoding algorithm, neglecting the information provided by the CRC. Finally, a CRC robust decoder combines the intra-layer redundancy together with the information provided by the CRC through the header recovery method based on Eq. (20). EHR is lower  
 20 than  $10^{-5}$  for SNRs larger than 3 dB when exploiting the CRC redundancy whereas the two other methods need at least 14 dB.

The respective robust decoder modules described above at PHY layer and MAC-Lite layer can be used independently one from the other or in combination. In a combined embodiment, both decoder modules 32 and 33 of Figure 5 execute a  
 25 corresponding header recovery method, which increases the number of payloads reaching the next protocol layers. In a numerical simulation, the combination of the proposed permeable PHY and MAC-Lite layer mechanisms recovered eventually all PHY and MAC headers from 3 dB SNR onwards.

In the above described embodiments of PHY and MAC layer decoder  
 30 modules, the allocation of the respective header fields to the categories of the model defined as known vector  $\mathbf{k}$ , predictable vector  $\mathbf{p}$  and unknown vector  $\mathbf{u}$  are purely illustrative and not limitative. Those allocations are based on the assumption that

some elements of the communication context are fixed a priori. In other use cases, there may be more or less context information known a priori. As a consequence, in other embodiments, some of the fields presented as known or predictable will be treated as unknown instead. Although the computational complexity of the header recovery process may increase if more fields are treated as unknown, an adequate use of intralayer or interlayer redundancy will allow keeping this complexity to an acceptable level in many instances.

Second embodiment of the MAP estimator

In other protocols, the CRC covers not only the control data at layer L but also some or all of the service data, e.g. the payload in a regular 802.11 MAC layer. In the generic model, this situation implies that the vector  $\mathbf{o}$  is not empty. Embodiments of a MAP estimator suitable for recovering the control data of a PDU, e.g. a MAC packet header in such a case will now be described.

When  $\mathbf{o}$  is not empty, we assume that the bits of  $\mathbf{o}$  are i.i.d. and do not depend on the other parameters. The second term of Eq. (6) then becomes

$$\Psi(\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{y}_o, \mathbf{y}_c) = \sum_{\mathbf{o}} P(\mathbf{o}) P(\mathbf{y}_o | \mathbf{o}) P(\mathbf{y}_c | \mathcal{F}([\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{o}])) \quad \text{Eq. (9)}$$

In Eq. (9),  $P(\mathbf{o})$  defines the a priori probability of vector  $\mathbf{o}$ ,  $P(\mathbf{y}_o | \mathbf{o}) \approx N(\mathbf{o}, \sigma^2 \mathbf{I}_{\ell_o})$  and  $P(\mathbf{y}_c | \mathbf{c}) \approx N(\mathbf{c}, \sigma^2 \mathbf{I}_{\ell_c})$ , where  $N(.,.)$  denotes a Gaussian law with the first argument being the mean value and the second argument being the variance.

Evaluating Eq. (9) requires the computation of the sum of probabilities related to the  $2^{\ell_o}$  combinations of vector  $\mathbf{o}$  and to their corresponding CRCs. This evaluation has obviously a complexity exponential in  $\ell_o$ . In this section, two methods are proposed for reducing the complexity of Eq. (9). The first one involves an exact computation and the second one provides an approximate solution.

Exact Sum Computation

This method involves a trellis construction consisting in grouping combinations of values taken by  $\mathbf{o}$  resulting to the same value of the CRC. The trellis is composed of a collection of states interconnected by segments. The states are grouped into  $\ell_o + 1$  sets indexed by the parameter  $j=0, 1 \dots \ell_o$  representing the depth in the trellis and corresponding to the bits of  $\mathbf{o}$ . At any depth  $j$ ,  $2^{\ell_c}$  states exist, i.e. the  $2^{\ell_c}$  values of the CRC. Segments connect states between depth  $j$  and  $j+1$ , for

$j=0, 1 \dots \ell_0-1$ . At depth  $j$ , each state indexed by  $i=0, 1 \dots 2^{**}\ell_c-1$  takes a value  $S_i(j)$ . The index  $i$  specifies the numerical value of the CRC  $c_i$ . Hence, the vector  $c_i$  contains the binary representation of  $i$ .

The steps for evaluating Eq. (9) are detailed below. The trellis is built starting from depth 0 and going forwards to depth  $\ell_0$ .

Step 1 - At depth  $j=0$ ,  $S_i(0)=0$  for all  $i$ , except for  $i=u$  where  $S_u(0)=1$ . The only possible state at depth 0 is specified by  $\mathbf{c}_u=F([\mathbf{k}, \mathbf{p}, \mathbf{u}, 0])$ , which represents the value of the CRC when vectors  $\mathbf{k}$ ,  $\mathbf{p}$ , and  $\mathbf{u}$  are known.

Step 2 - For  $j=1, 2 \dots \ell_0$ , each state is connected to two previous states depending on the  $j$ -th entry of vector  $\mathbf{o}$ :

$$S_i(j) = P(o_j = 0)P(y_{o_j}|o_j = 0)S_i(j - 1) + P(o_j = 1)P(y_{o_j}|o_j = 1)S_q(j - 1)$$

where  $i=0, 1 \dots 2^{**}\ell_c-1$  and  $q$  is the integer represented by  $\mathbf{c}_q = \mathbf{c}_i \oplus \pi(j)$ .

Step 3 - After  $\ell_0$  iterations, one gets for any  $i=0, 1 \dots 2^{**}\ell_c-1$ :

$$S_i(\ell_0) = \sum_{\mathbf{o}|F([\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{o}])=\mathbf{c}_i} P(\mathbf{o})P(\mathbf{y}_o|\mathbf{o})$$

Multiplying  $S_i(\ell_0)$  by  $P(\mathbf{y}_c|\mathbf{c}_i)$ , one obtains:

$$L_i = P(\mathbf{y}_c|\mathbf{c}_i) \sum_{\mathbf{o}|F([\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{o}])=\mathbf{c}_i} P(\mathbf{o})P(\mathbf{y}_o|\mathbf{o})$$

where  $L_i$  depends on all the values of  $\mathbf{o}$  giving the same CRC  $\mathbf{c}_i$ .

Step 4 - Summing up the values of  $L_i$ , for  $i=0, 1 \dots 2^{**}\ell_c-1$ , one gets finally:

$$\sum_{i=0}^{2^{\ell_c}-1} L_i = \Psi(\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{y}_o, \mathbf{y}_c)$$

Example 1: Figure 12 represents the trellis, which may be obtained for a systematic binary Hamming (7, 4) code with:

$$\mathbf{\Pi} = \begin{bmatrix} 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 1 & 1 \end{bmatrix}$$

In the example, it is assumed that  $[\mathbf{k}, \mathbf{p}, \mathbf{u}] = [1]$  and that  $\ell_0 = 3$ .

For a given value of  $\mathbf{k}$ ,  $\mathbf{p}$ , and  $\mathbf{u}$ , this technique allows a complexity reduction for the evaluation of Eq. (9) from  $O(2^{**\ell_0})$  to  $O(\ell_0 2^{**\ell_c})$ . Nevertheless, the initialization at Step 1 depends on the value taken by  $F([\mathbf{k}, \mathbf{p}, \mathbf{u}])$ . For all values of  $\mathbf{u}$  in  $\Omega_u$ , a new trellis has to be constructed. The global complexity is thus  $O(|\Omega_u| \ell_0 2^{**\ell_c})$ , where  $|\Omega_u|$  denotes the cardinal number of  $\Omega_u$ .

In the following, a method evaluating simultaneously Eq. (9) for all the initial states at depth 0 (the  $2^{**\ell_c}$  possible values of the CRC) is proposed. This method consists in constructing the trellis by starting from depth  $\ell_0$  and going backwards to depth 0. The steps for the sum evaluation are presented below.

Step 1 - At depth  $j = \ell_0$ ,  $S_i(\ell_0) = P(y_c | c_i)$  for any  $i = 0, 1 \dots 2^{**\ell_c} - 1$ .

Step 2 - For  $j = \ell_0 - 1 \dots 1, 0$ ,

$$S_i(j) = P(o_{j+1} = 0)P(y_{o_{j+1}} | o_{j+1} = 0)S_i(j+1) \\ + P(o_{j+1} = 1)P(y_{o_{j+1}} | o_{j+1} = 1)S_q(j+1)$$

for all  $i, q$  denoting the numerical value of  $\mathbf{c}_q = \mathbf{c}_i \oplus \pi(j+1)$ . At depth  $j$ , the state  $i$  is thus connected to the states  $i$  and  $q$  at depth  $j+1$ .

Step 3 - After  $\ell_0$  iterations, for  $i = u$ , one gets:

$$S_u(0) = \sum_{\mathbf{o}} P(\mathbf{o})P(\mathbf{y}_o | \mathbf{o})P(\mathbf{y}_c | \mathbf{c}_u \oplus \mathcal{F}(\mathbf{0}, \mathbf{0}, \mathbf{0}, \mathbf{o})) \\ = \Psi(\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{y}_o, \mathbf{y}_c),$$

with  $\mathbf{c}_u = F([\mathbf{k}, \mathbf{p}, \mathbf{u}, 0])$ .

Example 2: Using the same code as in Example 1, one gets the trellis (constructed backwards) represented in Figure 13. This method allows to compute directly the sum in Eq. (9) for all values taken by  $\mathbf{u}$  with a complexity  $O(\ell_0 2^{**\ell_c})$ .

#### Approximate Sum Computation

Most CRCs are larger than 16 bits. In some instances, the complexity  $O(\ell_0 2^{**\ell_c})$  may be too large to allow a real-time implementation of the method presented above. An approximate computation consists in splitting the CRC into  $n_b$  blocks of  $\ell_b = \ell_c / n_b$  bits. For example a CRC with length  $\ell_c = 32$  can be split into  $n_b = 4$  blocks with length  $\ell_b = 8$ . In what follows, each of these blocks is assumed to be statistically independent from the others. Using this decomposition, one may write

$\mathbf{y}_c$  as:

$$y_c = [y_{c1}, y_{c2} \dots y_{c_{n_b}}]$$

Using the independence assumption, the sum in Eq. (9) becomes:

$$\Psi(\mathbf{k}, \mathbf{p}, \mathbf{u}, y_o, y_c) = \prod_{m=1}^{n_b} \Psi_m(\mathbf{k}, \mathbf{p}, \mathbf{u}, y_o, y_{c_m}) \tag{Eq. (10)}$$

with

$$\Psi_m(\mathbf{k}, \mathbf{p}, \mathbf{u}, y_o, y_{c_m}) = \sum_{\mathbf{o}} P(\mathbf{o})P(y_o|\mathbf{o})P(y_{c_m}|\mathcal{F}_m([\mathbf{k}, \mathbf{p}, \mathbf{u}, \mathbf{o}]))) \tag{Eq. (11)}$$

where  $F_m$  is the encoding function associated to the columns  $(m-1) \cdot \ell_b + 1$  to  $m \cdot \ell_b$  of  $\mathbf{P}$ , evaluating a partial CRC of  $\ell_b$  bits. The evaluation of Eq. (11) is similar to that of  $\Psi$  described in the previous section. The only difference lies in the size of trellis:  $2^{**\ell_b}$  states have to be considered at any depth (instead of  $2^{**\ell_c}$  states without splitting the CRC). The total computational complexity for evaluating Eq. (10) is now  $O(n_b \ell_o 2^{**(\ell_c/n_b)})$ .

The second embodiment of the MAP estimator makes it possible to recover transmission errors in selected portions of the information covered by the error detection code, i.e. fields of interest identified as  $\mathbf{u}$ ,  $\mathbf{k}$  and  $\mathbf{p}$ , while disregarding potential errors affecting other portions of the information covered by the error detection code, i.e. secondary fields identified as  $\mathbf{o}$ . A permeable protocol layer decoder can thus be implemented in which a MAP estimator of this type serves to recover errors in the control data only. An embodiment of this type can be used for a standard 802.11 MAC layer. The decoder module 10 shown on Figure 1 can be easily adapted to implement this embodiment. The functions of the control data correction module 6 are modified to allow determining the marginal likelihoods of the respective candidate values of the header.

MAC Header Recovery

Combining the properties of a 802.11 MAC layer in Eq. (7), one obtains the following MAC metric:

$$\hat{\mathbf{u}}_n^{\text{MAC}} = \arg \max_{\mathbf{u}_n^{\text{MAC}} \in \Omega_{\mathbf{u},n}^{\text{MAC}}} P(y_{\mathbf{u},n}^{\text{MAC}} | \mathbf{u}_n^{\text{MAC}}) \cdot \Psi(\mathbf{k}_n^{\text{MAC}}, \mathbf{p}_n^{\text{MAC}}, \mathbf{u}_n^{\text{MAC}}, y_{\mathbf{o},n}^{\text{MAC}}, y_{\mathbf{c},n}^{\text{MAC}}) \tag{Eq. (18)}$$

where the second term represents the marginal likelihoods of the respective candidate values of the header and may be computed with methods presented in the second embodiment of the MAP estimator.

A robust MAC decoder executing a header recovery method based on  
5 Eq. (18) has been numerically simulated under conditions similar to the PHY layer simulations. Figure 14 compares the coding gains obtained by the standard, robust, and CRC-robust MAC decoders. Two payload sizes (50 and 100 bytes) have been considered. Moreover, the Approximate Sum Computation method presented above has been used, dividing the CRC in four blocks of 1 byte each. The shape of the  
10 curves is very similar to the results obtained at PHY layer, but with significantly less coding gains. Gains due to MAC CRC information improve with increasing SNR. With payloads of 100 bytes, EHR lower than  $10^{-5}$  are achieved for SNRs of 11 dB, 14 dB, and 15 dB when using CRC-robust, robust, and standard MAC decoders respectively. The MAC header recovery processing is more complex than the one  
15 done at PHY layer due to the marginalization operation required in Eq. (9). The larger the payload, the more complex the decoding process. A difference between the robust MAC decoder and the CRC-robust MAC decoder lies in the taking into consideration of last term of Eq. (9), i.e. the correlation of CRC values. Figure 14 allows appreciating the contribution of this feature in the efficiency of the CRC-robust  
20 MAC decoder.

Figure 17 is a representation similar to Fig. 9, which illustrates an embodiment of a decoding method adapted to a 802.11 protocol stack. Fig. 17 combines a step of PHY header recovery 85 based on evaluating Eq. (16), which may be executed by the PHY layer decoder module 32, and a step of MAC header  
25 recovery 86 based on evaluating Eq. (18), which may be executed by the MAC layer decoder module 33 at the receiver. With this combined embodiment, a higher number of MAC payloads may be passed to the upper layer, as shown at reference number 87 e.g. in the form of soft decision data.

The above described second embodiment of the MAP estimator can be  
30 applied to permeable protocol layer decoders as well as any other application in which a selective recovery of transmission errors is desired.

In an embodiment, the second embodiment of the MAP estimator serves as a basis for an error correction method applied to error resilient video transmission

using a scalable video codec. A scalable video codec is defined as a codec capable of producing a bitstream that can be divided into embedded subsets. These subsets can be independently decoded to provide video sequences of increasing quality. Thus, a single compression operation can produce bitstreams with different rates and  
5 corresponding reconstructed quality levels. Scalable video codecs are supported by most of the video compression standards such as MPEG-2, MPEG-4 and H. 263.

Video data partitioning may be used to facilitate scalability. For example in MPEG-2, the slice layer indicates the maximum number of block transform coefficients contained in the particular bitstream, known as the priority break point.  
10 Data partitioning is a frequency-domain technique that breaks the block of 64 quantized transform coefficients into two bitstreams. The first, higher priority bitstream, e.g. base layer, contains the more critical lower frequency coefficients and side information, e.g. zero-frequency values and motion vectors. The second, lower priority bitstream, e.g. enhancement layers, carries higher frequency video data.

15 In an embodiment illustrated on Figure 15, a subset of the original bitstream can be transmitted as primary data to provide a base layer quality with one or more extra layers transmitted as secondary data to provide enhancement layers. Thus, the error correction method allows applying unequal error protection, e.g. stronger error protection to the base layer than to the enhancement layers while transporting  
20 multiple layers in the same packet. As a result, the base layer can be successfully decoded with higher probability even during adverse transmission channel conditions.

Fig. 15 is a diagrammatic representation of a transmission system 80 for transmitting video, i.e. a sequence of pictures. The system 80 comprises a transmitter  
25 device 70, which transmits video data to a receiver device 71 on a communication channel 72, e.g. a wireless channel. The video is encoded with a scalable video codec. In the transmitter device 70, a coder module 73 encodes the sequence of pictures with a scalable video codec, e.g. MPEG-4, to produce a first bitstream corresponding to a base layer and at least one second bitstream corresponding to an  
30 enhancement layer and places successive segments of the resulting bitstreams into packets 74 to be transmitted. Thus the coder module 73 produces a sequence of data packets 74. In the illustrated embodiment, a packet 74 comprises a primary field 75 for data belonging to the first bitstream, a secondary field 76 for data

belonging to the second bitstream and an error detection field 77 for an error detection code (e.g. CRC) associated to the primary and the secondary fields. The sequence of the data packets 74 is transmitted through the communication channel 72 using any suitable transmission protocol or protocol stack and any suitable encapsulation techniques. Hence in the transmitter device 70, the coder module 73  
5 may be connected to further coder modules corresponding to lower protocol layers, which Fig. 5 does not show.

In the receiver device 70, a decoder module 90 receives the successive packets 74, e.g. from further decoder modules corresponding to lower protocol  
10 layers, not shown. The decoder module 90 checks the CRC received in the field 77 against the corresponding data contained in the packet. When the CRC check is unsuccessful, the decoder module 90 performs a selective error recovery method to recover transmission errors affecting the primary field 75, i.e. the base layer.

Figure 16 is a diagrammatic representation of an embodiment of the  
15 decoder module 90. The decoder module 90 comprises an input module 91 for receiving the data packets 74 and an error detection code checking module 92 for checking the error detection code of the received packet. If the check is successful, the module 92 removes the field 77 and passes the video data of the fields 75 and 76 to an output module 93. The output module 93 outputs the video data to a  
20 suitable video decoder, e.g. MPEG-4, which decodes the first and second bitstreams to reconstruct the sequence of pictures.

If the check is unsuccessful, the error detection code checking module 92 passes the packet 74 to a selective error correction module 94 which operates to find the most likely correct value of the data belonging to the first bitstream, i.e. to recover  
25 the primary field 75. The selective error correction module 94 determines a finite set of candidate values for the primary field 75, e.g. an exhaustive list of  $2^{*L}$  binary values where L is the length of the field 75 or a more limited set if a priori known information allows excluding some of those binary values. For each candidate values of the set, the selective error correction module 94 determines a marginal likelihood  
30 of the candidate value as a function of the data received in the fields 76 and 77 of the received packet 74, e.g. by evaluating Eq. (9) or Eq. (10). The selective error correction module 94 also determines a correlation, e.g. Euclidian distance, between the data received in the primary field 75 of the received packet 74 and the candidate

value. Then it uses the respective marginal likelihoods and correlation values to select a candidate value for the primary field, which best matches the observation. Then the selective error correction module 94 passes the corrected value of the primary field 75 to the output module 93, so that at least the base layer can be  
5 decoded. The received value of the secondary field 76 may be passed to the output module 93 as well, although transmission errors affecting it, if any, will not be corrected by the above described process.

In a modified embodiment of the transmission system 80, the packet 74 further comprises a header field 78, which is also covered by the error detection code  
10 in field 77. For instance such a packet 74 can be used at an UDP or UDP-Lite protocol layer. Then the selective error correction module 94 can be adapted to recover transmission errors found in the header field 78 as well as the primary field 75. Assuming that those fields are independent, candidate values to be considered in that case include any combination of a candidate value for the control data of the header  
15 field 78 with a candidate value for the video data of the primary field 75.

In further embodiments, the packet 74 may comprise multiple primary fields and or multiple secondary fields. For the decoder module 90 to handle the primary and secondary data as such, a corresponding map of the packet format must be available to the decoder module 90. This can be made available to the decoder  
20 module in any suitable manner, e.g. fixedly configured or communicated through another channel. Preferably, the primary field or fields are located closer to the packet head than the secondary field or fields, but this is not mandatory.

The described methods and devices for recovering transmission errors can be applied to other transmission protocols layers than those mentioned above, e.g.  
25 DVB, Wimax as well as non-standard protocol layers. The methods and devices for recovering for recovering transmission errors can be applied at a single protocol layer or at several protocol layers of a protocol stack.

The described methods and devices for recovering transmission errors can be applied to the transmission of any type of data. Applications to the transmission  
30 of compressed multimedia contents are contemplated in particular, since the payload of a PDU at any underlying layer may comprise a large amount of application data, i.e. multimedia contents information. Applications to the transmission of information over wireless channels are contemplated in particular, since those are more prone to

transmission errors. Applications to broadcast transmissions, e.g. satellite television or to transmissions with strong delay constraints, e.g. visiophony are contemplated in particular, since those make it difficult to rely on retransmissions to recover erroneous packets.

5           The described methods and devices for recovering transmission errors can be applied to permeable protocol layer decoders, especially for one or more lower transmission protocol layers of a protocol stack in which joint source-channel decoding techniques allow many errors to be corrected at the application layer. By increasing the amount of packets reaching the application layer, e.g. in the form of  
10 soft information, such permeable protocol layer decoders allow improving the performance of joint source-channel decoding at the application layer.

The functions of the various elements shown in the Figs., including any functional blocks referred to as "modules", may be provided through the use of dedicated hardware as well as hardware capable of executing software in association  
15 with appropriate software. When provided by a processor, the functions may be provided by a single dedicated processor, by a single shared processor, or by a plurality of individual processors, some of which may be shared. Moreover, explicit use of the term "processor" or "controller" should not be construed to refer exclusively to hardware capable of executing software, and may implicitly include, without  
20 limitation, digital signal processor (DSP) hardware, network processor, application specific integrated circuit (ASIC), field programmable gate array (FPGA), read-only memory (ROM) for storing software, random access memory (RAM), and non-volatile storage. Other hardware, conventional and/or custom, may also be included.

The invention is not limited to the described embodiments. The appended  
25 claims are to be construed as embodying all modification and alternative constructions that may be occurred to one skilled in the art, which fairly fall within the basic teaching here, set forth.

In the claims hereof any element expressed as a means for performing a specified function is intended to encompass any way of performing that function  
30 including, for example, a) a combination of circuit elements which performs that function or b) software in any form, including, therefore, firmware, microcode or the like, combined with appropriate circuitry for executing that software to perform the function. The invention as defined by such claims resides in the fact that the

functionalities provided by the various recited means are combined and brought together in the manner which the claims call for. Applicant thus regards any means which can provide those functionalities as equivalent as those shown herein.

5 The use of the verb "to comprise" or "to include" and its conjugations does not exclude the presence of elements or steps other than those stated in a claim. Furthermore, the use of the article "a" or "an" preceding an element or step does not exclude the presence of a plurality of such elements or steps. The invention may be implemented by means of hardware as well as software. The same item of hardware may represent several «means».

10 In the claims, any reference signs placed between parentheses shall not be construed as limiting the scope of the claims.

**CLAIMS**

1. A method for recovering transmission errors, comprising:  
receiving a data packet (50, 74) comprising an error detection code (51, 77)  
associated to data contained in the packet, wherein the data associated to the error  
5 detection code comprises primary data (52, 75) and secondary data (53, 76),  
checking the error detection code of the received packet to detect an erroneous state  
of the associated data,  
when the erroneous state is detected, (86) determining a finite set of candidate values  
for the primary data and, for each candidate values of the set:  
10 determining a marginal likelihood of the candidate value over the secondary data as  
a function of the error detection code of the received packet,  
determining a first correlation between the primary data of the received packet and  
the candidate value, and  
selecting a corrected value for the primary data among the set of candidate values as  
15 a function of said marginal likelihoods and said first correlations.

2. A method in accordance with claim 1, wherein the step of determining  
a marginal likelihood of the candidate value comprises:  
determining a finite set of potential values for the secondary data,  
sorting the potential values of the secondary data into subsets associated to respective  
20 error detection code values, wherein all the potential values of the secondary data  
within a subset are selected to yield the associated error detection code value when  
combined with the candidate value of the primary data,  
for each subset, determining a marginal likelihood of the secondary data belonging to  
the subset as a function of the secondary data of the received packet and the error  
25 detection code of the received packet,  
determining said marginal likelihood of the candidate value by cumulating said  
marginal likelihoods for all said subsets.

3. A method in accordance with claim 2, wherein the step of determining  
the marginal likelihood of the secondary data belonging to a subset comprises:  
30 determining a second correlation between the error detection code value associated to  
the subset and the error detection code of the received packet,  
determining third correlations between the potential values of the secondary data  
within the subset and the secondary data of the received packet, and

determining the marginal likelihood of the secondary data belonging to the subset as a function of said second correlation and said third correlations.

4. A method in accordance with claim 1, wherein the step of determining a marginal likelihood of the candidate value comprises:

5 splitting the error detection code of the received packet into a plurality of blocks, for each block of the error detection code of the received packet, determining a partial marginal likelihood of the candidate value as a function of said block, and determining said marginal likelihood of the candidate value as a function of the partial marginal likelihood associated to all blocks of the error detection code.

10 5. A method in accordance with claim 4, wherein the step of determining a partial marginal likelihood of the candidate value comprises:

determining a finite set of potential values for the secondary data, sorting the potential values of the secondary data into subsets associated to respective values of the block of the error detection code, wherein all the potential values of the secondary data within a subset are selected to yield the associated block value when  
15 combined with the candidate value of the primary data, for each subset, determining a partial marginal likelihood of the secondary data belonging to the subset as a function of the secondary data of the received packet and the block of the error detection code of the received packet,  
20 determining said partial marginal likelihood of the candidate value by cumulating said partial marginal likelihoods for all said subsets.

6. A method in accordance with claim 5, wherein the step of determining the partial marginal likelihood of the secondary data belonging to a subset comprises: determining a second correlation between the block value associated to the subset  
25 and the block of the error detection code of the received packet, and determining third correlations between the potential values of the secondary data within the subset and the secondary data of the received packet, determining the partial marginal likelihood of the secondary data belonging to the subset as a function of said second correlation and said third correlations.

30 7. A method in accordance with any one of claims 2 to 6, wherein the step of sorting the potential values of the secondary data into subsets associated to respective error detection code values or respective block values comprises constructing a trellis representation of the potential values of the secondary data.

8. A method in accordance with claim 7, wherein the trellis representation is constructed from a lower depth value to a higher depth value, wherein the depth of the trellis represents a number of bits of secondary data already determined.

5 9. A method in accordance with claim 7, wherein the trellis representation is constructed from a higher depth value to a lower depth value, wherein the depth of the trellis represents a number of bits of secondary data already determined.

10 10. A method in accordance with any one of claims 2 to 9, wherein the finite set of potential values for the secondary data comprises all possible combinations of binary values for each and every bit of data of a field of the packet reserved for secondary data.

11. A method in accordance with any one of claims 1 to 10, wherein the packet comprises a protocol data unit (50), wherein the primary data includes control data (52) of the protocol data unit and the secondary data includes service data (53) of the protocol data unit.

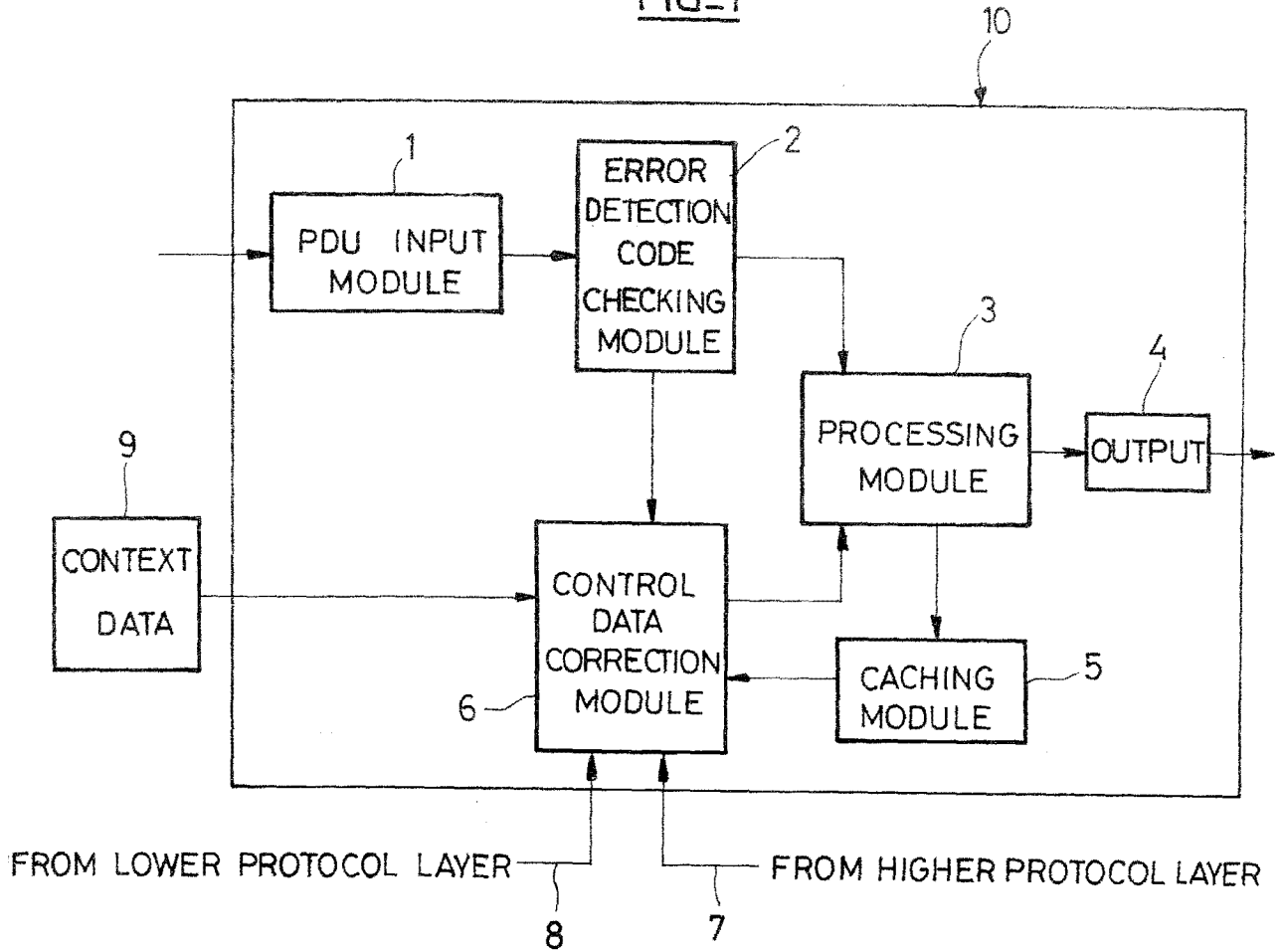
15 12. A method in accordance with claim 11, further comprising the step of processing the service data (53) of the received protocol data unit as a function of the corrected value of the control data.

20 13. A method in accordance with any one of claims 1 to 12, wherein the packet (74) comprises video data coded with a scalable video codec, wherein the primary data (75) includes video data corresponding to a base layer and the secondary data (76) includes video data corresponding to an enhancement layer.

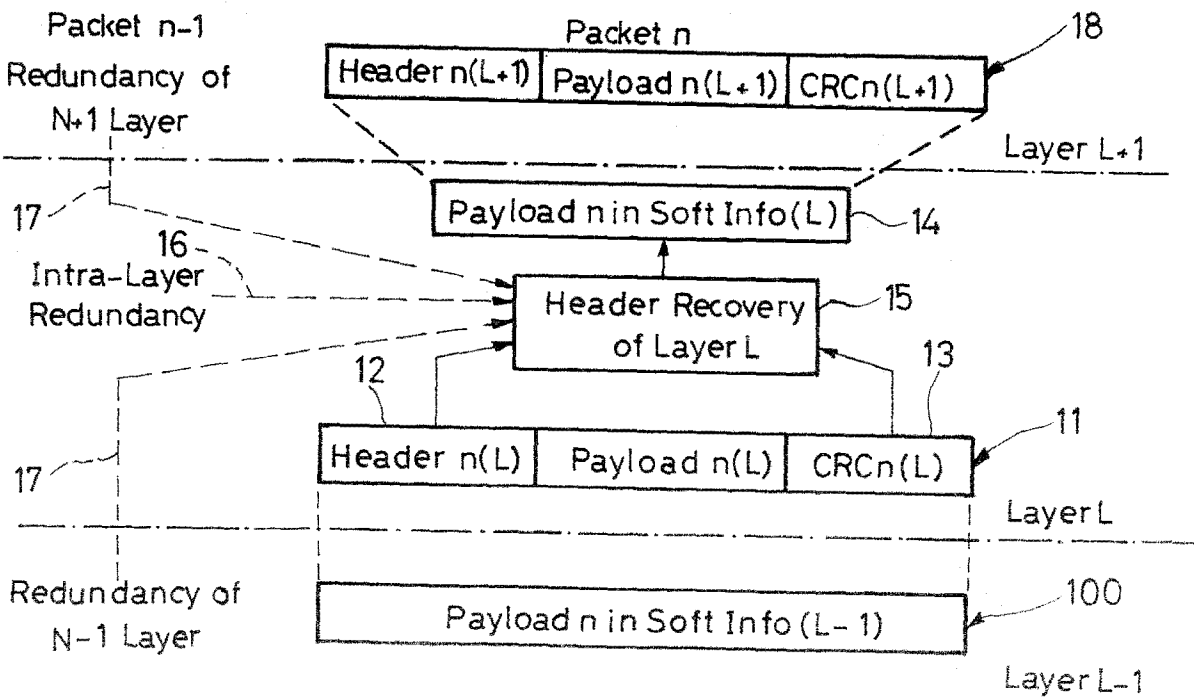
25 14. A device for recovering transmission errors (90) comprising:  
an input means (91) for receiving a data packet (74) comprising an error detection code (77) associated to data (75, 76, 78) contained in the packet, wherein the data associated to the error detection code comprises primary data (75) and secondary data (76),  
an error detection code checking means (92) for checking the error detection code of the received packet to detect an erroneous state of the associated data,  
a primary data correction means (94) operable to determine a finite set of candidate  
30 values for the primary data and, for each candidate values of the set, to determine a marginal likelihood of the candidate value over the secondary data as a function of the error detection code of the received packet, to determine a first correlation between the primary data of the received packet and the candidate value, and to select a corrected value for the primary data among the set of candidate values as a  
35 function of said marginal likelihoods and said first correlations.

15. A permeable protocol layer decoder comprising the device in accordance with claim 14 and a processing module (93), wherein the packet comprises a protocol data unit (50), wherein the primary data includes control data (52) of the protocol data unit and the secondary data includes service data (53) of the protocol data unit, wherein the processing module is operable to process said service data as a function of said control data.
- 5

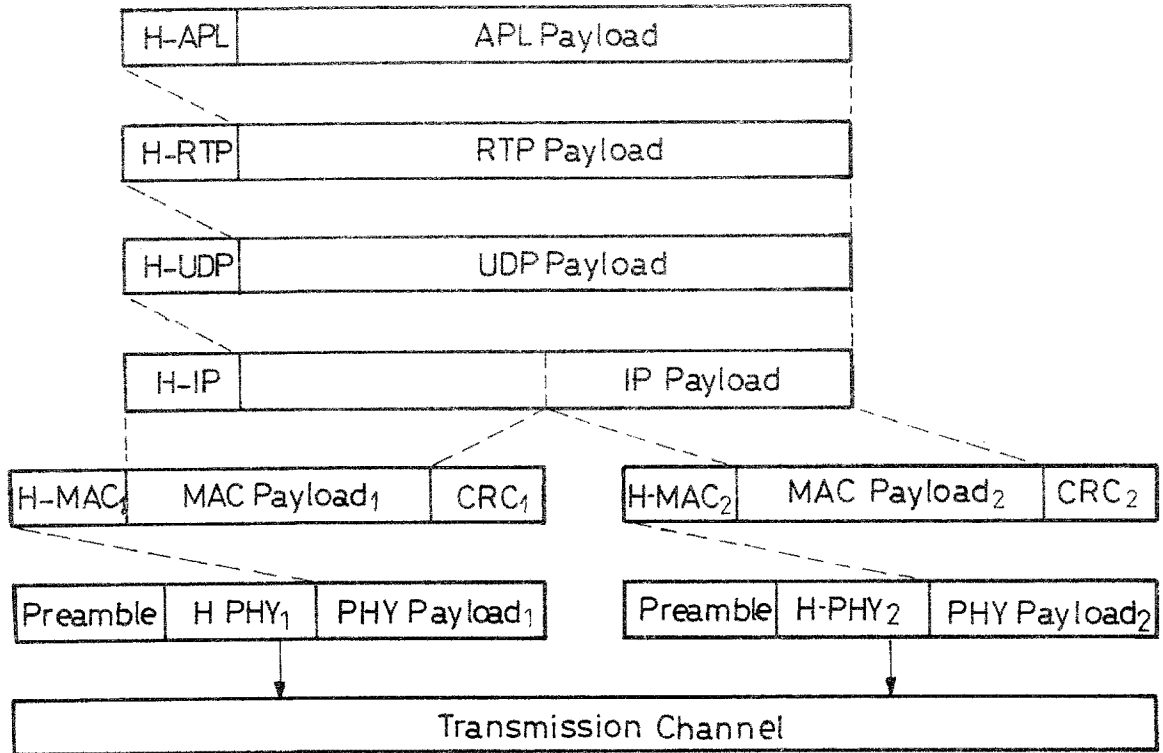
FIG\_1



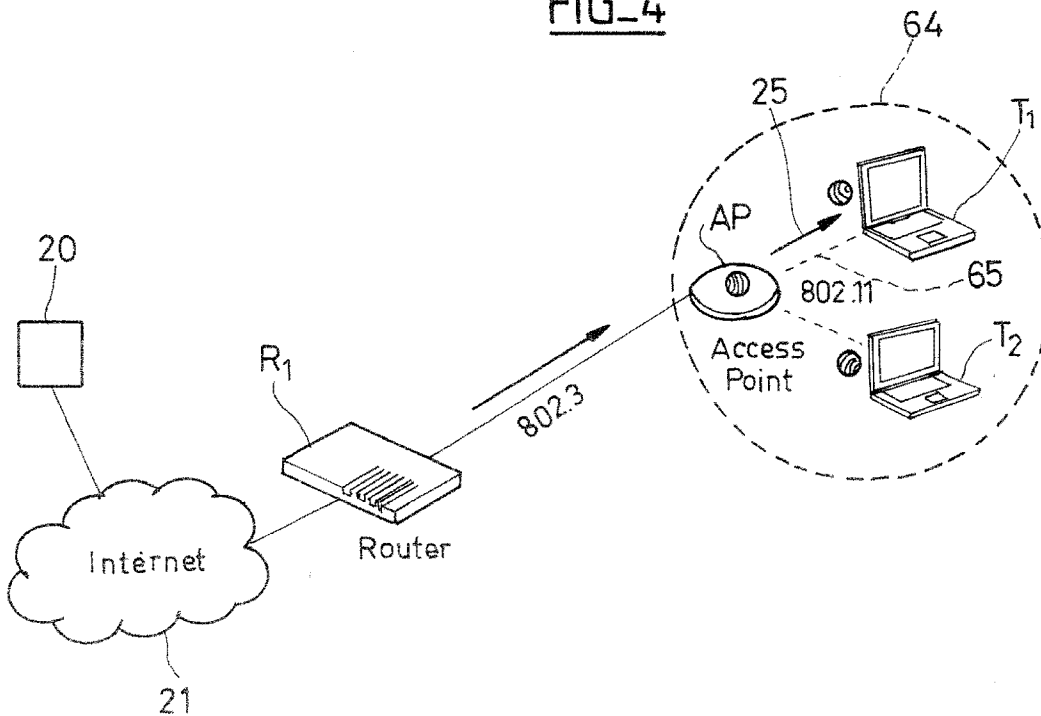
FIG\_2

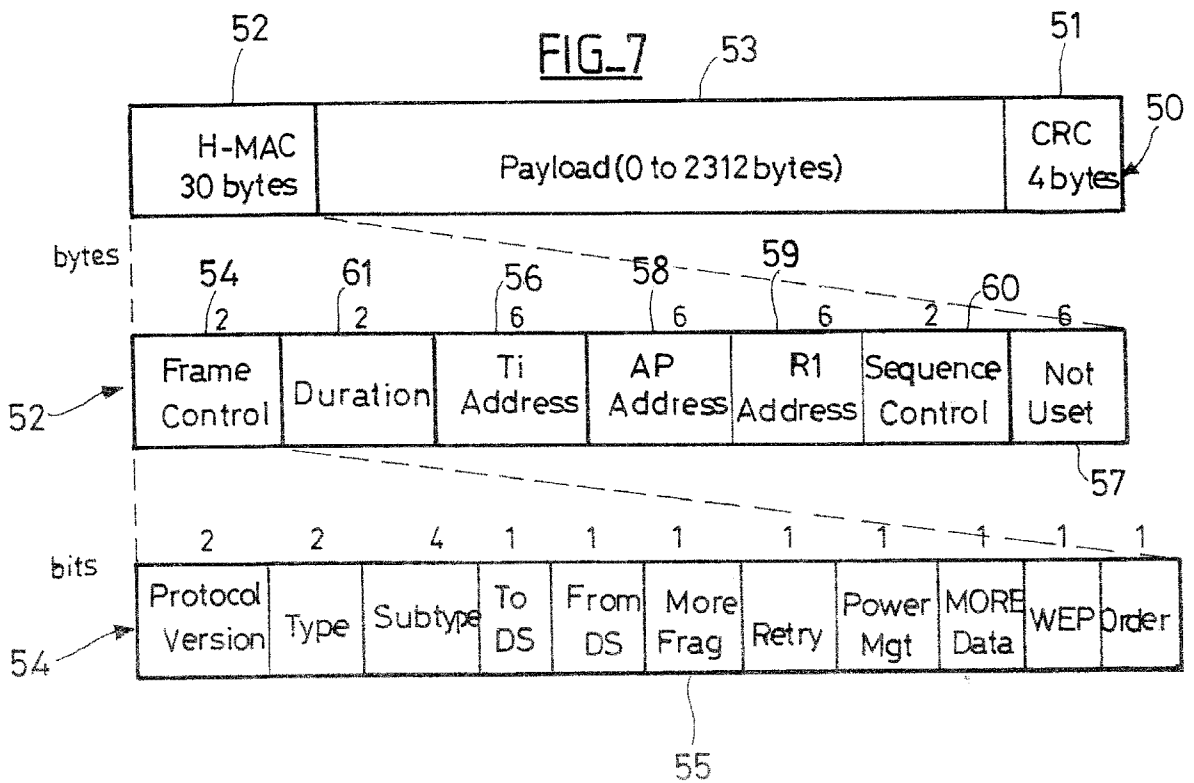
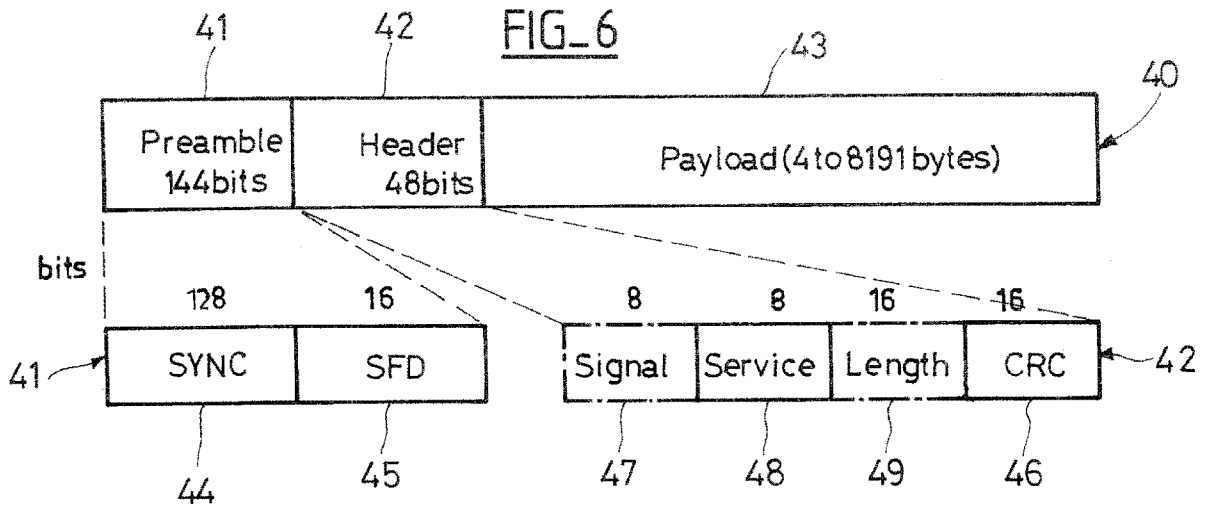
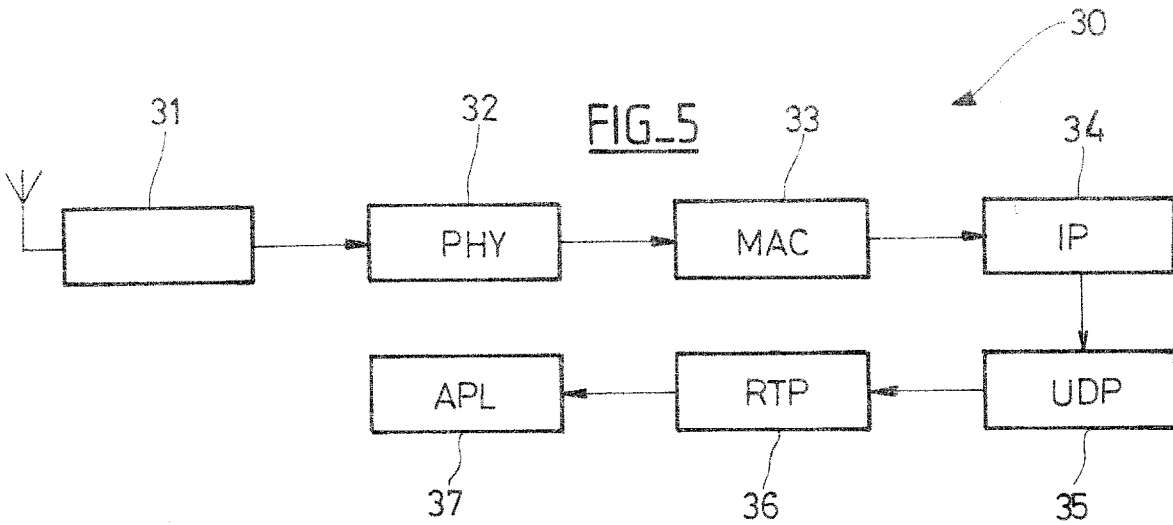


FIG\_3

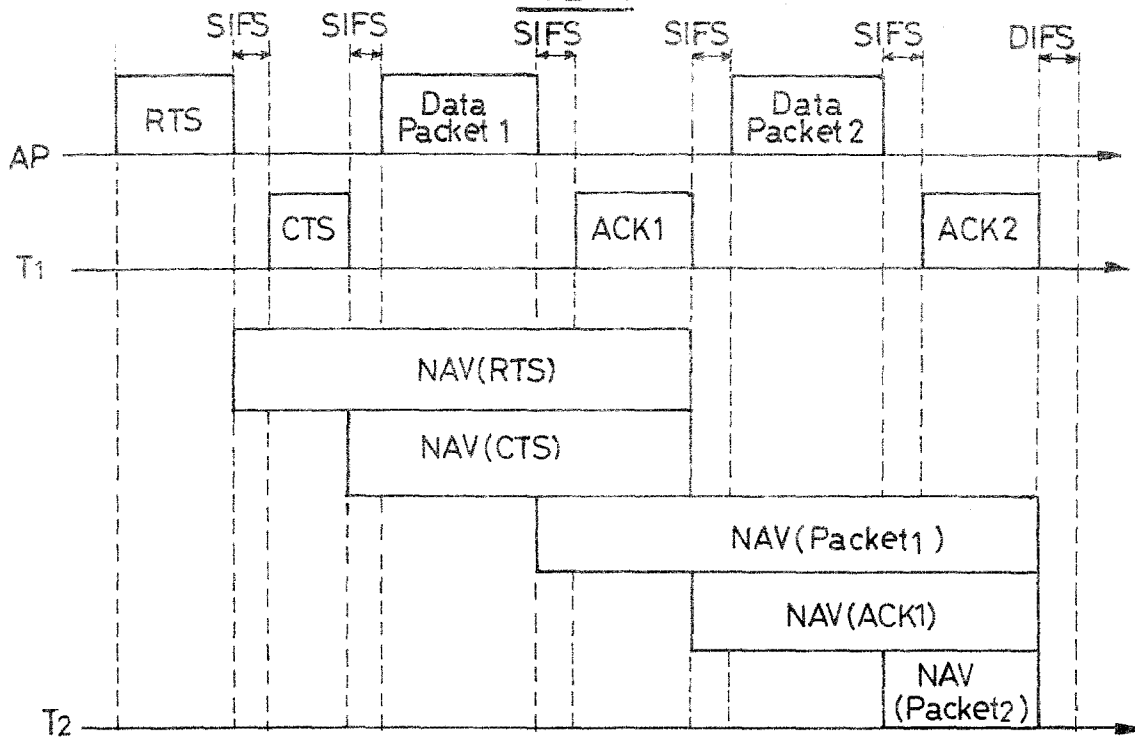


FIG\_4

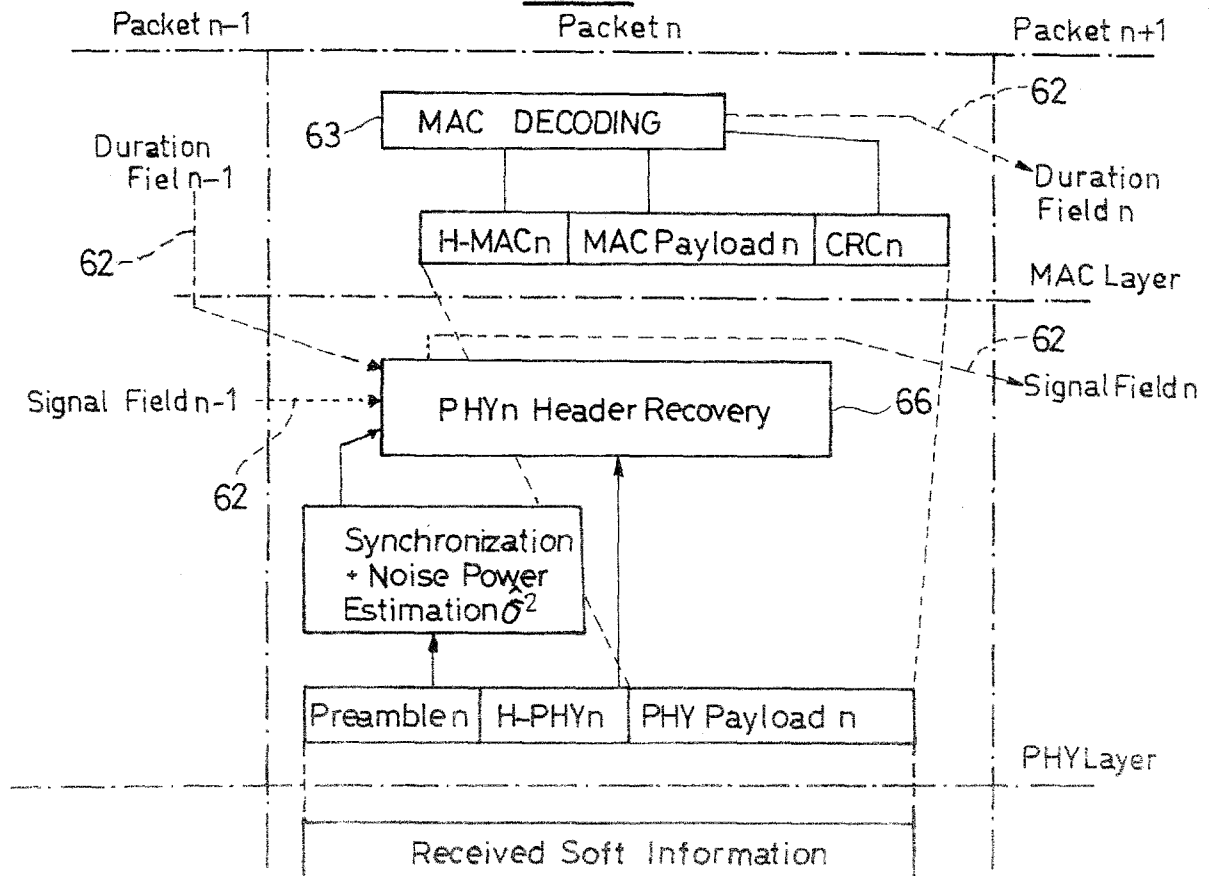




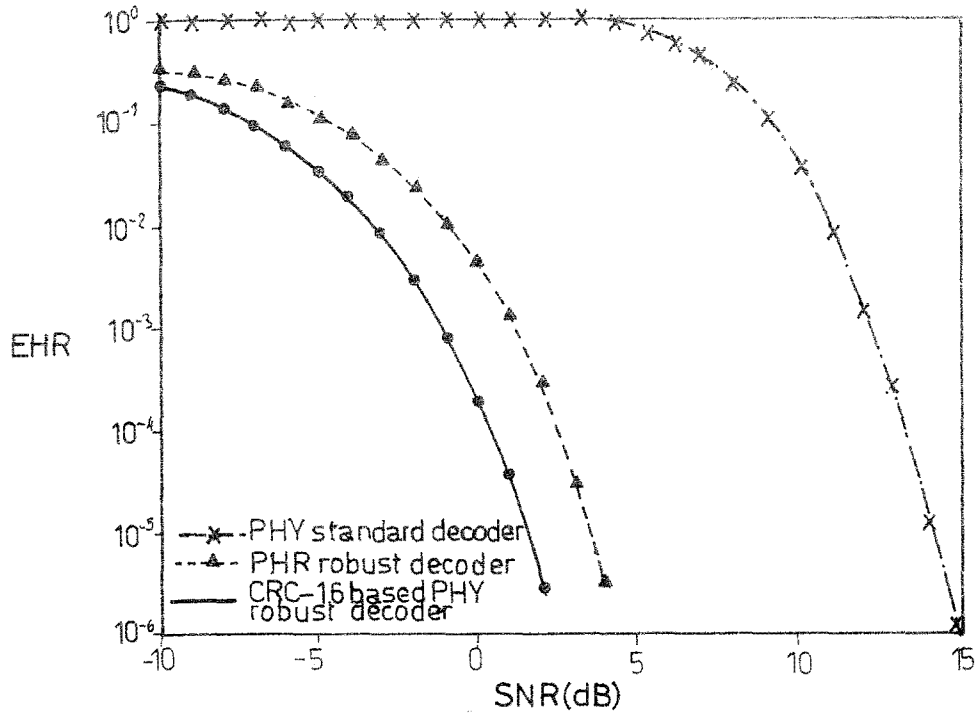
FIG\_8



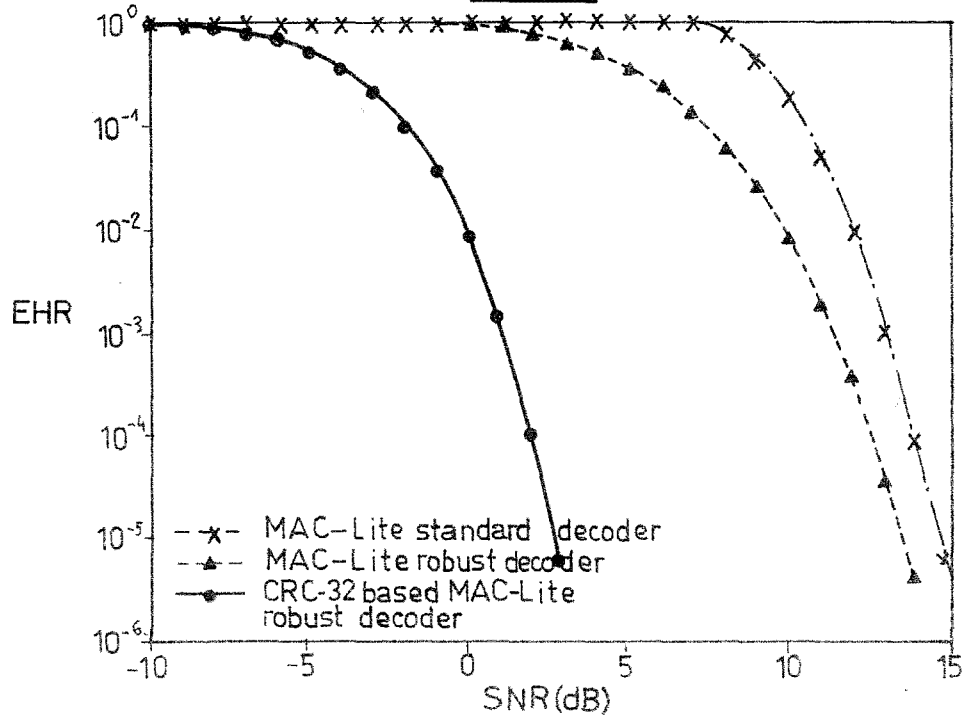
FIG\_9



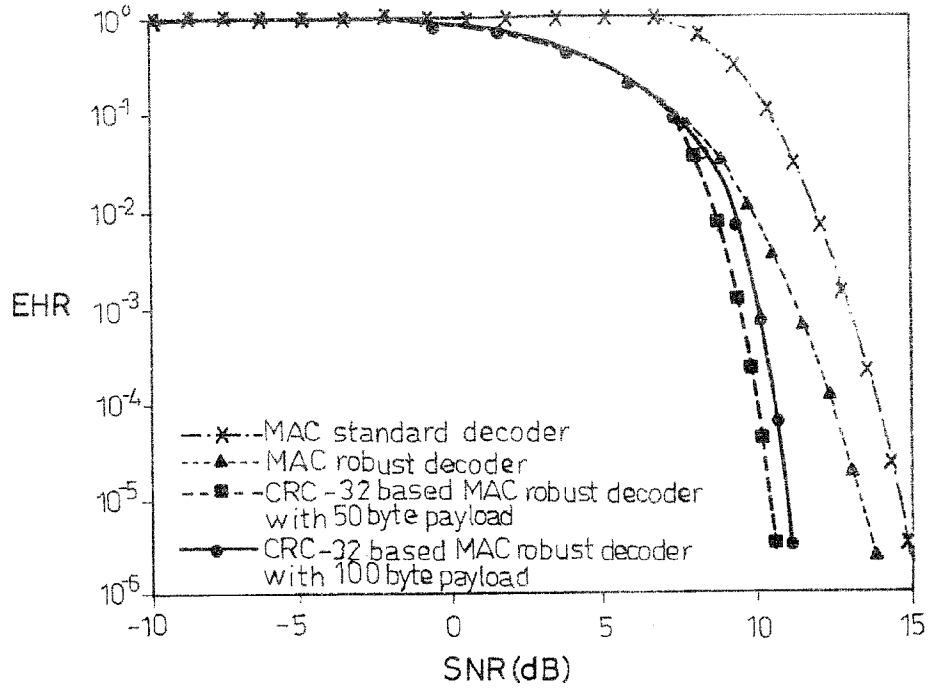
FIG\_10



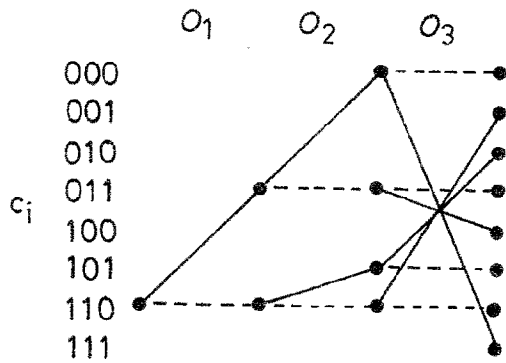
FIG\_11



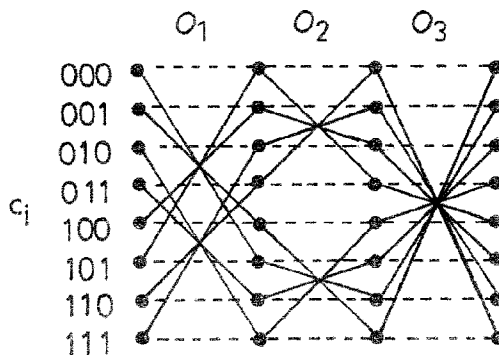
FIG\_14



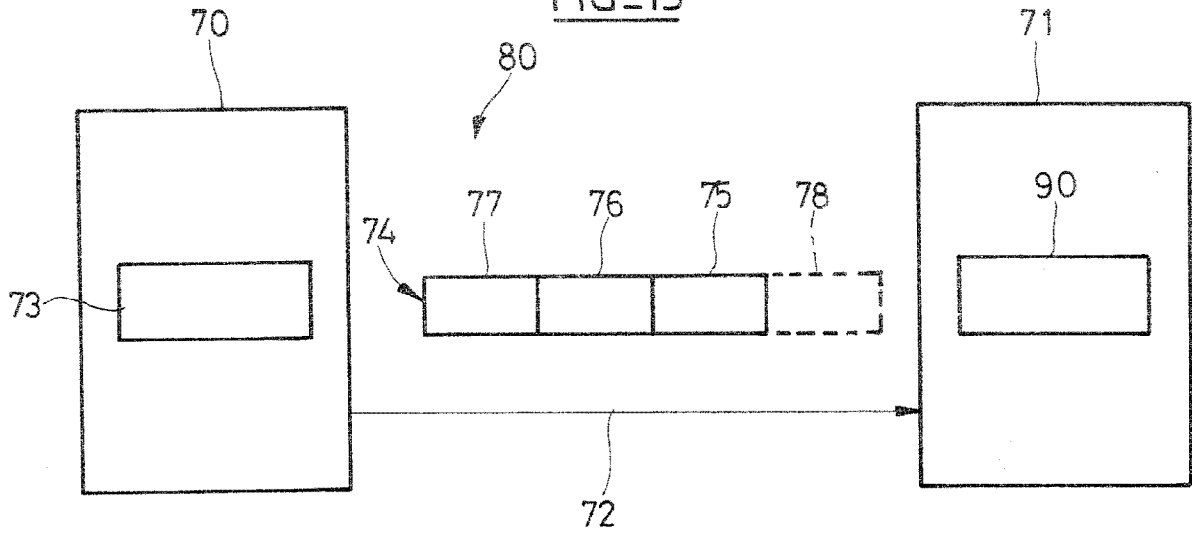
FIG\_12



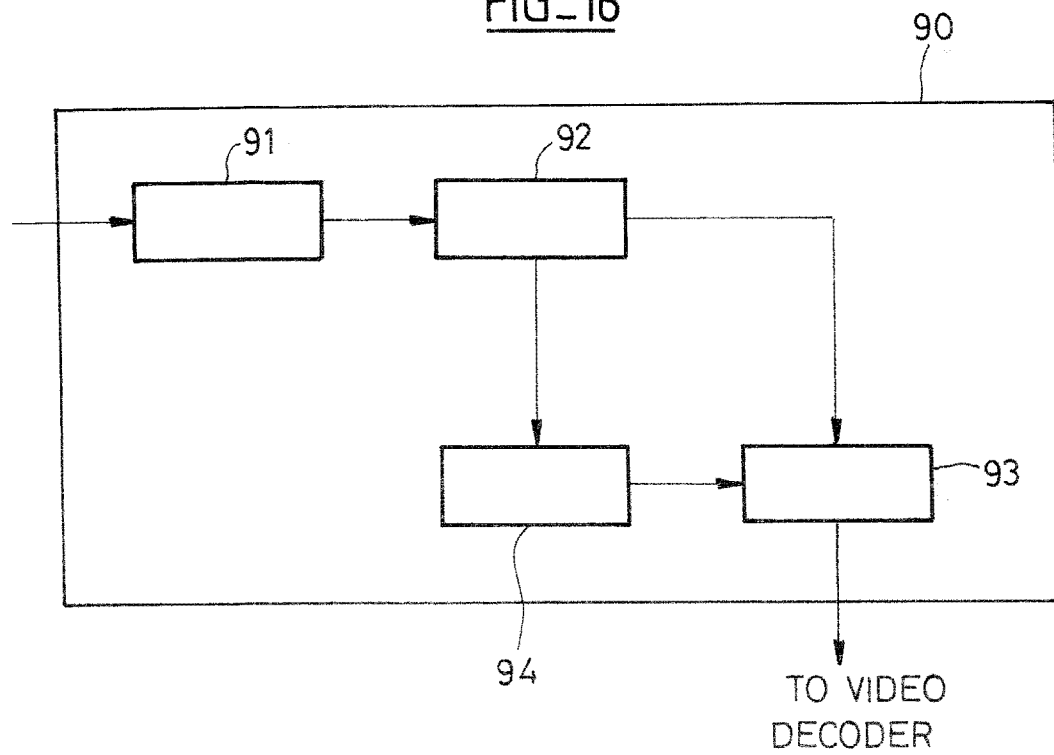
FIG\_13



FIG\_15



FIG\_16





# INTERNATIONAL SEARCH REPORT

International application No  
PCT/EP2009/055476

<b>A. CLASSIFICATION OF SUBJECT MATTER</b> INV. H04L1/00				
According to International Patent Classification (IPC) or to both national classification and IPC				
<b>B. FIELDS SEARCHED</b>				
Minimum documentation searched (classification system followed by classification symbols) H04L				
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched				
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal				
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>				
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
Y	WO 2005/096553 A (SONY ERICSSON MOBILE COMM AB [SE]; BLOEBAUM L SCOTT [US]) 13 October 2005 (2005-10-13) abstract; claims 1-3,5,6,14-16,25; figures 1a,1b,2-6 page 3, line 11 - line 33 page 5, line 14 - line 30 page 9, line 13 - line 29 page 10, line 11 - line 31 page 11, line 20 - page 12, line 28 page 13, line 8 - line 31	1-15		
Y	US 2004/005012 A1 (SUZUKI MITSUHIRO [JP] ET AL) 8 January 2004 (2004-01-08) abstract; claims 25,39,40,46,60,61; figures 13,15-17 paragraphs [0137], [0138], [0203] - [0207]	1-15		
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<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C.				
<input checked="" type="checkbox"/> See patent family annex.				
* Special categories of cited documents :				
<table style="width: 100%; border: none;"> <tr> <td style="width: 50%; border: none; vertical-align: top;">                     *A* document defining the general state of the art which is not considered to be of particular relevance                      *E* earlier document but published on or after the international filing date                      *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)                      *O* document referring to an oral disclosure, use, exhibition or other means                      *P* document published prior to the international filing date but later than the priority date claimed                 </td> <td style="width: 50%; border: none; vertical-align: top;">                     *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention                      *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone                      *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.                      *Z* document member of the same patent family                 </td> </tr> </table>			*A* document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *Z* document member of the same patent family
*A* document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *Z* document member of the same patent family			
Date of the actual completion of the international search <p style="text-align: center; font-weight: bold;">3 August 2009</p>		Date of mailing of the international search report <p style="text-align: center; font-weight: bold;">10/08/2009</p>		
Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016		Authorized officer <p style="text-align: center; font-weight: bold;">Wolters, Robert</p>		

## INTERNATIONAL SEARCH REPORT

International application No  
PCT/EP2009/055476

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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A	WO 02/51087 A (ERICSSON INC [US]; KHAYRALLAH ALI S [US]; BOTTOMLEY GREGORY EDWARD [US]) 27 June 2002 (2002-06-27) abstract; claims 1-4,6,7,12,14-17,19-22,30-33,35,36; figures 6-8,10-12 claims 38-40,46,47,49,50 page 3, line 12 - page 4, line 19 page 7, line 14 - page 8, line 17 page 12, line 3 - line 16 page 12, line 32 - page 13, line 31 page 18, line 1 - page 19, line 5 page 19, line 22 - line 34	1-15

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Information on patent family members

International application No

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