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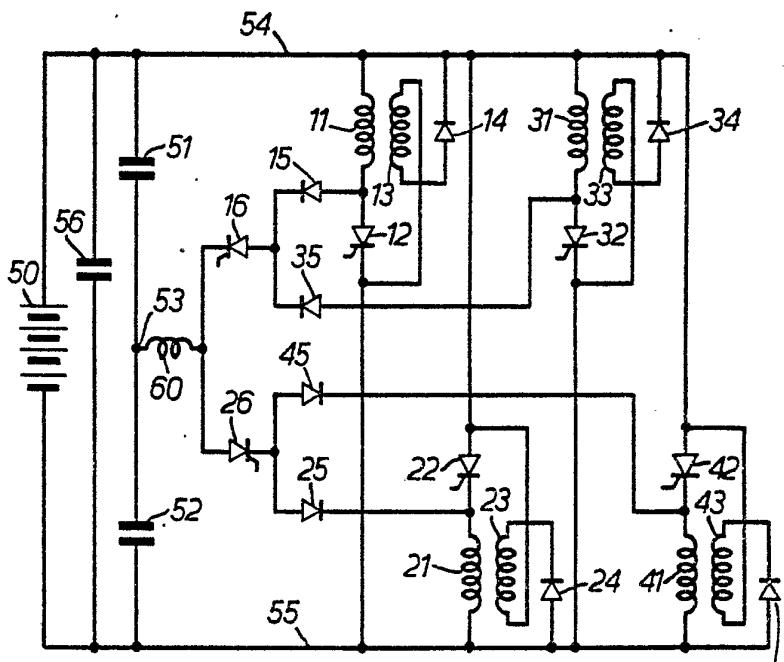
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## (54) Title: VARIABLE RELUCTANCE MOTOR SYSTEMS

## (57) Abstract

A variable reluctance electric motor system includes a variable reluctance motor having a main winding for each phase and a power converter circuit including a main thyristor in series with each main winding connected across a direct voltage source from which the main windings are supplied sequentially with uni-directional current pulses, characterised by a commutating thyristor (16) for starving the main thyristor (12) of the current flowing through one main winding (11) while charging a capacitor unit (51, 52) in one sense, and a commutating thyristor (26) for starving the main thyristor (22) of the next phase winding (21) of the current flowing through the next main winding (21) while charging the capacitor unit in the other sense. The arrangement is such that commutation of a main thyristor initiated by firing a commutating thyristor and terminated when this commutating thyristor ceases to conduct, leaves the commutating capacitor unit charged with the correct polarity and adequate voltage for the commutation of the next main thyristor in the conducting sequence.



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**VARIABLE RELUCTANCE MOTOR  
SYSTEMS**

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This invention relates to variable reluctance motor systems and is concerned with circuits employing thyristors to connect a direct voltage (d.v.) source sequentially to the windings of a variable reluctance

5. motor.

According to the present invention a variable reluctance electric motor system includes a variable reluctance motor having a main winding for each of an even number of phases greater than two, and a power converter circuit including a main thyristor in series with each main winding of the motor so connected across one or more d.v. sources, from which the main windings are supplied sequentially with unidirectional current pulses, that the current in each main

10. winding is independent of that in the other main windings, characterised in that of two successive phases (taken in their order of conduction) one has its phase winding connected to the positive terminal of a d.v. source and has a commutating thyristor

15. which connects the junction of the main thyristor and main winding to a junction point of a capacity unit so as to starve the main thyristor of the current flowing from the positive source terminal through the winding, while charging the capacitor terminal

20. positively, whereafter the commutating thyristor of the next phase, having its phase winding connected to the negative terminal of a d.v. source, connects the junction of its main thyristor and main winding to a junction point of the said capacitor unit so as to

25. starve the main thyristor of the current flowing

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through the main winding to the negative source terminal while charging the capacitor terminal negatively, whereby a commutation of a main thyristor, initiated by firing a commutating thyristor

5. and terminated when this commutating thyristor ceases to conduct, leaves the commutating capacitor or capacitors charged with the correct polarity and adequate



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voltage for the commutation of the next main thyristor in the conducting sequence.

Known commutating circuits, having performed their turn-off function for a particular thyristor, leave the

5. commutating capacitor or capacitors charged with a polarity which is opposite to that required to turn-off the next thyristor in the sequence of thyristor conduction. An additional thyristor in series with an inductor is necessary forming a resetting circuit
10. through which a half cycle of current flows whereby the polarity of the capacitors is reversed.

The present invention is concerned not only with the arrangement of the thyristors through which current flows from the d.v. source to the motor

15. windings whereby the completion of one commutation leaves the commutating capacitor(s) with the correct polarity for the commutation of the next thyristor in the conducting sequence, but also with the requirement that the magnitude of the voltage left on the
20. capacitor at the completion of one commutation is adequate for the commutation of the next thyristor in the conducting sequence.

The invention may conveniently be applied to variable reluctance motors having a main winding and a

25. closely coupled auxiliary winding for each phase, or



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to such motors having a single winding for each phase in which case one end of every winding is connected together and connected to a mid-tapping on the d.v. source.

5. When the motor has main and auxiliary windings the phase current referred to hereafter is defined as the sum of the main winding current and  $m$  times the auxiliary winding current where  $m$  is the ratio of the auxiliary to main winding turns.
10. The present invention has the advantage that it avoids the need for a resetting circuit formed by an additional thyristor and inductor in series, and so eliminates the power losses associated with this circuit. The invention also halves the frequency of
15. fundamental voltage across the commutating capacitor compared to the frequency necessary when a resetting circuit is used. The frequency reduction reduces capacitor losses. A third advantage of the invention is to reduce the voltage excursions across the commutating capacitor compared to those which occur when a resetting circuit is used, thereby reducing the voltage ratings of the commutating thyristors and the diodes in series with the auxiliary windings.
20. In one form of the invention the commutating

25. circuit common to two successive phases has a



.5.

junction point connected to the junction of the main thyristor and main winding of a first phase through a commutating thyristor arranged when fired to divert current flowing through the winding to flow into the commutating circuit to turn off the main thyristor while making the junction points of the commutating circuit more positive, in preparation for commutating the main thyristor of the second phase, while the junction of the main winding and main thyristor of the second phase is connected to a junction point of the commutating circuit through a commutating thyristor arranged when fired to divert current flowing through the winding to flow out of the commutating circuit to turn off the main thyristor while making the junction points of the commutating circuit more negative, in preparation for commutating the main thyristor of the next phase.

In one arrangement both phases are connected to the same junction point of the commutating circuit. Thus the commutating circuit may comprise two capacitors connected in series between the terminals of a source common to both phases. It may also be desirable to include a boosting circuit comprising a pair of boosting thyristors connected in series across the source, and an inductor connected between the junction point of the commutating circuit and the junction of the boosting



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thyristors.

In another arrangement the source is centre tapped and the main winding of each phase is the only motor winding, and has one end connected to the centre tapping, with the remote terminal of main thyristors of alternate phases connected to each terminal of the source and the junction of the main winding and main thyristor connected through a regenerating diode to the opposite terminal of the source.

Successive phases may be connected to junction points of the commutating circuit between which a substantially constant potential difference is maintained. Thus the commutating circuit may comprise a pair of capacitors connected in series between the terminals of the combined source with a device maintaining a substantially constant potential difference interposed between them. The device maintaining a constant potential difference may comprise a battery, or it may comprise a relatively large capacitor connected to each terminal of the source through an inductor, or connected to an isolating DC - DC converter from the source.

Further features and details of the invention will be apparent from the following description of certain specific embodiments, given by way of example, with reference to the accompanying drawings in which:

Figure 1 is a circuit diagram of one form of



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variable reluctance motor system embodying the invention;

Figure 2 is a diagram of a slight modification of part of Figure 1;

Figure 3, divided into Figures 3A, 3B and 3C, is

5. a graph showing waveforms; and

Figures 4 to 6 are circuit diagrams respectively of further modifications.

The arrangements of Figures 1, 2, 4 and 5, when operating in the low-speed chopping mode, also incorporate the invention set forth in our co-pending

10. British patent application No. 22892/78 (Case EPS 282).

The arrangement shown in Figure 1 is particularly suitable for low voltage motors in which space is available for bifilar windings to provide a main

15. winding and an auxiliary winding for each phase of the motor. The system incorporates a four phase motor having a main winding 11, 21, 31 or 41 for each phase connected in series with a main thyristor 12, 22, 32 or 42 across the supply terminals of a battery

20. 50. Each main winding is closely coupled to an auxiliary winding 13, 23, 33 or 43 connected across the supply in series with a diode 14, 24, 34, or 44. The junction of each main winding with its main thyristor is connected through one of four diodes

25. 15, 25, 35 or 45 to one of two commutating thyristors



.8.

16 or 26 to the junction point 53 of a commutating circuit comprising a pair of capacitors 51 and 52 connected across the supply terminals. The supply terminals are shown connected also to a capacitor 5. 56 which reduces the alternating component of current in the battery. The three capacitors 56, 51, 52 of Figure 1 may be replaced by three capacitors 64, 65 and 66 of Figure 2. It is also necessary to include one or more inductors to protect thyristor 16, 26

10. against excessive  $di/dt$  at turn-on and a single inductor 60 is shown to fulfil this function.

Each phase is arranged in a generally conventional manner and the operation of each phase is also generally conventional, that is to say the main

15. thyristor 12 of phase 1 is fired to allow a current to build up in the main winding 11.

In order to terminate the current in the main winding, the main thyristor is commutated by firing the commutating thyristor 16, the point 53 being at 20. a negative potential, so that the current in the winding 11 is diverted by the thyristor 16 through the inductor 60 and the capacitors 51 and 52, allowing the thyristor 12 to switch off. The voltage at the junction 53 of the capacitors becomes progressively more positive until 25. the current transfers from the main winding 11 to the



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auxiliary winding 13 so as to feed energy back into the supply. This gives the thyristor 16 the opportunity to turn off.

At this point in the operation the arrangement

5. differs from the conventional arrangement. It will be noted that the voltage at the junction 53 of the capacitors is now positive with respect to the point 54 so that the commutating circuit is incapable of repeating the commutation of a phase arranged like the first phase. In conventional
10. arrangements, a separate resetting circuit is provided comprising a resetting thyristor in series with an inductor connected between the point 53 and a convenient point in the circuit having a fixed potential, for example a mid-point on the battery 50. When the reset-
15. ting thyristor is fired, a pulse of current flows through it to effect resonant reversal of the voltage across the capacitor.

In accordance with the present invention, it

- will be noted that whereas the main winding 11 of the
20. first phase is connected to the positive supply terminal 54, and its thyristor to the negative supply terminal 55, the main winding 21 of the second phase is connected to the negative supply terminal 55, whilst its thyristor 22 is connected to the positive terminal 54. Accordingly,
25. the second phase requires the junction 53 of the



with respect to the point 5  
capacitors to be at a positive voltage/in order to  
commutate it, and this is the state in which capacitors  
are left after the commutation of the first phase  
has been completed. In other respects, the operation

5. of the second phase follows that of the first phase  
with the polarities reversed, whilst the third phase  
follows with the same polarities as the first and  
the fourth phase follows with the same polarities as  
the second.

10. The operation may be considered in somewhat  
more detail and since the circuit is completely  
symmetrical it is appropriate to consider the  
positive supply terminal voltage as being + E/2 and  
the negative supply terminal voltage as being minus  
15. E/2, zero voltage corresponding to a fictitious mid-  
tapping of the supply.

The effective capacitor voltage VC is taken to  
be the voltage at the point 53 with respect to zero  
voltage.

20. Prior to the commutation of the current from  
the main winding 11 to the auxiliary winding 13 the  
capacitor voltage VC has been set to  $VC = -[3E/2 + \Delta V_1]$ .

The commutation occurs as follows:-

On firing the commutating thyristor 16 the main

25. thyristor 12 is reverse biased and current  $I_{11}$  flows through

.11.

the commutating thyristor into the capacitors so that VC increases positively. The reverse voltage time for the main thyristor 12 is sufficiently long for it to turn off. When VC equals  $3E/2$  the diode 14 becomes

5. forward biased due to the transformer action of the closely coupled main and auxiliary windings having unity turns ratio and the main winding current  $I_{M1}$  transfers to the auxiliary winding 13. The transfer is not instantaneous due to imperfect coupling between
10. the windings and resultant leakage inductance so that for a short period of time, a decreasing current continues to flow through the commutating thyristor 16 so that VC is further increased to  $3E/2 + \Delta V_1$

It will be noted that in the conventional

15. arrangement each time the commutating circuit is used the commutating capacitor must undergo two reversals of charge. On the other hand, according to the present invention it only undergoes a single reversal of charge, which leaves it in the required state to
20. commutate the next phase. Thus at the end of commutation, the capacitor point 53 is left at  $VC=+3E/2 + \Delta V_1$ .

Figures 3A and 3B show typical waveforms of phase current and inductance when running at appreciable speed indicating the magnitude and angular duration

25. of phase current. The motor operates over the majority

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of its speed range in a single commutation mode which involves only one commutation for each phase during each cycle, and the fact that the resonant reversal of the commutating capacitor voltage by a resetting

5. circuit is not required throughout this range means that a source of energy loss is eliminated and gives greater efficiency. In addition the thyristor and inductor required for a capacitor resetting circuit are eliminated.

10. In the high speed range, with the above single commutation mode, the rate at which the winding current  $i$  builds up is controlled by the inductance  $L$  of the winding and by its rate of increase  $dL/dt$  according to the equation.

15. 
$$\frac{di}{dt} = \frac{1}{L} [E - Ri - i \frac{dL}{dt}]$$

where  $R$  is the winding resistance and  $E$  is the battery voltage.

As the motor speed reduces,  $dL/dt$  reduces and the time for a cycle increases, both effects resulting

20. in the winding current reaching an increasingly high value for particular choices of the rotor angles at which the main thyristor is turned on and commutated.

Accordingly in a low speed range from standstill upwards, a chopping mode is adopted and the main

thyristor is commutated each time the phase current rises to a predetermined higher value and fired again each time the phase current sinks to a predetermined lower value, so that a number of 5. commutations are performed on a particular phase before any are performed on the following phase. Figure 3C shows a diagrammatic graph of the sort of shape of such phase currents.

Accordingly, it will be appreciated, that for 10. operating with the chopping mode several consecutive commutations are required for each phase and hence for all but the last of these it is necessary to reset the commutating capacitor voltage by resonant reversal. It is not however necessary to provide a separate 15. resonant reversal thyristor and inductor for this purpose as the result may be achieved by the method claimed in the present applicant's British patent Specification No 22892/78 (Case EPS 282).

Thus after the main thyristor 12 has been 20. commutated and the current  $I_{11}$  has transferred as described above from the main winding 11 to the auxiliary winding 13 of phase 1, the main and commutating thyristors 12 and 16 have turned off. When the phase current has fallen to a lower predetermined level 25. the main thyristor 12 is fired again thereby transferring



the phase current back to the main winding. At or approximately at the same time that the thyristor 12 is refired, the commutating thyristor 26 of the second and fourth phases is also fired. The main windings of

5. phases two and four are now effectively in parallel and act in the same way as the resonant reversal inductor of a conventional arrangement. Thus the voltage of the commutating capacitor is now resonantly reversed from  $VC = +3E/2 + V_1$  to approximately  $VC = - [3E/2]$ ,  $VC$  being limited

10. to  $-3E/2$  by the clamping action of the auxiliary windings 23 and 43 whose diodes 24 and 44 become forward biased when  $VC \leq - 3E/2$ .

The use of adjacent phase windings for resonant reversal of the commutation capacitors gives rise to current

15. pulses in these windings as shown at 71 in Figure 3C but these do not interfere with the operation of these phases of which the main thyristors 22 and 42 are not at this time conducting.

Additional current pulses 74 arise in each phase caused

20. by the commutations 72 of the other thyristor connected to the same source terminal.

The next commutation of thyristor 12 cannot take place until the previous resonant reversal of  $VC$  has been completed and the commutating thyristor 26 has had sufficient time to turn

25. off. During this period the current  $I_{11}$  in the main winding will be increasing. A pertinent question is whether the resonant reversal will be sufficiently fast, using the motor windings

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themselves in lieu of a resonant reversal inductor, so that when the current  $I_{11}$  has built up to the level at which commutation is required the thyristor 26 is off and the thyristor 16 may be fired.

5. It will be seen from Figures 3B and 3C that during the chopping of current in phase 1, phase 2 has its minimum inductance value, and, since the time for resonant reversal is proportional to  $\sqrt{L}$ , this is most beneficial for a fast reversal.

10. It may be shown that for a typical reluctance motor application even using thyristors with relatively slow turn-off times (compared with known conventional inverter grade thyristors), current waveforms of the type shown in Figure 3 C may be achieved.

15. It will also be seen from Figures 3B and 3C that when the inductance of a phase 1 has built up to a value approaching its maximum due to rotor rotation it is desirable to terminate the current in phase 1 and to build up current in phase 2.

20. Hence at or approximately at the same time that the commutating thyristor 16 is fired for the final commutation of the main thyristor 12 in phase 1, the main thyristor 22 is fired to build up current in phase 2. However the second phase requires the junction 53 of the capacitors to be at a positive voltage in order

to commutate thyristor 22, and this is the state in which the capacitors are left after this commutation of thyristor 12. Hence when the main thyristor 22 is initially fired to build up current in phase 2

5. no resetting of the voltage  $VC$  is required, resetting only being required for the second and subsequent commutations in any given phase. In other respects the operation of the second phase follows that of the first phase with the polarities reversed, whilst
10. the third phase follows with the same polarities as the first and the fourth phase follows with the same polarities as the second.

Priming of the power converter circuit of Figure 1 when first connected to the source is also

15. effected, as will now be described without the need for additional components.

When the circuit is first connected to the source, the voltage  $VC$  on the commutating capacitors at point 53 will become approximately zero, the positive and negative

20. terminal voltages of the source being  $+E/2$  and  $-E/2$  respectively. Depending on which main thyristor is to be first fired, it is necessary to preset  $VC$  to approximately  $-3E/2$  or  $+3E/2$ .

$VC$  may be set to  $-3E/2$  by initially firing

25. the commutating thyristor 16, when current will flow

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through main windings 11 and 31 to resonantly charge VC to approximately +E. After the thyristor 16 has turned off, the commutating thyristor 26 is fired, when current flows through main windings 21 and 41

5. to resonantly reverse VC, VC being eventually held at approximately  $-3E/2$  by the clamping action of the auxiliary windings 23 and 43 and diodes 24 and 44 as previously described. The commutating capacitor voltage is now preset to such a value that it may
10. be used to turn off the main thyristors 12 or 32 when either of these is fired.

VC may similarly be preset to  $+3E/2$  by firing the commutating thyristor 26 followed by firing the commutating thyristor 16.

The arrangement shown in Figure 1 involves slight additional capacitor cost as compared with a conventional arrangement but this is greatly outweighed by the advantages of having no resonant

20. inductor and thyristor, reduced voltage ratings for commutating thyristors and main diodes, and a greater circuit efficiency.

In a modification of Figure 1 the diodes 15, 25, 35 and 45 and thyristors 16 and 26 are replaced by

25. four thyristors, in the positions of the diodes, with

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their terminals remote from the main windings connected to the end of the inductor 60 remote from the point 53. In this case the pulses 71 and 74 of Figure 3C may be absent.

For high voltage high power motors it may be

5. desirable from the point of view of compactness to eliminate auxiliary windings, and Figure 4 shows an arrangement in which this is effected.

The arrangement of Figure 4 differs from the arrangement of Figure 1 in employing a direct voltage source 50 centre-tapped at 56 with the winding and main thyristor of the first and third phases connected across one half of it and those of the second and fourth phases connected across its other half. Each winding has one terminal connected to the centre 10. tapping 56. The winding voltages are thus one half of those associated with Figure 1 during main thyristor conduction.

- 15.

In addition the arrangement of Figure 4 employs a second direct voltage source 57 of voltage EA and 20. much lower power rating connected between the two commutating capacitors 51 and 52. The commutating thyristor 16 of phases one and three is connected to the negative terminal 59 of the second dv source 57 whilst the commutating thyristor 26 of phases two and 25. four is connected to its positive terminal 58. In

addition the junction of each motor winding and its main thyristor is connected through an inductor 19, 29, 39 or 49 and a diode 17, 27, 37 or 47 to the remote supply terminal, that is to say the junctions

5. of phases one and three are connected through main inductors 19 and 39 and diodes 17 and 37 to the positive terminal 54, and the junctions of phases two and four are connected through their inductors 29 and 49 and diodes 27 and 47 to the negative terminal 55.
10. The operation is analogous to that of Figure 1 and a brief description will, it is thought, suffice.

For operation in the single commutation per cycle mode, the negative terminal 59 of the auxiliary supply 57 is initially at a potential  $-E/2 -EA$  when

15. the main thyristor 12 of phase 1 is fired. The thyristor 12 is turned off when the commutating thyristor 16 is fired. The winding current now flows through the inductor 19, commutating diode 15 and thyristor 16 and to the capacitor 52 and via the auxiliary supply 57 to the capacitor 51. The voltage of the point 59 increases until the auxiliary diode 17 conducts when the point 59 is at a potential of  $+E/2$  which means that the point 58 is at a potential  $+E/2 +EA$ . The second phase main thyristor 22 may be fired at any time
20. 25. provided that by the time it is to be turned off the

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point 58 has reached the potential  $+E/2+EA$  referred to above. This is the potential and polarity required to commutate the second phase.

For the chopping mode, the point 59 is initially

5. at a potential of  $-E/2- EA$ . The first phase main thyristor 12 is fired, and when the current reaches the desired predetermined maximum value the commutating thyristor 16 is fired. Current flows via the inductor 19, commutating diode 15 and thyristor 16 and to the capacitor 52, and to the capacitor 51 through the auxiliary source 57. The potential of the point 59 increases until the diode 17 conducts when the point 59 is at  $+E/2$  and the point 58 is therefore at  $+E/2+EA$ . After the thyristor 16 has recovered the commutating
10. thyristor 26 of the second and fourth phases is fired to effect resonant reversal of the capacitors using the main windings 21 and 41 of the second and fourth phases so that the potential of the point 58 oscillates from  $+E/2+EA$  to approximately  $-E/2$ . Thus the potential
15. of the point 59 must be driven to  $-E/2- EA$ , equal to its initial value. The main thyristor 12 of phase one may be fired at any time provided that by the time that it is to be turned off the point 59 has reached this potential  $-E/2- EA$ .
20. 25. For priming the power converter circuit of Figure



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4 it is necessary to preset the voltage V58 at the point 58 to  $-E/2$  if either main thyristor 12 or 32 is to be fired first, or to preset the voltage V59 at the point 59 to  $+E/2$  if either main thyristor 22 or 42 is to be

5. fired first.

When the power converter circuit of Figure 4 is first connected to the source the voltages V58 and V59 will be approximately  $+EA/2$  and  $-EA/2$  respectively, where EA is the voltage of the auxiliary source 57.

10. To set V58 to  $-E/2$ , the commutating thyristor 16 is fired when current will flow through motor windings 11 and 31 such that V58 is resonantly reversed to  $+EA/2$  and V59 becomes  $+3EA/2$ . The commutating thyristor 26 is then fired so as to resonantly reverse

15. V58, current flowing through motor windings 21 and 41. Provided EA is sufficiently greater than  $E/3$ , V58 will reach  $-E/2$  in which case the diodes 27 and 47 will clamp V58 at  $-E/2$ . V59 will now be  $-E/2-EA$ , which is of sufficient value to turn off

20. main thyristor 12 or 32 when either of these is fired.

V59 may similarly be preset to  $+E/2$  by firing the commutating thyristor 26 followed by the commutating thyristor 16, after which V58 will be  $+E/2+EA$  which is of sufficient value to turn off the main thyristor

25. 22 or 42 when either of them is fired.

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The second direct voltage source 57 may be provided from the battery 50 via a subsidiary power convertor or via similar isolating circuitry of known design.

5. In a modification of Figure 4 (not illustrated) the diodes 15, 25, 35 and 45 and thyristors 16 and 26 are replaced by four thyristors in the positions of the diodes, and whose terminals remote from the main windings are connected to the point 59 for the commutating
10. thyristors for phases 1 and 3 and to point 58 for those for phases 2 and 4.

In another modification of Figure 4 shown in Figure 5, diodes 17 and 37 are replaced by a single diode 77 between the supply terminal 54 and the

15. cathodes of the diodes 15 and 35, and diodes 27 and 47 are replaced by a single diode 87 between the supply terminal 55 and the anodes of the diodes 25 and 45. This modification permits the two inductors 19 and 39 to be replaced by a single inductor 79 in series with
20. the diode 77 and the two inductors 29 and 49 to be replaced by a single inductor 89 in series with the diode 87.

The arrangement of Figure 6 avoids the need for the auxiliary DV source 57 by injecting the extra charge needed to provide adequate commutating

25. voltage on the commutating capacitor from a circuit



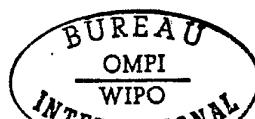
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comprised of two thyristors and either a common inductor or two separate inductors in series. Figure 6 is based on Figure 5, including the diodes 77 and 87, and to protect thyristors against excessive

5.  $di/dt$ , inductors 79 and 89. The circuit which boosts the voltage on the commutating capacitors in preparation for the next commutation comprises two thyristors 61 and 62 and an inductor 63. The capacitors 51 and 52 are connected in series across
10. the main supply 50, as are a pair of thyristors 61 and 62, and the junction 53 of the former is connected through the inductor 63 to the junction 54 of the latter.

The operation of the circuit of Figure 6 is similar to that for Figures 4 and 5 but the operation of

15. the booster circuit requires some explanation. During the commutation of the thyristor 12, for example, the initial voltage at the point 53 is conveniently chosen to be  $-3E/2$  when the thyristor 16 is fired to initiate commutation. Subsequently the potential at the
20. point 53 increases as the current in the winding 11 flows into the commutating capacitors 51, 52. The thyristor 61 must be fired during the discharge or recharge of the commutating capacitors at a time  $t_1$  when the voltage difference  $V63$  between the points 53 and 54 (which
25. appears across the inductor 63 when thyristor 61 is



24.

fired) satisfies the equation.

$$\int_{t_1}^{t_2} V_{63} dt \approx E \sqrt{2LC}$$

5. where  $t_2$  is the time at which  $V_{63}$  becomes zero, L is the value of the inductor 63 and C is the capacitance of the combined commutating capacitors. The approximate equality allows for resistive losses in the booster circuit. The firing time  $t_1$  for the thyristor 61 must

10. be determined by a circuit of conventional design which monitors the voltage at the point 53 and its rate of change, and calculates a time  $t_1$  which will approximately satisfy the equation. The above description applies equally to the commutation of the thyristor 32,

15. and with reversed polarities to the commutation of thyristors 22 and 42.

In the chopping mode the booster circuit 61, 62 and 63 may be used for the reversal of the potential at the point 53 as an alternative to using adjacent

20. motor phase windings according to .....(CASE 282), thus enabling a particular main thyristor to be turned off repeatedly.

In the chopping mode, the boosting function of the circuit 61, 62, 63 is only required for the final

25. commutation of a main thyristor, after which the next



.25.

commutation will be directed to the next phase. For all earlier commutations voltage boosting is not required, provided the booster circuit components are used to reset the polarity of the commutating 5. capacitors. For example, the thyristor 12 is commutated by firing thyristor 16 when the point 53 is at a potential conveniently chosen to be  $-3E/2$ . The thyristor 16 turns off when the current in the winding 11 transfers from the thyristor 16 to the 10. diode 77, leaving the point 53 at the potential of the battery terminal 54 namely  $+E/2$ . The thyristor 62 is now fired, which resonantly reverses the potential of the point 53 about  $-E/2$ , leaving the point 53 at a potential  $-3E/2$  ready for the next commutation of the 15. thyristor 11.

In a further embodiment, not shown, the arrangement of Figure 6 embodies a modified boosting circuit employing an inductor in series with each of the freewheel diodes 77 and 87.

20. This has the effect that may be described as follows. Assume that Phase II is to be switched off by diverting current from the thyristor 12 via the thyristor 16. This will cause the voltage at 53, the junction point of the capacitors, to rise and in the existing circuit it would rise until the diode 77 conducted, i.e. until the point 53 exceeds the supply voltage. An inductor in series with the diode 77 changes the picture slightly because current transfer to the diode 77 is delayed and the voltage at the point 25. 53 can now exceed the supply rail by an amount 30.

.26.

determined by the size of the additional inductor and by the magnitude of the current. The excess voltage available for commutation is thus related to the current which has just been commutated and provided

5. large changes in current between one phase and the next are not experienced, as they would not, this forms a neat way of linking commutation energy requirements to the current.

The principle may be applied in exactly the same

10. way if thyristors are substituted for the diodes 15, 25, 35, 45.

A similar provision may be incorporated in the other arrangements employing a split battery.

It will be appreciated that where reference is

15. made herein to a centre-tapped source the split battery need not have the split in the electrical centre of the battery provided the motor design is modified to suit.

.27.

CLAIMS

1. A variable reluctance electric motor system including a variable reluctance motor having a main winding for each phase, and a power converter circuit including a main thyristor in series with each main winding connected across a d.v. source, from which the main windings are supplied sequentially with unidirectional current pulses, characterised by a commutating thyristor (16) for starving the main thyristor (12) of the current flowing through one main winding while charging a capacitor unit (51,52) in one sense, and a commutating thyristor (26) for starving the main thyristor of the next phase winding (21) of the current flowing through the next main winding while charging the capacitor unit in the other sense whereby a commutation of a main thyristor, initiated by firing a commutating thyristor and terminated when this commutating thyristor ceases to conduct, leaves the commutating capacity or capacitors charged with the correct polarity and adequate voltage for the commutation of the next main thyristor in the conducting sequence.



2. A system as claimed in Claim 1 in which the commutating circuit common to two successive phases has a junction point connected to the junction of the main thyristor and main winding of a first phase through a commutating thyristor arranged when fired to divert current flowing through the winding to flow into the commutating circuit to turn off the main thyristor while making the junction points of the commutating circuit more positive, in preparation for commutating the main thyristor of the second phase, while the junction of the main winding and main thyristor of the second phase is connected to a junction point of the commutating circuit through a commutating thyristor arranged when fired to divert current flowing through the winding to flow out of the commutating circuit to turn off the main thyristor while making the junction points of the commutating circuit more negative, in preparation for commutating the main thyristor of the next phase.
3. A system as claimed in Claim 2 in which both phases are connected to the same junction point of the commutating circuit.
4. A system as claimed in Claim 3 in which the commutating circuit includes two capacitors connected in series between the terminals of a source common to both phases.



.29.

5. A system as claimed in any one of Claims 2 to 4 including a boosting circuit comprising a pair of boosting thyristors connected in series across the source, and an inductor connected between the junction point of the commutating circuit and the junction of the boosting thyristors.

6. A system as claimed in any one of the preceding Claims in which each phase of the motor also has an auxiliary winding closely coupled to the main winding and connected across the source in series with a diode so as to return energy to the source.

7. A system as claimed in Claim 1 or Claim 2 in which the source is centre tapped, the main winding of each phase is the only motor winding, and has one end connected to the centre tapping, with the remote terminal of main thyristors of alternate phases connected to each terminal of the source and the junction of the main winding and main thyristor connected through a regenerating diode to the opposite terminal of the source.

8. A system as claimed in Claim 7 in which successive phases are connected to junction points of the commutating circuit between which a substantially constant potential difference is maintained.

9. A system as claimed in Claim 8 in which the commutating circuit comprises a pair of capacitors connected in series between the terminals of the combined supply with a device maintaining a substantially constant potential difference interposed between them.



.30.

10. A system as claimed in Claim 8 or Claim 9 in which the device maintaining a constant potential difference comprises a battery.

11. A system as claimed in Claim 8 or 9 in which the device maintaining a constant potential difference comprises a relatively large capacitor connected to each terminal of the supply through an inductor.

12. A system as claimed in Claim 8 or Claim 9 in which the device maintaining a constant potential difference comprises a relatively large capacitor charged from the main DV source via an isolating DC-DC converter.

13. A system as claimed in any one of Claims 8 to 12 in which the junction of the main winding and main thyristor is connected to the commutating circuit through an inductor to limit the rate of change of current.

14. A system as claimed in any one of Claims 8 to 12 in which the junction of the main winding and main thyristor of each phase is connected through an inductor to the remote terminal of the supply to limit the rate of change of current.

15. A system as claimed in any one of Claims 7 to 14 including an inductor in series with each regenerating diode to boost the voltage to which the junction point of the capacity unit is charged beyond that of the said opposite terminal.

16. A system as claimed in any one of the preceding Claims in which each commutating thyristor connects the junction of the main thyristor and main winding to the junction point of the capacity unit through a diode.



.31.

17. A system as claimed in any one of the preceding Claims comprising four phases, each having the junction of its main winding and main thyristor connected to the commutating circuit through an individual diode and a commutating thyristor common to it and another phase having its main thyristor connected to the same source terminal.

18. A system as claimed in any one of the preceding Claims including means operative at low speeds for repeatedly firing each main thyristor, followed by the appropriate commutating thyristor and then effecting resonant reversal by firing the commutating thyristor of one or more other phases.

19. A variable reluctance electric motor system as specifically described herein with reference to any one of Figures 1, 2, 4, 5 or 6, and Figure 3 of the accompanying drawings.

20. A variable reluctance electric motor system including a variable reluctance motor having a main winding for each phase and a power converter circuit including a main thyristor in series with each main winding connected across a direct voltage source from which the main windings are supplied sequentially with uni-directional current pulses, characterised by a commutating thyristor (16) for starving the main thyristor (12) of the current flowing through one main winding (11) while charging a capacitor unit (51,52) in one sense, and a commutating thyristor (26) for starving the main thyristor (22) of the next phase winding (21) of the current flowing through the next main winding (21) while charging the capacitor unit in the other sense.



**AMENDED CLAIMS**  
(received by the International Bureau on 4 October 1979 (04.10.79))

1. A variable reluctance electric motor system including a variable reluctance motor having a main winding for each of an even number of phases greater than two, and a power converter circuit including a main thyristor in series with each main winding of the motor so connected across one or more D.V. sources, from which the main windings are supplied sequentially with unidirectional current pulses, that the current in each main winding is independent of that in other main windings, characterised in that of two successive phases (taken in their order of conduction) one has its phase winding connected to the positive terminal of a D.V. source and has a commutating thyristor which connects the junction of the main thyristor and main winding to a junction point of a capacitor unit so as to starve the main thyristor of the current flowing from the positive source terminal through the winding, while charging the capacitor terminal positively, whereafter the commutating thyristor of the next phase, having its phase winding connected to the negative terminal of a D.V. source connects the junction of its main thyristor and main winding to a junction point of the said capacitor unit so as to starve the main thyristor of the current flowing through the main winding to the negative source terminal while charging the capacitor terminal negatively, whereby a commutation of a main thyristor, initiated by firing a commutating thyristor and terminated when this commutating thyristor ceases to conduct, leaves the commutating capacitor or capacitors charged with the correct polarity for the commutation of the next main thyristor in the conducting sequence.



2. A system as claimed in Claim 1 in which the commutating circuit common to two successive phases has a junction point connected to the junction of the main thyristor and main winding of a first phase through a commutating thyristor arranged when fired to divert current flowing through the winding to flow into the commutating circuit to turn off the main thyristor while making the junction points of the commutating circuit more positive, in preparation for commutating the main thyristor of the second phase, while the junction of the main winding and main thyristor of the second phase is connected to a junction point of the commutating circuit through a commutating thyristor arranged when fired to divert current flowing through the winding to flow out of the commutating circuit to turn off the main thyristor while making the junction points of the commutating circuit more negative, in preparation for commutating the main thyristor of the next phase.

3. A system as claimed in Claim 2 in which both phases are connected to the same junction point of the commutating circuit.

4. A system as claimed in Claim 3 in which the commutating circuit includes two capacitors connected in series between the terminals of a source common to both phases.



5. A system as claimed in any one of the preceding claims in which the main windings of all the phases, each in series with its main thyristor, are all connected in parallel across a common D.V. source.
6. A system as claimed in any one of Claims 2-5 including a boosting circuit comprising a pair of boosting thyristors connected in series across the source, and an inductor connected between the junction point of the commutating circuit and the junction of the boosting thyristors.
7. A system as claimed in any one of the preceding claims in which each phase of the motor also has an auxiliary winding closely coupled to the main winding and connected across the source in series with a diode so as to return energy to the source.
8. A system as claimed in Claim 1 or Claim 2 in which the source is centre tapped, the main winding of each phase is the only motor winding, and has one end connected to the centre tapping, with the remote terminal of main thyristors of alternate phases connected to each terminal of the source and the junction of the main winding and main thyristor connected through a regenerating diode to the opposite terminal of the source.
9. A system as claimed in Claim 8 in which successive phases are connected to junction points of the commutating circuit between which a substantially constant potential difference is maintained.



10. A system as claimed in Claim 9 in which the commutating circuit comprises a pair of capacitors, connected in series between the terminals of the combined supply with a device maintaining a substantially constant potential difference interposed between them.



11. A system as claimed in Claim 9 or Claim 10 in which the device maintaining a constant potential difference comprises a battery.

12. A system as claimed in Claim 9 or 10 in which the device maintaining a constant potential difference comprises a relatively large capacitor connected to each terminal of the supply through an inductor.

13. A system as claimed in Claim 9 or Claim 10 in which the device maintaining a constant potential difference comprises a relatively large capacitor charged from the main DV source via an isolating DC-DC converter.

14. A system as claimed in any one of Claims 9 to 13 in which the junction of the main winding and main thyristor is connected to the commutating circuit through an inductor to limit the rate of change of current.

15. A system as claimed in any one of Claims 8 to 14 including an inductor in series with each regenerating diode to boost the voltage to which the junction point of the capacity unit is charged beyond that of the said opposite terminal.

16. A system as claimed in any one of the preceding Claims in which each commutating thyristor connects the junction of the main thyristor and main winding to the junction point of that capacity unit through a diode.



## STATEMENT UNDER ARTICLE 19

The change in page 27, is to replace the original Claim 1 by a more restricted claim, and the change in pages 29 and 30 is to introduce a new Claim 5, for a particular method of connecting the main windings of all the phases in relation to a common d.v. source, to renumber the existing claims, 5-13 as 6-14 with appropriate amendments to the dependencies, to cancel the original Claim 14, and in Claim 15 to correct the dependency to be on Claim 8, rather than Claim 7, and to correct a spelling mistake in the fourth line.



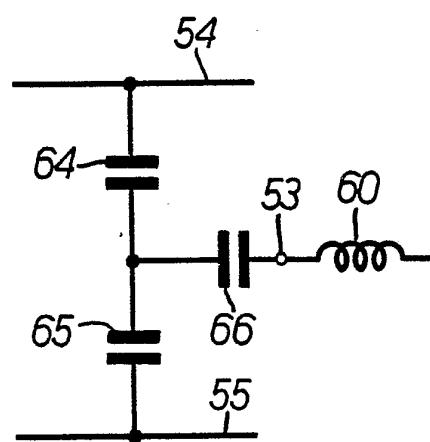
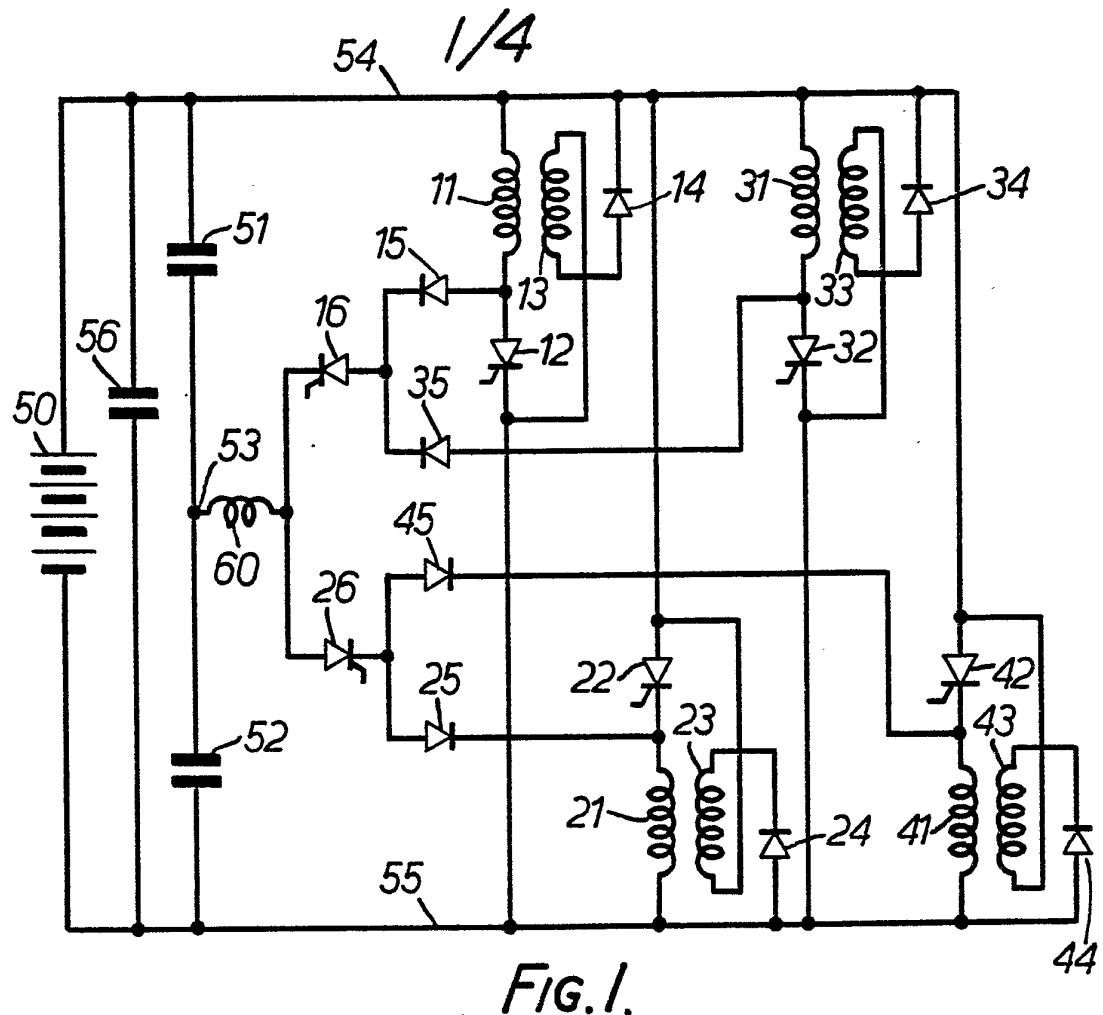
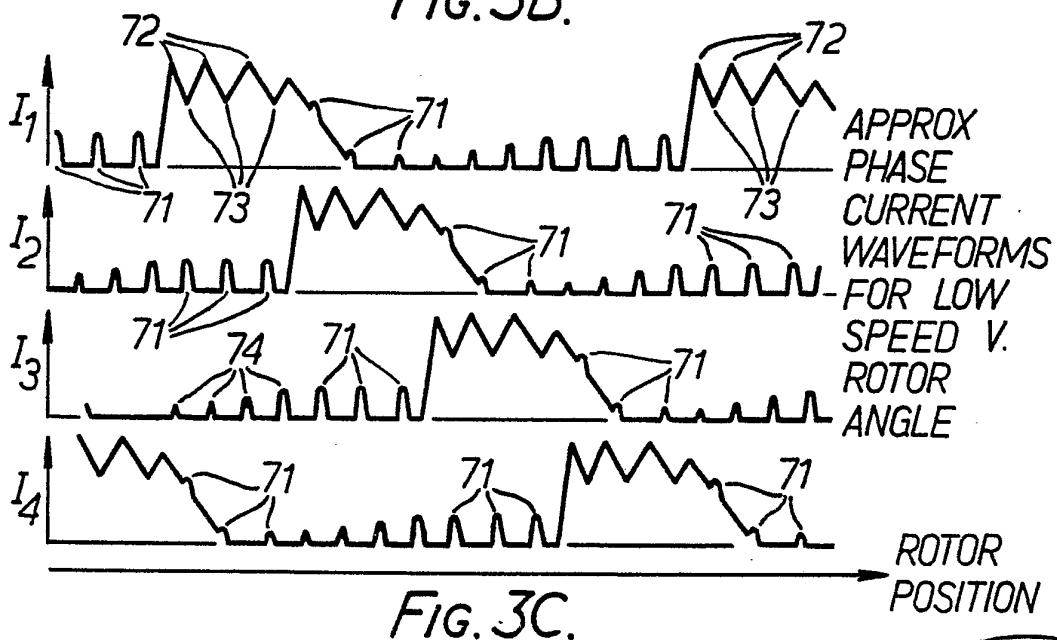
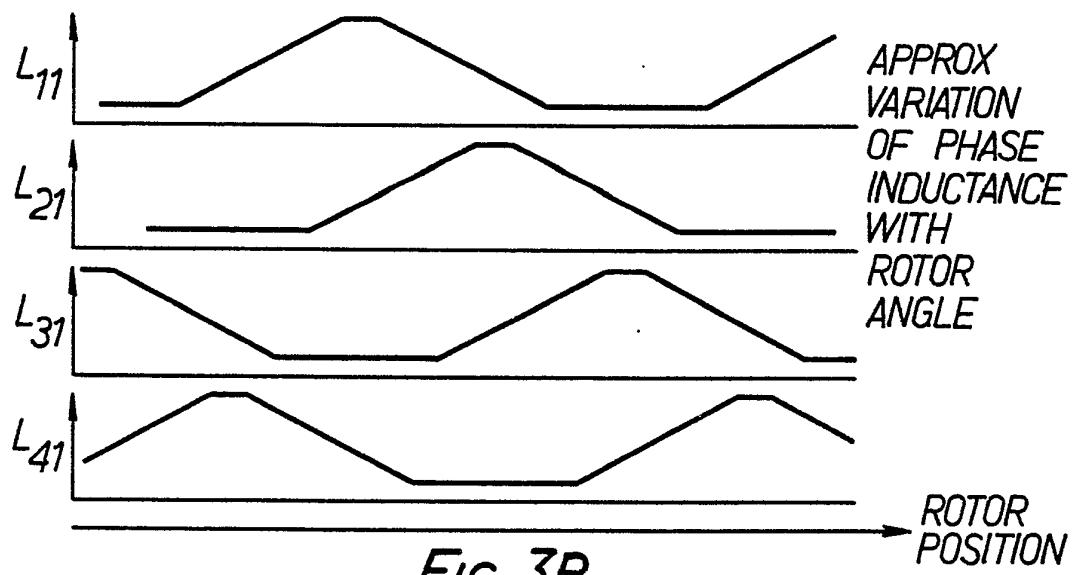
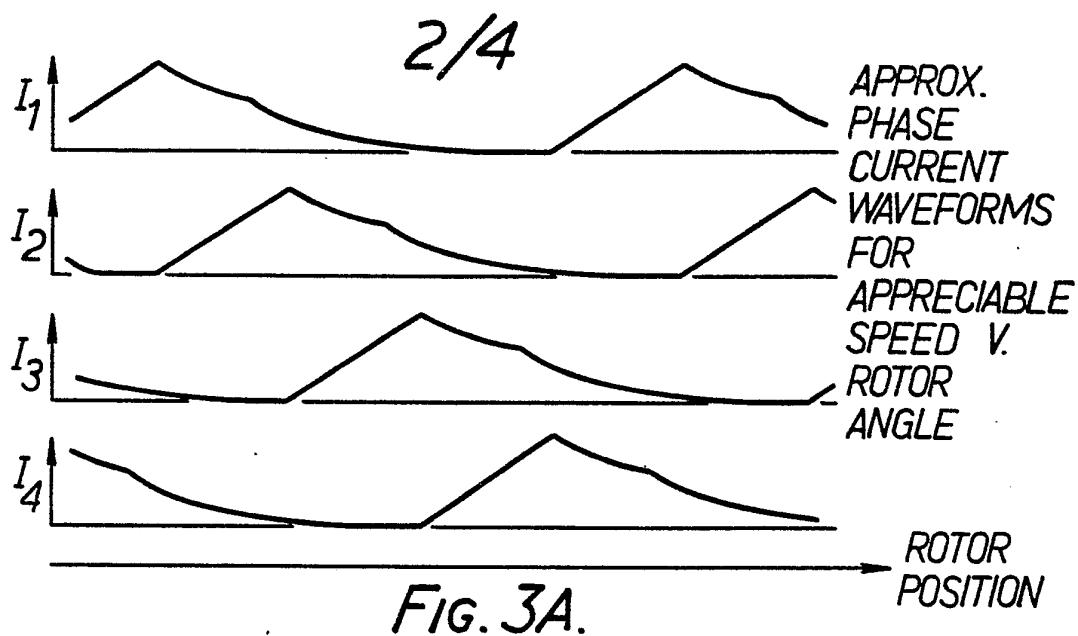


FIG. 2.



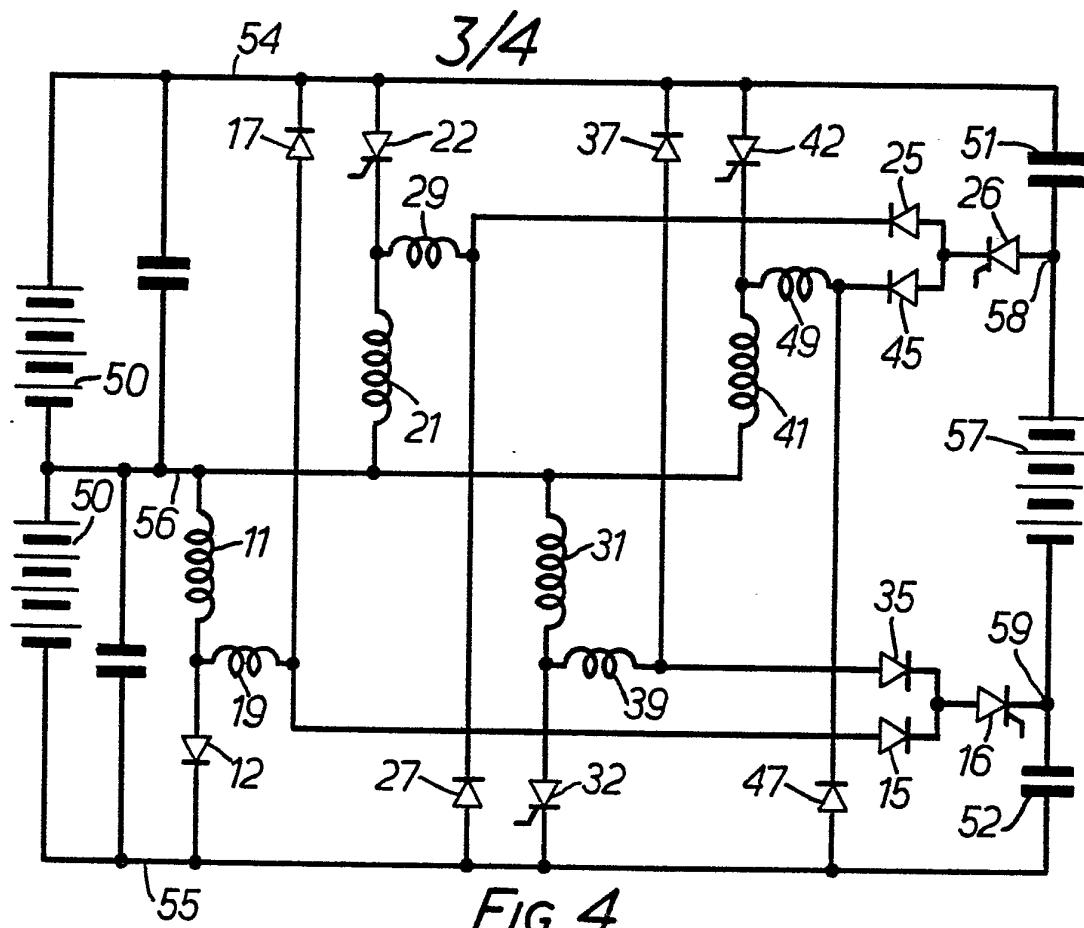


FIG. 4.

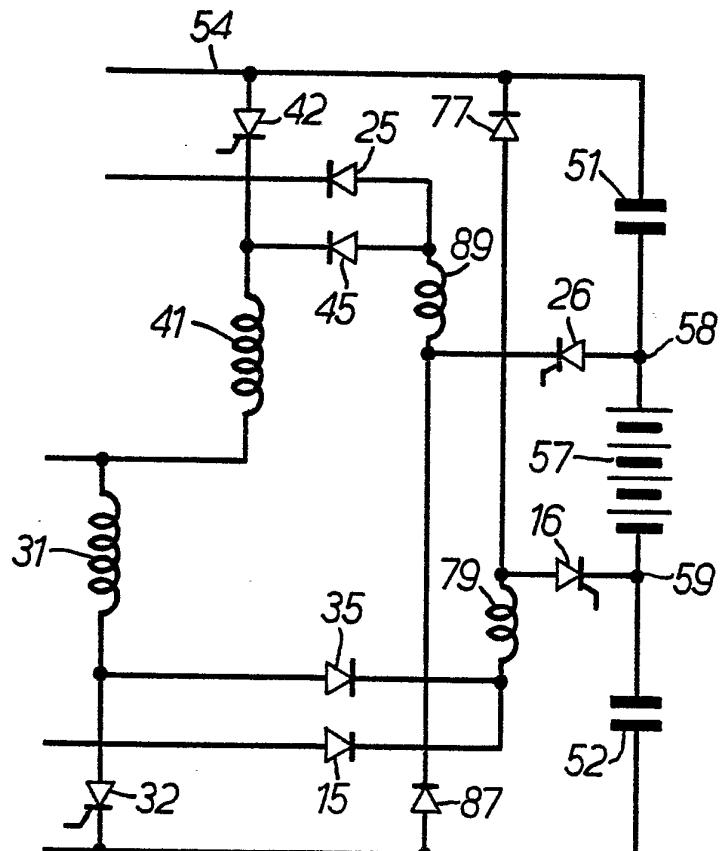


FIG. 5.



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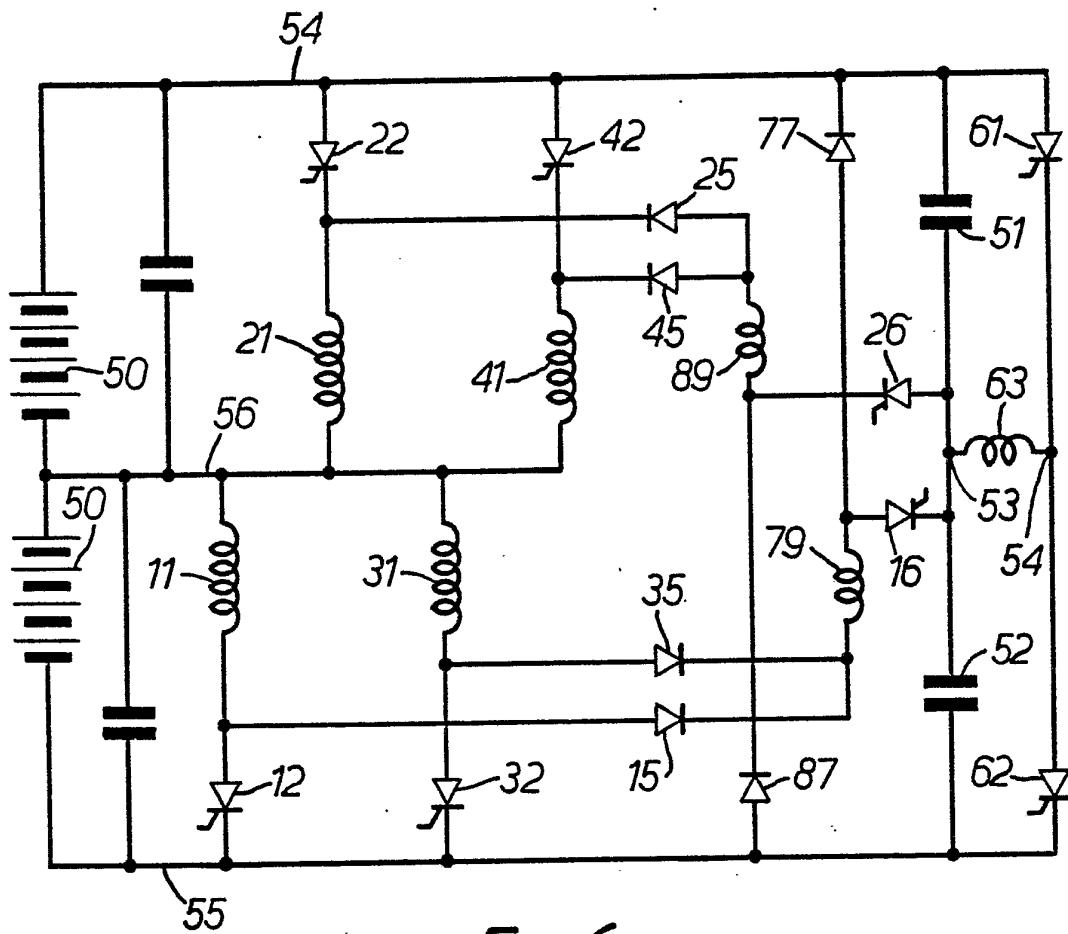


FIG. 6.

# INTERNATIONAL SEARCH REPORT

International Application No. PCT/GB 79/00078

## I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) <sup>3</sup>

According to International Patent Classification (IPC) or to both National Classification and IPC

H 02 P 7/00

## II. FIELDS SEARCHED

### Minimum Documentation Searched <sup>4</sup>

Classification System	Classification Symbols
INT.Cl <sup>2</sup>	H 02 M 3/135; H 02 P 7/00; H 02 K 19/20; H 02 K 19/24 H 02 K 19/06; H 02 K 29/00; H 02 K 29/02; H 02 K 29/04; H 02 K 19/10; H 02 M 5/45, B 60 L 11/18

Documentation Searched other than Minimum Documentation  
to the Extent that such Documents are Included in the Fields Searched <sup>5</sup>

## III. DOCUMENTS CONSIDERED TO BE RELEVANT <sup>14</sup>

Category	Citation of Document, <sup>16</sup> with indication, where appropriate, of the relevant passages <sup>17</sup>	Relevant to Claim No. <sup>18</sup>
	FR,A, 2352435, published 16th December, 1977 see page 2, line 28 to page 3, line 14; page 3, line 28 to page 4, line 28; page 8, line 11 to page 9, line 33; figure 2; Engins Matra. --	1-4,9,15, 20
	FR,A, 2094313, published 4th February, 1972 see page 2, lines 6 to 19; page 3, line 11 to page 5, line 40; figure 2; J. Jarret and Jac. Jarret. --	1-4,15,16, 17,20
A	US,A, 3956678, published 11th May, 1976, see column 7, lines 37 to 60; figure 3; J. Byrne and J.C. Lacy. --	1,6
A	US,A, 3697840, published 10th October, 1972, see column 1, line 60 to column 2, line 36; Ford Motor Company. --	1,2,20
A	US,A, 3401323, published 10th September, 1968, see column 1, lines 50 to 64; TRW Inc. --	1 ..

\* Special categories of cited documents: <sup>19</sup>

"A" document defining the general state of the art

"E" earlier document but published on or after the international filing date

"L" document cited for special reason other than those referred to in the other categories

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but on or after the priority date claimed

"T" later document published on or after the international filing date or priority date and not in conflict with the application, but cited to understand the principle or theory underlying the invention

"X" document of particular relevance

## IV. CERTIFICATION

Date of the Actual Completion of the International Search <sup>20</sup>

23rd July 1979

Date of Mailing of this International Search Report <sup>21</sup>

9th August 1979

International Searching Authority <sup>22</sup>

EUROPEAN PATENT OFFICE

Signature of Authorized Officer <sup>23</sup>

F.M. VRIJDAG

## FURTHER INFORMATION CONTINUED FROM THE SECOND SHEET

-2-

A GB,A, 1295103, published 1st November, 1972, see page 2, line 126 to page 4, line 12; figure 3, Ford Motor Company Ltd.

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1,7

V.  OBSERVATIONS WHERE CERTAIN CLAIMS WERE FOUND UNSEARCHABLE <sup>10</sup>

This international search report has not been established in respect of certain claims under Article 17(2) (a) for the following reasons:

1.  Claim numbers ..... because they relate to subject matter<sup>12</sup> not required to be searched by this Authority, namely:

2.  Claim numbers ....., because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out<sup>13</sup>, specifically:

VI.  OBSERVATIONS WHERE UNITY OF INVENTION IS LACKING <sup>11</sup>

This International Searching Authority found multiple inventions in this international application as follows:

1.  As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims of the international application.

2.  As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims of the international application for which fees were paid, specifically claims:

3.  No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claim numbers:

## Remark on Protest

- The additional search fees were accompanied by applicant's protest.
- No protest accompanied the payment of additional search fees.