A new add-on vehicular system is capable of responding to large area or nation-wide commands over paging networks, to remotely foil the unauthorized use or theft of a vehicle or a fleet automobile or a group of fleet vehicles, as well as to help the recovery of stolen vehicles. The preferred embodiment of the system comprises a paging receiver (2) and decoder (3), a microcontroller (4) with embedded programmable software (29) and memory and a vehicular system control interface (6). The system does not require central monitoring systems, or portable controls or vehicular user set controls or portable key chain controls or keypads or cellular phones or separation of vehicular transceivers from owner carried transceivers to activate the system. In a second embodiment, a two-way radio paging approach is employed in the system to expand its capabilities and to additionally provide remotely controlled transmission of data from vehicles including data pertaining to the position coordinates of the vehicle.
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FIELD OF INVENTION

This invention is in the field of security. It pertains to fleet and individual vehicle security and, more specifically, a system for foiling of carjackings and car thefts over a large area. The preferred embodiment of the system of the invention employs a new add-on vehicular system with means of responding to large area or nation-wide commands over paging networks, to remotely stall or stop or deter the unauthorized use or theft of a vehicle or a fleet automobile or a group of fleet vehicles, as well as to help the recovery of stolen vehicles, without necessitating portable controls or vehicular user set controls or portable key chain controls or cellular phones or separation of vehicular transmitter and receiver systems from owner carried transmitter and receiver systems to activate the system. In a second embodiment, two-way paging is employed in the system of the invention to additionally provide remotely controlled transmission of data from the vehicles, including position coordinates.

BACKGROUND OF INVENTION

One of the most traumatic experiences of modern times is to be subjected to a carjacking after a fearsome encounter with a criminal, without any concrete hope of foiling the carjacker or recovering the vehicle. Everyday, thousands of vehicles are being stolen from fleets or individuals or hijacked without any quick resolutions being available. Since a vehicle is one of the easiest means of getaway, the stolen vehicle is typically utilized to quickly place a large distance between the scene of the crime, the victim and law enforcement agencies. This phenomenon has dramatically changed our lives as we no
longer feel tranquil and free of fear when moving about in a vehicle, or when observing stop signs or traffic signals, or going towards our parked cars or enjoying a ride. Often when a friend or a loved one leaves with a car, there is a sinking feeling of anxiety for their safety. Furthermore, in most cases, when the recovery of the stolen vehicle is delayed beyond several hours, it is taken apart and its subsystems sold through various criminal networks. Prompt action is required to foil carjackers and recover the vehicles.

This type of crime is not unique to the United States, as exemplified during a recent trip to Moscow where a car was carjacked right in front of the hotel in plain daylight. Car thefts and carjackings are major issues in many developing nations. The loss of one's vehicle in a large city like Moscow is quite debilitating, especially where there is widespread crime and when the cost of a replacement car can be prohibitive. In most cases, all any victim can do, is to report such a crime to the police by telephone as soon as possible, hoping the vehicle can be recovered. Carrying portable security remote controls is of no value in the case of a carjacking. The criminal almost always frisks and robs the victims, often making them lie face down and searching them thoroughly.

Criminals are also stealing government and police vehicles, posing a greater hazard to citizens and public safety organizations. Car dealerships and garages suffer equally when unattended vehicles are stolen at night or on weekends. Other vulnerable fleets include rental cars, military and commercial trucks carrying cargo, distribution vehicles with cargo, vehicles transporting money or precious items, very expensive tractors and bulldozers, armored personnel carriers, tanks and vans used for moving.

A simple, effective vehicular security system that can be accessed over a wide area to foil fleet or individual vehicle carjackings and car thefts during getaways, that can also help locate the stolen vehicles, as well as deter the theft of unattended vehicles, is needed to stop the rampage of carjackings and car thefts plaguing society. The system of the invention specifically and effectively addresses and fills this need.
DESCRIPTION OF THE PRIOR ART

The present invention has not been found in prior art, nor does prior art suggest or teach anything resembling the method of this invention. There are various conventional and seemingly exotic vehicle security systems on the market falling into a number of well-known categories. However, there are no vehicular systems designed for fleet managers and vehicle owners to directly, over the phone - from anywhere in the world where there is telephone service, to foil carjackings and car thefts in progress, for one or more vehicles at a time, over wide geographical areas, using pager networks that are available in most parts of the world, without needing portable control transmitters or portable and mobile transceivers, with means to clearly point out stolen vehicles, plus means to prevent the use of the vehicles for getaways and additional means to also deter the theft of parked fleet vehicles and individual cars with a single phone call.

The following are the known types of vehicular security or anti-theft protection systems as compiled from pertinent sources, including "Installation News" a periodical that specializes in vehicle security products, recent shows related to vehicular security systems, previous patents and security product supply stores such as Radio Shack and Sears:

- Motion sensor based car security systems
- Entry code activated car security systems
- Key chain transmitter or other portable transmitter controlled car security systems
- Proximity detector car security systems
- Vehicular security systems that alert the owner by sending a transmitted signal
- Key controlled electrical security systems
- Mechanical security systems
bullet Cellular phone controlled security systems
bullet Data transmission based security system with centralized remote control
bullet Vehicular transceiver and portable transceiver system activated by their separation

None of the above prior art car security systems is designed for foiling fleet, as well as individual carjackings and car thefts in progress by means of simple phone calls placed by the owner of the car or manager of the fleet, using wide area paging networks, without having to use portable remote controls or expensive cellular systems that can be ripped out or without relying on a control system managed by others from some other location. None of the prior art vehicular systems are designed for vehicle operators and fleet managers to arm and disarm both the carjacking related getaway foiler mode or unattended car protection mode, for one or more vehicles, from any telephone in the world.

Examining prior art systems in more detail, the motion sensor type car security systems employ motion sensors, such as pendulums, which upon movement of the car or its portions, close electrical contacts that produce alarms, such as the honking of the horn or activation of the lights. Some motion sensing systems, when triggered by motion, also open electrical contacts that prevent starting of the car. In addition to simple mechanical opening and closure of contacts, such systems can use digital circuitry with solid state control of various portions of the vehicle. Other versions of this type of security system also cause an alarm to be triggered when a door or window or other locked portion of the vehicle is opened. However, these types of systems are ineffective during carjackings, as most vehicles are carjacked while occupied or in the presence of the legitimate operator or with the keys of the car and the controls of the security system provided to the carjacker under duress and with the security system inactivated. Furthermore, these systems can not foil the progress of the carjacking after the thief drives away in the stolen vehicle nor can they provide control for fleets to provide instant securing of fleet vehicles whether attended or unattended.
Entry code activated systems employ a keypad that is in or on the vehicle, to activate or deactivate the security system of the vehicle, which when activated, produces various alarms and can also prevent starting of the car. With some versions, a short-range radio signaling employing a portable quasi-pager is provided to alert the vehicle’s legitimate operator of the theft. However, these types of systems are ineffective during carjackings, as most vehicles are car-jacked while occupied or in the presence of the legitimate operator or with the keys of the car and code provided to the carjacker under duress and with the security system inactivated. Furthermore, these systems can not foil the progress of the carjacking after the thief drives away in the stolen vehicle nor can they provide control for fleets to provide instant securing of fleet vehicles whether attended or unattended.

Key chain transmitter or other portable transmitter controlled car security systems employ a low power transmitter, such as a UHF Band transmitter in the 350 MHz Band, to remotely arm and disarm the system over short distances. They also remotely activate various electrical systems of the car, such as the head lamps. The control range of key-chain control transmitters is quite limited. When the system is armed, opening doors, windows or other locked portions of the vehicle trigger an alarm and may also prevent starting of the car. However, these types of systems are ineffective during carjackings, as most vehicles are car-jacked while occupied or in the presence of the legitimate operator or with the keys of the car and the key-chain transmitter or other portable transmitter control of the security system provided to the carjacker under duress and with the security system inactivated. Furthermore, these systems can not foil the progress of the carjacking after the thief drives away in the stolen vehicle nor can they provide control for fleets to provide instant securing of fleet vehicles whether attended or unattended.

An example of the above portable transmitter controlled car security system is illustrated in U.S. Patent No. 4,691,801 to Yale Mann and Pekka Sarssi in which a hand-held infrared transmitter is employed to control the parking brake of vehicles to prevent theft or carjacking. The shortcomings are evident. Only a short range is achieved. The infrared signal from the portable infrared transmitter requires direct line of sight with the vehicle’s infrared receiver. or a consistent reflective path guiding the light beam into the
infrared receiver. Furthermore, the infrared signal is prone to interference from other heat sources, such as heat from sun heated roads which can reach over 100°F in many parts of the world, as well as heat from vehicle exhausts and other sources of heat. Additionally, the remote control can be seized by the carjacker as most carjackings include robberies and frisking of the victim. Such a system, unlike the system of the invention, cannot control the vehicle's incapacitation over large distances nor able to control multiple fleet vehicles at the same time. The system of U.S. Patent No. 5,307,048 to David G. Sonders also uses a remote controller with limited range. The same shortcomings apply for such systems. The portable remote control would have limited range and cannot control a plurality of vehicles. Such a system lends itself to the carjacker taking the controller unit from the victim while frisking the legitimate owner during a carjacking involving robbery as in many cases. It is also an expensive approach, since both a portable transmitter is required, as well as a receiver. Given the low power outputs of portable transmitters, according to radio propagation laws, line of sight between the transmitter and receiver determines the actual range of the control when frequency bands such as VHF and UHF that are relatively immune to ambient vehicular and electrical noise interference are commonly chosen for this type of transmission and reception. With the antenna of the vehicle's security system antenna being at a height of maximum six feet and the height of the antenna of the transmitter held by the victim being around 6 feet, the range of the line of sight communication is seriously diminished with distance, with noise from computers, vehicle ignition systems, high voltage power lines soon competing with the weak signals involved after a short distance where there is no longer a line of sight.

Proximity detector car security systems rely on the principle of impedance changes induced in electronic circuitry, such as oscillators, by body proximity or touch. One type of impedance change is the change in capacitance produced by the proximity of a human body or by a human touch. Such proximity or touch triggers an alarm and activates or deactivates various parts of the car to prevent theft. This type of system often requires a key chain-transmitter to deactivate the system for the legitimate operator and to set the alarm on upon parking the vehicle. However, these types of systems are ineffective during carjackings, as most vehicles are car-jacked while occupied or in the presence of the
legitimate operator or with the keys of the car provided to the carjacker under duress and with the security system inactivated. Furthermore, these systems can not foil the progress of the carjacking after the thief drives away in the stolen vehicle nor can they provide control for fleets to provide instant securing of fleet vehicles whether attended or unattended.

Remote signaling vehicular security systems are typically optional portions of the other types of prior art security systems. Some remote signaling systems, however, are stand-alone and provide a signal to the legitimate operator of the vehicle when a car theft is in progress. This type of system typically comprises a low power transmitter that is installed in the vehicle and which is activated when a sensor, such as a door switch, triggers an alarm during a car theft attempt. A matching receiver that is carried by the legitimate operator receives the signal signifying a car theft in progress. Other, vehicular portions of this type of car security system produce alarms and can also prevent starting of the car. This category of systems is illustrated in U.S. Patent No. 4,904,983 to Steven Mitchell and comprises an alarm condition detector, an auto-dialing mechanism and a radio telephone conveying the alarm condition to an alarm receiving station which records the event. U.S. Patent No. 4,665,379 to Glomski Arthur F. and Howell Jack D. describes a system which uses sound discrimination to sense an alarm condition and provide various types of alarm reporting for theft prevention. A pager is mentioned without much clarification as one means of reporting an alarm condition. However, this would not be a conventional pager but simply a remote receiver that would be triggered to alert the legitimate owner of the vehicle. However, this type of system is ineffective during thefts as it requires the owner to rush to the vehicle and, at best, if the vehicle is not already stolen, either wait for the police to arrive or confront a criminal who may be armed. Also, such systems are ineffective for securing fleets or individual carjackings as most vehicles are car-jacked while occupied or in the presence of the legitimate operator or with the keys of the car provided to the carjacker under duress and with the security system inactivated. Furthermore, these passive and expensive systems using both transmitters and receivers
can not foil the progress of a theft or carjacking after the thief drives away in the stolen vehicle.

Key controlled electrical security systems comprise a security system that is armed and disarmed with a key similar to an ignition key that closes an electrical system and arms the security system or opens a circuit and deactivates the security system. Once the system is armed and a sensor triggered, the actions of the alarm are analogous to the other types discussed above and can involve preventing the starting of the car. Again, these types of systems are ineffective during carjackings, as most vehicles are car-jacked while occupied or in the presence of the legitimate operator or with the keys of the car and the key control of the security system provided to the carjacker under duress and with the security system inactivated. Furthermore, these systems can not foil the progress of the carjacking after the thief drives away in the stolen vehicle nor can they provide control for fleets to provide instant securing of fleet vehicles whether attended or unattended.

There are many types of mechanical security systems that deter driving or steering of the vehicle by a car thief. None of these types of mechanical security systems are effective during a carjacking, as most vehicles are car-jacked while occupied or in the presence of the legitimate operator or with the keys of the car provided to the carjacker under duress and with the security system inactivated. Furthermore, these systems can not foil the progress of a theft or carjacking after the thief drives away in the stolen vehicle, nor can they be used to stall or stop a fleet vehicle that is stolen or a group of cars that need to be simultaneously secured.

Other schemes, such as a secret activation of an alarm by the legitimate operator before leaving the vehicle can prevent a carjacker from driving away but is not advisable as, in most cases, carjackings are carried out by armed criminals and the last thing one wants to do is to provoke them while in a life threatening situation. Furthermore, many carjackings occur when the legitimate owner is approaching the vehicle, such as in a parking area.

Cellular phone controlled security systems require cumbersome modifications of cellular phones of which there are many types. In such an impractical system, a cellular phone in a stolen or carjacked vehicle has to be called and commands given to control
various parts of a vehicle. This appeared as a humorous sequence in a recent movie. U.S. Patent No. 5,276,728 to Kenneth Pagiarolli and Dean Pagiarolli represent vaillant attempts of utilizing a mobile telephone based control approach from fixed and portable transmitters using mobile telephone networks and cellular phones. However, such a system is expensive, since it requires the installation of a mobile telephone such as a cellular phone. It would require custom interfacing due to the many types of cellular phones in use. Furthermore, the carjacker can turn off the cellular phone since it is a known fact that the phone can be used to trace the criminal.

The above referenced U.S. Patent 5,276,728 mentions in its abstract the use of a scanning receiver. This signifies the use of multiple frequencies being scanned. In addition said abstract cites the requirement of at least one additional frequency outside conventional mobile telephone frequency ranges. This immediately represents potential problems and a waste of the finite and scarce resource of frequencies available especially in larger cities with little availability of free frequencies. Potential problems can ensue from the requirement of the minimum one extra frequency outside the normal mobile telephone frequency range. Such a frequency may not be available or, even if available, may not fall within the multifrequency spread capability of the mobile transmitter receiver and also may fall outside the range of the mobile telephone's duplexer which is designed for the mobile telephone operating band only.

The scanning requirement in Patent 5,276,728 can further cause the preamble or part of the code being transmitted during the emergency to be lost, because the receiver can be on another of the scanned channels at the moment the first code is received. The signaling scheme in U.S. Patent 5,276,728 is not clear whereas the actual scheme that would be utilized has an important bearing on reliability, accuracy and speed of such a system, as well as compatibility with the mobile telephone. The disablement means of said patent is also moot and ineffective if ignition control for disabling is used. Sudden interruption of ignition can be disastrous to other motorists driving around the vehicle controlled and would not disable a diesel vehicle that relies on compression to set off the explosion of the fuel-air mixture inside its cylinders and not on ignition. The cellular phone can also be torn out of its mounting bracket or have its exposed wiring in the passenger
of the world and is further hampered by the lack of uniformity in the designation of universal frequencies and signaling schemes for mobile telephone service. In Europe, adjacent countries have to use different frequencies not to have mutual interference. Among further drawbacks, many current cellular users prefer portable phones thus not being able to prevent the theft of the unattended vehicle whenever the portable phone is with the legitimate operator or whenever the portable phone has to be surrendered to the criminal during a carjacking.

In a further pursuit of employing cellular telephones for vehicular security systems, U.S. Patent No. 5,81,667 to Drori Ze'ev and Woskow Robert M. discuss providing multiple types of interfacing for different types of mobile cellular phones through software libraries and then utilizing the radio link established to provide various telemetry and alarm functions. In one variation, the alarm condition is reported by the vehicular cellular telephone dialing a remotely located pager through preprogrammed coding presumably to report an alarm situation. This is quite different from the system of the invention where a paging receiver is used in the vehicle and not for a passive reporting role but for effecting control over the vehicle though a unique combination of the to same elements of the system of the invention. U.S. Patent 5,081, 667 suffers from the principal problems as the aforementioned U.S. Patent No. 5,276, 728 to Kenneth Pagliaroli and Dean Pagliaroli. It is not economical to have to purchase a cellular telephone, adapt it with special interfacing and, during carjackings or theft, risk the mobile telephone being torn out or incapacitated through cutting of wiring attached to the cellular phone. As all criminals have realized through cases such as the murder and carjacking of Michael Jordan's father, the mobile telephone can be used to trace them. Furthermore, simply providing a software library to cover various types of cellular phones is not sufficient to impart special capabilities to mass manufactured or even custom manufactured cellular phones. There would have to be hardware additions and modifications necessary to accommodate the library of software and for the software to interact with the microcontroller and radio frequency circuits of the cellular telephone and its memory banks which are nowadays miniaturized and densely packed through multilayer surface mounted circuitry. Cellular phones are intended for
modifications will certainly interfere with the original manufacturer's design and may invoke legal disputes on proprietorship of designs and revocation of warranties. To the long list of shortcomings of such an approach is added the fact that miniature hand-held portable cellular phones are coming into widespread use since they provide safety access and communications beyond a vehicular cellular telephone. Limitations on the above cited system of U.S. Patent 5,081,667 include an inability to control fleets of vehicles and coping with the myriad of mobile telephone systems in use around the world each with its signaling scheme and regulations. Data transmission based vehicular security system with centralized remote control are not self sufficient or stand-alone systems and depend on central systems wherein lie most of the intelligence and protocols of the system. Such systems require costly central supervisory facilities which would have to be duplicated or connected to different regions and installed in various parts of the country and parts of the world. The data format and frequency bands used are under control of PTT (Post Telephone and Telegraph) authorities in almost all parts of the world, which regulate data transmission protocols and allocation of frequency bands.

Finally there are the vehicular transceiver plus portable transceiver systems that are activated by the separation of the a transceiver in the vehicle and another "palm-sized" transceiver that is carried by the vehicle operator. This is indeed a very expensive approach with serious limitations. In addition to the transmitter and receiver in the vehicle forming part of the system, a portable transmitter-receiver is required and must be carried by the legitimate operator at all times. Among other limitations of such systems is the requirement of continuous and heavy battery usage for the portable transmitter-receiver, very limited range - certainly not operable over a wide area, inability to secure fleets, complications during valet parking when the vehicle has to be separated from the legitimate operator, a requirement for multiple portable transceivers when more than one person, such as a husband and wife, need to use the transmitter, complications when multiple users of the vehicle each carrying a portable transceiver are separated and ineffectiveness during carjackings, as most of the time, the victim is frisked and robbed and thus would have to surrender the hand-held transmitter receiver.
None of the above discussed prior art systems provide the full capabilities of the system of the invention and have inherent shortcomings in meeting the objects of the invention.

SUMMARY OF THE INVENTION

The system of the invention is primarily intended for vehicular applications, although it can be employed for non-vehicular or fixed applications to effect essentially all types of remote control functions desired. Accordingly, the following disclosure primarily relates to vehicular applications of the invention.

The system of the invention is referred to by the inventors as “Vindicator”, said term being intended for use interchangeably hereinafter with the term “system of the invention”. Vindicator is a new add-on vehicular system for vehicles that are already in use and is also intended for factory installation in new vehicles. The invention provides a means of responding to wide area or nation-wide commands over paging networks that are available in most parts of the world, without necessitating portable transmitter controls or hand-held operator controls in the vehicle. The system of the invention is capable of being remotely activated from anywhere in the world that has telephone service. Depending on the mode of remote activation chosen, the system of the invention will prevent the theft of an unattended vehicle or theft of a fleet automobile or theft of a group of fleet vehicles, as well as gradually incapacitate such vehicles during a getaway after theft, including during carjackings. Additionally, the system of the invention is designed to help the recovery of stolen vehicles.

It is thus an object of the invention to provide a means of foiling fleet vehicle car thefts, whether involving one fleet vehicle or multiple fleet vehicles, or an individual vehicle and whether the vehicles are unattended or upon the occurrence of a carjacking.
Another object of the invention is to provide a means of reaching the vehicular system of the invention by wireless signals over city wide areas covered by paging networks and initiating the foiling of fleet or individual car thefts and carjacking from anywhere in the city where the theft or carjacking occurs.

It is an additional object of the invention to provide further means of reaching the vehicular system of the invention over wide area or statewide paging networks and remotely initiating the foiling of fleet or individual car thefts and carjacking on a statewide basis.

It is a further object of the invention to provide further means of reaching the vehicular system of the invention over a nationwide paging network and remotely initiating the foiling of fleet or individual car thefts and carjacking on a nationwide basis.

It is another object of the invention to provide the legitimate owner of the fleet or individual vehicles to reach the vehicular system of the invention and initiate the foiling of fleet or individual car thefts and carjacking, within the full extent of the geographical coverage of the paging network the vehicular system is registered in, from anywhere in the world where there is telephone service.

It is a further object of the invention to foil carjackings in such a manner as to draw attention to each stolen vehicle to help its recovery.

It is another object of the invention in one of its embodiments to foil carjackings by providing means for locating the vehicle through the transmission of pertinent data from various systems in a vehicle, including data on exact location coordinates from a vehicular global positioning system working in conjunction with the system of the invention.

Another object of the invention is to deter the criminals from using the stolen vehicle for a getaway, through a gradual, safe and partial incapacitation of the vehicle while it is being driven in public areas, so as not to pose a danger to other motorists or the public, yet prevent the use of the stolen vehicle for escaping.
It is an objective of the invention through a remote slowing down and incapacitation of a stolen vehicle to obviate high speed chases by police, often endangering the public and the lives of officers, to catch a speeding car thief.

It is yet another object of the invention to eliminate the need for operator controls in the vehicle to activate the foiling system or the use of portable remote controls. This is intended to make the system of the invention inconspicuous, to deter the potential coercion of the legitimate vehicle operator into yielding the remote controls used in prior art systems to the carjacker or being forced under carjacking duress to deactivate the vehicular security system.

Another object of the invention is to greatly reduce the expense of a system for foiling fleet or individual car thefts and carjackings through the simple, yet very effective system of the invention essentially comprising of 1) an inexpensive new generation mass produced paging receiver chip with a decoder (without the usual batteries, display and other frills of a conventional complete pager), 2) a commonly available microcontroller with programmable software instructions and 3) a vehicular interface. The software instructions, once developed and adjusted to the specific use, do not represent any tangible or significant future manufacturing costs, requiring only a loading or programming adjustment procedure that is essentially common to all microcontrollers. Prior art systems cannot provide the capabilities of the system of the invention. They also employ a plurality of expensive elements, including special limited range transmitter and receiver units, dual transceivers, modified mobile cellular telephones, software libraries, hand-held remote controls, expensive central systems, expensive use of multiple radio frequency channels and expensive custom radio networks to provide communications.

It is a further object of the invention to effect control over vehicular systems for the purpose of foiling fleet or individual car thefts and carjackings without requiring pervasive modifications of the car vehicle manufacturer’s systems. The approach utilized
in the system of the invention simplifies installation and does not interfere with the delicate electronic computer based systems of vehicles.

A further object of the invention is the use of a versatile, open and programmable software infrastructure in the system of the invention so as to allow programmable customization without hardware changes. Such customization capability includes expansion of the basic operating modes of the system of the invention, expansion of the number of vehicular controls it effects, expansion of the number of vehicles a fleet consists of, adjustment of the number of digits required to enter over the phone to take over control of portions of the vehicle, adjustment of the timing of various control and alerting sequences, customization for external data transmission in the two-way embodiment of the invention, customization for fleet operation or individual use, adjustment for grouped control of vehicles, adjustment of the degree to which the vehicle is incapacitated and over how much time and adjustment of the initializing protocols that enter the vehicular system into a paging network.

**BRIEF DESCRIPTION OF THE DRAWINGS**

**FIGURE 1** is a block diagram of the preferred embodiment of the system of the invention comprising a paging receiver and decoder unit 1, a microcontroller 2 and a vehicular interface 3.

**FIGURE 2** is a detailed system connection and flow diagram of the preferred embodiment of the invention represented by **FIGURE 1**, disclosing the system connections and signal flow details of the system blocks, details of the interaction between the system elements and details of each sub-element of the vehicular interface system.

**FIGURE 3** is a description of the actual detailed, down to component level, electrical schematic of the preferred embodiment of the system of the invention, disclosing
details of all the ancillary components related to the system blocks and the total circuitry of the system of the invention.

FIGURE 4 is a block diagram of a second embodiment of the invention comprising a transceiver (transmitter/receiver) with its decoder unit 1, a microcontroller 2 and a vehicular interface 3.

FIGURE 5 is a detailed system connection and flow diagram of the embodiment of the invention represented by FIGURE 4 in which a two-way paging transceiver module is substituted in lieu of a pager receiver module to provide transmitted acknowledgment by the system of the invention of the receipt of commands and to also transmit other desired data. FIGURE 5 discloses the connections and signal flow details of the system elements, details of the interaction between the system elements and also details of each sub-element of the vehicular control systems interface.

FIGURE 6 is a description of the actual detailed, down to component level, electrical schematic of the second embodiment of the system of the invention, disclosing details of all the ancillary components related to the system blocks and the circuitry of the second embodiment system of the invention.

DETAILED DESCRIPTION OF THE DRAWINGS

FIGURE 1 is a block diagram of the preferred, currently lower cost embodiment of the system of the invention. It comprises a paging receiver and decoder unit 1, a microcontroller 2, and a vehicular interface 3. This system is designed to prevent the theft of unattended fleet vehicles or individual vehicles and to aid in the recovery of fleet vehicles or an individual stolen vehicle that is being driven away after a theft or carjacking. After the vehicle is stolen, the owner, using the public telephone system, calls an assigned telephone number and enters the customer defined identification number plus a code to specify the operating mode desired. In the case of a criminal escaping with a stolen vehicle, the particular code used will enable the getaway foiling mode. The call made will
result in a paging signal being sent over an existing paging network and received and
decoded by the paging receiver and decoder unit 1 of the system of the invention in the
vehicle. The identification dialed by phone plus the code for the desired operating mode
entered by phone is decoded by the receiver and decoder 1 and conveyed to the
microcontroller 2 interconnected with the paging receiver and decoder unit 1. This will
result in microcontroller 2 causing the vehicular interface 3 to activate a series of distinct
alarms, followed by a gradual loss of the fuel flow to the engine of the vehicle, culminating
in the fuel system being gradually and effectively disabled so as to prevent the criminal
using the vehicle for a getaway, without posing a hazard to other motorists. When the
engine is incapacitated after the activation of this getaway foiling mode, the vehicle will
not start due to the disablement of the ignition system as programmed into the system of
the invention.

Pursuant to the main alarm and incapacitation event, the vehicle will continue to
periodically sound the horn and flash the lights in a distinctive pattern. This series of
actions serves to foil the thief and to assist in the prompt recovery of the vehicle. The car
may be returned to normal operating service by simply calling the assigned paging
telephone number and entering the identification number of the legitimate owner of the
vehicle plus the alarm reset code.

**FIGURE 2** is a detailed system connection and flow diagram of the preferred
embodiment of the invention represented by **FIGURE 1**, disclosing the connections and
signal flow details of the system blocks, details of the interaction between the system
elements and details of each sub-element of the vehicular interface system. Antenna 1 is
normally an integral part of paging receiver 2. Decoder 3 can be an on-board decoder of
the paging receiver as in most commercial pagers or a separate unit. Antenna 1, paging
receiver 2 and decoder 3 are shown separately in this figure to disclose the signal flow and
interaction of the elements comprising the pager and decoder unit which are shown as a
single block in **FIGURE 1**. Such description is intended to enable one skilled in the art to
utilize elements 1, 2 and 3 separately if so desired.

The paging receiver 2 receives the paging signal. Paging receiver 2 and decoder 3
are configured for receiving and decoding a standard FSK (Frequency Shift Keyed) signal.
although they can also be configured for other types of paging signaling that may be employed. The FSK signal from antenna 1 and paging receiver 2 provides the encoded binary baseband signal 5 to the paging decoder 3. This implementation of the system of the invention employs a direct conversion receiver designed to receive data on one channel. Multichannel receivers can be substituted in the same configuration as single channel paging receiver 2 if desired. The data rate, modulation frequency deviation, and transmitter-receiver frequency offset is such that the signal power lies within +/- 10kHz of the Local Oscillator (LO) frequency of receiver 2. In the direct conversion receiver 2, the LO frequency is ideally equal to the transmitted carrier frequency. When the two are mixed, the modulating signal is the result. The encoded baseband signal 5 is the modulating signal. Decoder 3 receives the encoded baseband signal 5 from receiver 2, decodes it, checks and corrects errors, determines that the programmed Receiver Identification Code (RIC) is correct, stores the received data in the built-in Random Access Memory (RAM) buffer of the decoder 3, and communicates the data to the microcontroller 4. Decoder 3 minimizes power consumption by turning off the receiver 2 periodically via the Receiver Control line that is labeled Receiver Control in the flow line portion of FIGURE 2 and located between decoder 3 and receiver 2. Decoder 3 also provides "real time" timing for the system as needed and a built-in Electrically Erasable Programmable Read Only Memory (EEPROM) for storage of critical data in the event of power loss. In this embodiment of the invention, the encoded baseband signal 5 conforms to the internationally recognized CCIR Radio Paging Code #1 commonly referred to as POCSAG (Post Office Code Standardization Advisory Group) encoding. Any other accepted standard could be substituted in the same configuration as FIGURE 2 by simply substituting the receiver 2 and decoder 3 while maintaining the same scheme and other elements of FIGURE 2.

The heart and uniqueness of the system, often described as the "personality" of the system of the invention, is defined by microcontroller 4 and its embedded, modular, open and accessible programmable software instructions 29. The programmable software 29 used in the system of the invention, also referred to as source code, which is converted to object code, imparts instructions to microcontroller 4 and governs the system of the
invention according to the directives it contains. The programmable software 29 infrastructure used for the system of the invention is attached with comments as further disclosure to allow anyone skilled in the art to recreate the system of the invention without undue effort and customize it as desired.

Microcontroller 4 communicates with decoder 3 to acquire the decoded POCSAG data and compares it with what it is programmed to expect. If the data communicated to microcontroller 4 is correct per embedded software 29 instructions in the microcontroller 4, it executes the corresponding programmed action. Actions that require interaction with the vehicle subsystems are communicated to the logic line to relay interface unit 6 which performs the role of an interface for the vehicular systems control devices shown as relays 7, 8, 9, 11, 13, 14 and 18 in FIGURE 2. After weighing the pros and cons of using other types of vehicular systems control devices, in this preferred embodiment, heavy duty vehicular relays are employed for controlling the vehicular bsystems. Other devices, such as solid state vehicular systems devices could be substituted in lieu of the electromechanical relays 7, 8, 9, 11, 13, 14 and 18 within the same scheme. All holding coils (energizing electromagnetic coils) for relays 7, 8, 9, 11, 13, 14 and 18 are labeled HC in FIGURE 2. All contacts of the relays have a parallel bypass capacitor C to smooth the electrical make and break of the contacts and related current flow and interruption. The logic to relay interface 6 includes internal means of providing further smoothing of the make and break function of the contacts of relays 7, 8, 9, 11, 13, 14 and 18, minimizing related sparking that may cause pitting of the contacts over a period of time.

Relay 7 is reserved for activating any ready made vehicular installed loudhailer unit (also known as a megaphone) with a built-in verbal announcement, providing direct warning to the public during the getaway foiling activation of the system of the invention, that the vehicle is stolen and police need to be notified. Relay 7 is a single pole single throw device (SPST) with normally open contacts. Relay 8 is a double pole single throw device (DPST) with normally open contacts, although it can also be a SPST relay if so desired. One set of the contacts of relay 8 as shown, controls the hazard lights of the vehicle in which the system of the invention is installed. In special cases, such as with the
1995 Oldsmobile Aurora, there may be two hazard light control lines or it may be desirable to program the software to provide a different pattern for each hazard line or use an extra set of contacts to turn on the interior lights or activate another vehicular device. In such cases, the second pair of contacts of relay 8 can be used to control the second control line of the hazard lights or another vehicular device.

Relays 7 and 8 are both activated with the start of the getaway theft foiling programmed sequence during which they maintain a steady closure of their contacts. In FIGURE 2, the connections to the hazard lights are made through control lines 20 of relay 8 and connections to secondary hazard lights or to another vehicular device are made through control lines 21. Control lines 19 from relay 7 are used to operate a vehicular loudhailer or similar device as is desired.

Relay 11 controls the operation of the fuel supply system, specifically the fuel pump control line or electric supply line to the fuel injector banks of the vehicle. In its de-energized state, its contacts are closed and the normal electrical control line to the fuel supply system of the vehicle is not interrupted. When energized it interrupts the normal operation of the fuel supply of the vehicle at points 22 and 22A per programmed software instructions of microcontroller 4 until the stolen vehicle is incapacitated and cannot be used for a getaway.

Ignition status sensor line 30 provides information to microcontroller 4 as to whether the ignition is on or off. Relay 18 controls the ignition line of the vehicle. In the de-energized state of relay 18, the ignition control line of the vehicle is not interrupted because relay 18 has normally closed contacts. However, during a car theft getaway situation when the foiling mode of the Vindicator is activated, relay 18 is instructed by microcontroller 4, to open its contacts and interrupt the ignition line to prevent the vehicle from starting once the fuel starvation has incapacitated the engine and ignition is turned off. Turning off the ignition, to stop all the distressing events in the car after the system of the invention is activated in the getaway foiling mode, would be a normal reaction for the thief. Once the vehicle is incapacitated to the point of a slow speed, any attempt to turn on the ignition will activate relay 18 and interrupt the ignition line, preventing the restart of
the engine. Software 29 is currently configured to also effect an interruption of the ignition system once the vehicle is slowed down to a very low “crawling” speed. Any attempt to turn the ignition on will be sensed through the ignition sensing line 30 and ignition status reporting network 31, causing microcontroller 4 to command relay 18 to open its contacts and interrupt the ignition. This provides safety from the standpoint of avoiding a car engine remaining in a running state, even when moving at a very low speed. This ignition interrupting state continues until a reset mode is initiated by the legitimate owner of the vehicle to deactivate relay 18 and thus close its contacts when ignition is required to restore power to the ignition system of the vehicle. The connections to effect an interruption of the ignition are made at points 23 and 24. In a situation when the vehicle is unattended, the legitimate owner can access the vehicular system of the invention by calling a designated pager number, then enter the private user identification code, followed by dialing a specific code for invoking the unattended protection mode of the vehicle. In this case, any attempt to turn the ignition on will be sensed through the ignition sensing line 30 and ignition status reporting network 31, causing microcontroller 4 to command relay 18 to open its contacts and also start a cycle of enabling relays 7, 8, 9, 11, 13 and 14 to respectively activate the loudhailer if one is installed, the hazard lights, the headlights, fuel control shutdown and the horn. This occurs every time an attempt is made to turn on the ignition or to “hot wire” the vehicle in an attempt to steal and drive it away. This ignition interrupting state continues until a reset mode is initiated by the legitimate owner of the vehicle to deactivate relay 18 and thus close its contacts. The reset mode also deactivates all other relays.

The system of the invention, through minor changes in its programmed software 29 instructions, can be made to react differently or with different timing sequences or invoke other modes of shut down if so required, without necessitating hardware changes. As an example, the instruction to disable the ignition when the vehicle is incapacitated to a very slow speed by lack of fuel supply, can be changed by removing or adjusting the related programmed instructions. However, from a safety standpoint, the preferred embodiment of the invention draws attention to the stolen vehicle and effects a gradual
shut down of the fuel supply and the ignition finally. Thus effectively preventing any use of the vehicle by the criminal for a getaway.

Relay 14 controls the electrical lines that activate the horns of the vehicle in which the system of the invention is installed. When relay 14 is energized per instructions of the microcontroller 4, its contacts bypass the normally open contacts that control the horn and cause the horns to sound. This allows the energizing of the horn in a specific programmed pattern during a theft and attempted getaway situation. In the software 29 program disclosed for the preferred embodiment of the invention, an SOS (three short on/off commands followed by three longer on and off commands, followed by three on/off commands analogous to a “dit dit dit - dah dah dah - dit dit dit” morse signal) pattern for the horn is programmed and thus the horns produce an SOS pattern to draw public attention and discourage the criminal. SOS is a universally recognized sign of distress. Other patterns of horn activation can be easily implemented without hardware changes by modifying the accessible modular software 29 program statements attached in the program portion pertaining to the horn.

Relay 9 and relay 13 are used to control the high beam lights of the vehicle during the getaway foiling mode activation of the system of the invention. In their de-energized mode, relay 9 and relay 13 allow normal operation of the high beam of the vehicle. However, during the getaway foiling mode activation of the system of the invention, relay 9 bypasses the vehicle high beam switch system 10 of the lights and the normally closed contacts of relay 13 are opened and closed according to the pattern programmed. Such pattern is an SOS pattern command in the preferred embodiment of the invention.

When the microcontroller 4 is inactive, it is programmed to shut itself down to minimize power consumption and possible Radio Frequency Interference (RFI).

The 12 Volt nominal supply voltage 25 of the vehicle is connected to the vehicular system of the invention through power line wire 26 which includes an inline fuse and is connected to voltage regulator 27 and. Voltage regulator 27 provides the system power of 3.3 Volts for the vehicular system of the invention. specifically to paging receiver 2, decoder 3 and microcontroller 4. Logic line to relay interface unit 6 in FIGURE 2 is
connected to the fused vehicular nominal 12 Volt DC power 25 and provides control voltage to the holding coils of relays 7, 8, 9, 11, 13, 14 and 18.

The attached embedded, modular accessible programmable software approach for the Vindicator vehicular system of the invention, shown as software 29 in FIGURE 2, is designed to provide a wide latitude of customization without necessitating hardware changes.

Software 29, among other capabilities, provides security to assure that:

- An activity is not accidentally triggered by a random communication.
- Once triggered, the system of the invention cannot be disabled by anyone that does not have the legitimate owner's personal identification number.

The disclosed embedded open, accessible and modular programmable software 29 further provides the following features and means:

- Properly formatted communication with decoder 3 and its internal subsystems (Random Access Memory, EEPROM, and Timers).
- Microcontroller 4 remains in a dormant or inactive state to minimize power consumption and radio frequency interference (RFI) until decoder 3 indicates that a communication for the specific Vindicator vehicular system concerned has been received or a control function must be performed. The vehicular system of the invention, therefore, uses a low amount of power when during an inactive standby state.
- Initial factory setup for the specific desired configuration and testing.
- Entry of a specific user personal identification number upon installation in a vehicle.
- Visual and audible verification to the customer of the receipt of a valid or invalid personal identification number.
In the case of an invalid personal identification number entry during the initial setup following installation of Vindicator in a vehicle, the system resets and awaits a new initial personal identification number entry.

Determination of a received identification or data error and rejection of an erroneous communication

Determination of the required action to be taken upon entry of the desired foiling mode. Seven standard modes can be provided by programmable software 29 which allows expansion to 99 modes through program adjustments. The seven modes and their respective codes shown as digits following the word Mode, are as follows:

- Mode 0 - Factory Setup Mode, allowing factory setup and testing of the Vindicator. This mode is not available to non-factory users.

- Mode 1 - Getaway Foiling Mode, to enable the getaway foiling mode. In this mode, the high beam is flashed in a programmed pattern, horns sound in a programmed pattern, a vehicular loudhailer (if one is installed) is activated and will provide verbal loud messages to the public, the fuel supply to the vehicle is progressively reduced and at minimum programmed fuel supply state, ignition is finally disabled and restarting ignition is then disabled.

- Mode 2 - Reset Mode, to reset and deactivate the Vindicator after enabling a protective mode.

- Mode 3 - Unattended Protective Mode, to enable the unattended protective mode. In this mode, ignition is disabled by the ignition sensing circuit and by the ignition control relay as already described, every time an attempt is made to turn it on. In addition, all the other relays are activated providing a disabling of the fuel supply and drawing attention to the vehicle as in Mode 1. For safety reasons, Mode 3 can be activated only if the ignition is off already, as in the case of a parked vehicle. This prevents inadvertent disabling of the ignition.
while the vehicle is being driven at traffic speeds. Mode 3 cannot be activated during Mode 1.

- Mode 5 - Initialization Mode, to enable re-initialization and entry of a new identification code

- Mode 6 - Data Transmission Mode, to optionally enable transmission of data from vehicular sources external to the system of the invention. Such data is connected to the external data input port 32 described under FIGURE 4 pertaining to the second embodiment of the invention wherein a transceiver (two-way pager) is employed over a two-way paging network in lieu of a receiving pager operating over a one-way paging network. The open and accessible software 29 can accommodate program adjustments to adapt the application of this mode to the transmission of mobile data, GPS (Global Positioning System) information to indicate the exact coordinates of the vehicle and other desired information.

- Mode 7 - Reset Data Transmission Mode, to turn off the optional transmission of data from external data sources in the vehicle. This mode too pertains to the second embodiment of the invention in which a two-way pager is utilized over a two-way paging network.

User set modes can be activated by dialing one of the Mode digits, but only after entry of a valid identification code corresponding to the identification code which is programmed during the initialization of the vehicular system of the invention. Modular and accessible programmable software 29 can be adjusted to add further logical modes within the infrastructure of the hardware and the basic architecture of software 29.

- Implementation of the required action per instructions of the programmable software 29 program in the preferred embodiment of the system (which can be customized by program adjustments) is as described below:

1. Activation of the alarm sequence as follows:
• Hazard lights on.

• Then horn and bright lights flash SOS seven times or as programmed

• Then horn and bright lights continue flashing SOS, while the fuel flow from the fuel pump is slowly reduced to near zero. With minor modifications of the programmable statements of software 29, after an initial progressive shut down, Vindicator can completely shut down the fuel supply of the vehicle and also open the ignition line.

• During a getaway foiling mode, fuel supply to the vehicle engine is reduced by the system of the invention by control of the total time the fuel pump or fuel injectors is energized as opposed to long off times during the later stages of progressive fuel reduction. This avoids surging of the vehicle as opposed to just losing power.

• Then the alarm shifts to a shutdown state designed to save vehicular battery power but at the same time to attract attention. The horn will sound and the bright lights will flash SOS at regular intervals.

2. Remote reset of the vehicular system of the invention to a non-alarm state by calling the vehicular system of the invention from anywhere in the world with phones, entering a personal identification number and a reset mode code number.

• Maintains the state of the system at the time power is lost and restores the vehicle to that state when power is restored.

• Takes control of the high-beam lights so that the thief cannot turn the high beam lights on and off during a getaway foiling condition

• The customer identification number is set in software 29 at 9 digits long plus 1 digit for the action mode desired. This number of digits was chosen because it is the length of the Social Security Number which most people remember. At the same time. The 9 digit identification
code is long enough to provide excellent security against accidental or fraudulent activation.

The initialization process is carried out after installing Vindicator in a vehicle. First, the assigned pager number is called by telephone, causing a beep on the phone. Thereupon, the proprietary user identification number is entered. When the vehicular Vindicator system of the invention receives the call resulting from the paging, the vehicle lights produce three short flashes and the horn produces three short honks. To finalize initialization, the pager is called a second time and at the sound of the phone beep, the proprietary identification number is re-entered. If the Vindicator receives the same number as it received the first time, it will sound three beeps and three flashes. If the second number received is different than the first number received, the Vindicator will cause an SOS pattern light flashing and SOS horn signal before resetting initialization and return to the very first initializing step. For future change of the proprietary identification number, the assigned pager number is entered, followed by the existing valid identification number, followed by the digit 5 for Mode 5 (Re-initialization).

The standard nine digit code can be used for fleet control or vehicle group control by assigning common nine digit identification numbers or partially common numbers with sequenced last position digits for designating specific fleet vehicles. This is easily accomplished during the above described initialization process. However, the openly accessible programmable modular software 29 is designed to be custom configured for fleet control without requiring hardware changes. As an example, an optional scheme can be programmed requiring the entry of additional four digits, after entering a nine digit identification code. The extra four digits can be specifically implemented for fleet use, allowing the security control of up to 9,999 fleet vehicles in groups or individually. Such applications of the system of the invention would allow shutting down a whole fleet, such
as at car dealerships during nights and weekends, or a fleet of rental vehicles in the parking lot.

In fleet security control applications utilizing the above example of a thirteen digit identification code, the unattended vehicle theft foiling code would be entered after entry of the thirteen digits for preventing the theft of parked, unattended vehicles by disabling the ignition system of the vehicles being protected. In the same example, the vehicular system of the invention would also allow entry of a getaway foiling code after entering the thirteen digit fleet for all or individual fleet vehicles when the code for foiling a getaway is employed after entering the thirteen digits. Other modes are similarly accessed. The openly accessible modular programmable software 29 easily allows the programming of a variety of schemes for fleet managers. As a further description of the example using a thirteen digit scheme for group control in vehicle fleets, the additional four digits group code for fleet control can be programmed as the digits 1000, so that up to 999 fleet vehicles starting with the digit 1 are addressed as one group when the digits 1000 are entered after entry of the nine digit identification code. Thus the standard nine digit code implemented in software 29 can be employed for fleet control schemes or optional numerical schemes may be utilized by adjustments of the standard nine digit scheme. For example, the statement in software 29 pertaining to the number of required digits for the proprietary identification code can be changed to thirteen instead of nine. This change will require an entry of thirteen digits before a desired mode can be entered. The standard nine digit identification code and number of valid digits required for protecting fleet or individual vehicles control can already be designated as desired. The system, as is, already allows fleet or individual vehicles to be programmed during the initializing process.

FIGURE 3 is a disclosure of the actual detailed, down to component level, electrical schematic of the preferred embodiment of the system of the invention, disclosing details of all the ancillary components related to the system blocks and the total circuitry of the preferred embodiment of the system of the invention. The theory of operation of the system, its subsystems and interaction of the subsystems are disclosed under the description of FIGURE 2. The following additional description is intended to allow
anyone skilled in the art, without any guessing or trial, to duplicate the preferred embodiment of the invention.

In FIGURE 3, the paging receiver PR is built around a universal Philips UA 2080 paging receiver unit or an equivalent from Motorola and other sources. The choice of a universal Philips unit is intended to allow easy adaptation of the system of the invention in Europe and elsewhere in the world. The designation IC of an item in FIGURE 3 indicates it being an integrated circuit chip. Decoder IC4 is a Philips PCD5003 decoder chip or equivalent from Motorola and other sources. The decoder chip IC4 can be obtained in combination with the paging receiver PR as one module. IC1 is a Philips 83CL51 microcontroller or equivalent from Intel and other sources. IC3 is a Linear Technologies LT1086-3.3 voltage regulator or its equivalent with a 3.3 Volt direct current (DC) output providing the system power for the paging receiver PR, decoder IC4 and microcontroller IC1. Logic interface IC2 is an Allegro ULN2803 chip or equivalent. It is connected to the 12 Volts DC nominal vehicle voltage of the vehicle and controls the operation of control relays RY1, RY2, RY3, RY4, RY5, RY6 and RY7. IC2 provides a logic line interface between microcontroller IC1 and the relays RY1, RY2, RY3, RY4, RY5, RY6 and RY7.

Other FIGURE 3 elements include capacitors, resistors and inductor with ratings as shown. These components can be readily purchased from any electronics supply house, including the Radio Shack chain. C1 and R1 capacitor and resistor respectively provide a network for the reset timing pulse for microcontroller IC1 while resistor R4 is a pullup resistor for that circuit for compensating for variations in current. Capacitors C12, C13 and C14 provide bypassing for switching transients of IC1, IC4 and Paging receiver PR. R3 is a pullup resistor for the serial clock line and makes output adjustments for variations in current to the decoder integrated circuit IC4. Capacitors C6 and C9 are filters for the vehicle electrical power input line to voltage regulator IC3. Capacitors C7 and C8 are filters for the output side of voltage regulator IC3. C7 is for filtering the higher power, lower speed transients, while C8 is for filtering higher speed transients. CG is the common ground for the analog and digital grounds shown in FIGURE 3 with different symbols for analog and digital grounds. C4 is a second bypass capacitor for IC1. Resistor R6, inductor L1 and zener diode D1 are for suppressing voltage transients on the
electric power line from the vehicle. C5 is a line bypass capacitor for high power, high voltage transients on the electric power line of the vehicle. 12.0 Megahertz crystal Y1 and capacitors C2 and C3 interacting with microcontroller IC1 constitute an oscillator circuit providing a clocking signal for IC1. 76.8 Kilohertz crystal Y2, resistor R5 and capacitor C11 interacting with decoder IC4 constitute an oscillator circuit providing a clocking signal for decoder IC4. Resistors R7, R8, R9, R10, R11, R12, R13, and R14 are pullup resistors on the control lines from microcontroller IC1 to logic line interface IC2. Jumper JMP1 is a provision for testing. Capacitors C20, C28, C16, C15, C30, C21 and C29 are bypass capacitors for the contacts of relays RY6, RY4, RY2, RY1, RY3, RY5, and RY7 respectively. The number designations on the integrated circuits, such as on IC2, represent pin numbers on the chip. As an example, pin 17 on IC2 is connected to relay RY7, whereas pin 18 on the same integrated circuit is free and can be used for driving another relay if so desired.

As mentioned, one of the relays, RY5 in FIGURE 3, is reserved for operating a vehicular loudhailer (megaphone) audio alarm, preferably one with a 'canned', prerecorded repeating verbal alarm such as, “This is a stolen vehicle. Call the police”. The system of the invention is designed to control such a device, because it represents an additional means of helping locate a stolen vehicle. Such a loudhailer is a separate controlled vehicular subsystem and somewhat outside the scope of the invention. It can be purchased from a number of sources or custom constructed by a combination of a voice recording solid state chip with playback capability such as chip number ISD-VM1110A from Information Storage Devices of San Jose, California. The chip comes with full instructions on how to apply it and may be combined with any suitable megaphone including the Radio Shack Catalog Number 32-2037 Powerhorn.

Although the functions of the other relays have been discussed under a description of FIGURE 2, the specific functions of relays in FIGURE 3 will be further discussed. The 51ND12-N 12 Volt relays are made by Fujitsu. Other 12 Volt equivalent vehicular relays may be used. Relay RY7 is used for controlling the hazard lights. Relays RY5 and RY7 both activated with the start of the getaway theft foiling programmed sequence during which they maintain a steady closure of their contacts. When the hazard light control lines
of the vehicle are powered by relay RY7, the built-in flasher of the vehicle produces flashing as in any emergency.

Relay RY6 controls the fuel supply to the engine by controlling the operation of the fuel pump or fuel injectors. In its de-energized state its contacts are closed and the electrical supply to the fuel controlling system of the vehicle is not interrupted. When relay RY6 is energized, its contacts interrupt the electrical supply line to the fuel pump or fuel injector bank per programmed software instructions of the microcontroller IC1 until the stolen vehicle is incapacitated and cannot be used for a getaway.

Relay RY4 controls the ignition line of the vehicle. In the de-energized state of relay RY4, the ignition of the vehicle is not interrupted because relay 18 has normally closed contacts. However, during a car theft getaway situation when the foiling mode of the Vindicator is activated, relay RY4 is instructed by microcontroller IC1, to open its contacts and interrupt the ignition when the fuel supply has reached its programmed minimum stage and turning on ignition is attempted after it is turned off. This will prevent the vehicle from starting once the ignition is turned off and engine incapacitation stage is reached for the engine. Engine incapacitation in the context of the invention is reached when the fuel supply has progressively been interrupted so that the vehicle cannot be used for a getaway. Transistor Q1, resistor R15 and resistor R16 form an ignition status reporting network. R15 is a collector loading resistor. Resistor R16 is a bias resistor on the base of transistor Q1. When Vindicator is in the getaway foiling mode and the fuel supply has progressed to the minimum programmed state, when ignition is turned off, the vehicle engine will not be allowed to restart. The ignition control system is also programmed so that ignition is finally interrupted when the vehicle has reached a minimum fuel supply point, representing minimum speed. To accomplish this, the ignition status reporting network is required, so that when ignition is turned on at the point of minimum fuel supply, then resistor R16 located at the base of transistor Q1 goes to a low state at point 13 on microcontroller IC1. This in turn results in microcontroller IC1 commanding logic line interface IC2 to energize relay RY4. This results in relay RY4 opening its contacts and interrupting ignition. After a period where the vehicle fuel supply has been at
its minimum, microcontroller IC1 is programmed to disable the ignition system by maintaining a status of ordering relay RY4 to open and interrupt ignition any time it senses that an attempt is being made to turn ignition on. This state remains until the legitimate owner of the vehicle commands a reset activation mode by calling the pager number, entering a valid identification code and entering a code for the Vindicator to go to the reset mode. In a situation when the vehicle is unattended, the legitimate owner can access the vehicular system of the invention by calling a designated pager number, then entering the private user identification code, followed by dialing a specific code for invoking the unattended protection mode of the vehicle. In this case, relay RY4 gets energized per programmed software instructions of the microcontroller IC1 every time the ignition status sensor line reports an attempt to turn on ignition. When energized by such an attempt, relay RY4 interrupts the ignition line, preventing starting or driving of the car when it is unattended and the unattended protection mode is activated. This action also starts a cycle of activation of all other relays to draw attention to the vehicle and disable the fuel supply as described for FIGURE 2.

Relay RY3 controls the electrical lines that activate the horns of the vehicle in which the system of the invention is installed. When relay RY3 is energized per instructions of the microcontroller IC1, its contacts bypass the normally open contacts that control the horn and cause the horn to sound. This allows the energizing of the horn in a specific programmed pattern during a theft and attempted getaway situation.

Relay RY1 and relay RY2 are used to control the high beam of the vehicle during the getaway foiling mode activation of the system of the invention. In its de-energized mode, relay RY1 and RY2 allow normal operation of the high beam of the vehicle. However, during the getaway foiling mode activation of the system of the invention, relay RY1 bypasses the normal high beam control system of the lights and the normally closed contacts of relay RY2 are opened and closed according to the pattern programmed. Such pattern is an SOS pattern command in the preferred embodiment of the invention.

With all the components and their functions identified for the full schematic of FIGURE 3, a further detailed disclosure of the system of said schematic follows. As
mentioned earlier, the paging receiver **PR** provides a demodulated binary baseband signal to the paging decoder **IC4**. Decoder **IC4** decodes the baseband data received from the receiver, determines that it has the correct RIC (Receiver Identification Code), stores the data in its internal RAM (Random Access Memory), and notifies microcontroller **IC1** that data is waiting by making the "Interrupt" output LOW. Receiver **PR** is turned off by the decoder while the controller is reading the received data. The data is passed to microcontroller **IC1** over an industry standard Inter-Integrated Circuit (**I2C**) Bus (shown and identified in the flow chart in **FIGURE 2** between Decoder 3 and microcontroller 4). Decoder **IC4** in **FIGURE 3** can be reset by microcontroller **IC1** as required. Crystal **Y2**, **R4**, and **C11** form an oscillator, operating at 76.8 KHz, to provide the decoder clock. Pin 8 of **IC4** input allows decoder **IC4** to interface to microcontroller **IC1** during factory testing. To minimize power consumption, decoder **IC4** periodically turns off receiver **PR** for short periods of time.

When microcontroller **IC1** is ready to receive the data, it turns off decoder **IC4** via the "Decoder On" input (**IC4** pin 3), and reads the data from the internal RAM of decoder **IC4**. It then compares the data to the expected customer identification number, and code for the mode desired and, if correct, performs the requested mode. To provide long term storage of some data, microcontroller **IC1** makes use of extra internal EEPROM in decoder **IC4**. This data is thus preserved in the event power to the Vindicator system is interrupted.

Microcontroller **IC1** provides overall control of the Vindicator system. It decodes data received from the paging decoder **IC4**, determines the course of action via the embedded program and provides control via the output lines to the logic line interface relay drivers **IC2**. Microcontroller **IC1** uses a standard 12.0 MHz crystal oscillator circuit **Y1**, **C2**, and **C3** for a clock. Resistor **R1** and capacitor **C1** provide a power up reset for microcontroller **IC1**. A Phillips Semiconductor 83CL51 microcontroller used as **IC1** provides 4 kilobytes (4Kb) of internal Electrically Programmable Read Only Memory (**EPROM**) for storage of the program and 128 bytes of internal Random Access Memory (RAM) for temporary storage of data and variables. The 83CL51 permits its internal
processor to be stopped until action by the processor is required. Stopping the internal processor minimizes power consumption and electromagnetic noise.

Logic line to relay interface IC2 provides the interface between microcontroller IC1 and the relays. IC2 accepts a standard logic level signal from microcontroller IC1 and provides 12 Volt DC nominal vehicle power control to relays RY6, RY4, RY2, RY1, RY3, RY5, and RY7. IC2 also internally provides transient suppression for the relay coils to protect the output of the driver from switching transients.

A Linear Technologies LT1086 or equivalent voltage regulator is used as IC3. The voltage regulator IC3 reduces and regulates the supplied vehicular voltage. The supplied voltage from the vehicle is standard around 12 VDC (Volts Direct Current) +/- 20%. This voltage is reduced by IC3 to 3.3 VDC +/- 1%, which is the operating voltage of the receiving, decoding, and control circuitry. Diode D1 provides transient protection for the regulator. Capacitors C5, C6, C9, and inductor L1 filter the incoming vehicle power. Capacitors C7 and C8 act to filter out transients appearing at the output of voltage regulator IC3.

Paging decoder IC4 is a Phillips Semiconductor PCD5003 or equivalent. It decodes the received POCSAG encoded baseband paging signal, checks the RIC (Receiver Identification Code), notifies microcontroller IC1 of a valid received message, stores the received data, and provides Electrically Erasable and Programmable Read Only Memory (EEPROM). Decoder IC4 receives the POCSAG encoded data from the paging receiver PR, removes the synchronization bits, checks the paging system unit identification code, checks for and corrects any errors, stores the data in its RAM, and notifies microcontroller IC1 that the data is waiting to be read. Decoder IC4 notifies microcontroller IC1 when the last of the data has been read. The crystal circuit comprised of Y2, R5, and C11 provides the clock for decoder IC4.

Relays RY6, RY4, RY2, RY1, RY3, RY5, and RY7 provide interface devices to various vehicle subsystems being controlled by the Vindicator system of the invention.

FIGURE 4 is a block diagram of a second embodiment of the invention operating over a two-way paging network, comprising a transceiver and decoder unit 1, a
microcontroller 2 and a vehicular interface 3. The principal difference between this second embodiment and the preferred embodiment of the system of the invention is the substitution of a paging transceiver (transmitter/receiver) instead of a receiver as in the preferred embodiment of the invention. The transmitter portion of the transceiver in block 1 is under the control of microcontroller 2 and provides confirmation to the initiator of a theft foiling mode that the Vindicator has received and decoded a valid code entry. The transmitter portion of paging transceiver 1 can also be employed to transmit various types of data, such as GPS (global positioning system) information which would provide the exact location coordinates of the vehicle.

From an application standpoint, this second embodiment of the invention described in FIGURES 4, 5 and 6, aside from providing the transmission of an acknowledgment that a valid code is received and data transmission from external sources, is essentially identical to the preferred embodiment described in FIGURES 1, 2 and 3. Thus, the application of the second embodiment of the invention described in FIGURES 4, 5 and 6 is likewise the prevention of the theft of unattended fleet vehicles or individual vehicles, and to aid in the recovery of fleet vehicles or an individual stolen vehicle that is being driven away after a theft or carjacking. However, from a circuit and user standpoint, the preferred embodiment of the invention described in FIGURES 1, 2 and 3 employs a receiver alone approach. Said receiver alone approach is currently deemed the preferred embodiment because, the second embodiment of the invention employing two-way paging, is currently in its early stages, costs more, is limited with respect to sources of the transceiver unit and is still limited with respect to areas of coverage around the globe. However, the aforementioned limitations may change in the future and make the second embodiment of the invention more appealing.

Reverting to the block diagram of FIGURE 4 for the second embodiment of the invention, after a vehicle is stolen, the legitimate owner, using the public telephone system, calls an assigned telephone number and enters the customer defined identification number plus a code to specify the operating mode desired. For the second embodiment of the invention, a Mode 6 (Code 6) would cause the transmitter portion of paging transceiver 1 to transmit other desired external data. In the case of a criminal escaping with a stolen
vehicle, the Mode 1 (Code 1) used will enable the getaway foiling mode. The call made will result in a paging signal being sent over an existing paging network and received by the receiver portion of the paging transceiver and decoder in block 1, said decoder in block 1, being an integral portion of most commercial pager modules. The identification dialed by phone plus the code for the desired operating mode entered by phone is decoded by the decoder in block 1 and conveyed to the microcontroller 2 interconnected with the paging receiver and decoder in block 1. This will result in microcontroller 2 activating the transmitter portion of block 1 to transmit an acknowledgment that a valid identification code has been received. Microcontroller 2 will then also cause the vehicular interface 3 to activate a series of distinct alarms, followed by a gradual loss of the fuel flow to the engine of the vehicle, culminating in the fuel system being gradually and effectively disabled so as to prevent the escape of the criminal using the vehicle for a getaway, without posing a hazard to other motorists. Should the engine be shut off after the incapacitation of the engine whereby the vehicle is only capable of limited, low speed, the vehicle will not start due to the disablement of the ignition system as programmed into the system of the invention. Also, the software instructions cause the ignition to be turned off when a minimum fuel supply point is received. Pursuant to the main alarm and incapacitation event, the vehicle will continue to periodically sound the horn and flash the lights in a distinctive pattern. This series of actions serves to foil the thief and to assist in the prompt recovery of the vehicle. The car may be returned to normal operating service by simply calling the assigned paging telephone number and entering the identification number of the legitimate owner of the vehicle plus the alarm reset code. Upon decoding an identification code corresponding to the identification code programmed during the installation initialization process of the system of the invention, the decoder portion of the paging transceiver sends back an acknowledgment to the transmitter portion of the transceiver in block 1. The transmitter portion of block 1 then transmits a preprogrammed acknowledgment over the two way paging network, confirming receipt of a valid signal by the vehicular system of the invention. Upon entry of an optionally configured Mode 6 (Code 6), external data transmission is enabled to employ the transmitter portion of paging transceiver 1 to transmit other data, including data about the vehicle and its location.
FIGURE 5 is a detailed system connection and flow diagram of the preferred embodiment of the invention represented by block diagram FIGURE 4, disclosing the connections and signal flow details of the system blocks, details of the interaction between the system elements and details of each sub-element of the vehicular interface system. Antenna 1 is an integral part of paging transceiver 2. Decoder 3 can be a portion of the paging transceiver 2, as in commercial paging transceivers, or an independent unit. In FIGURE 5 Antenna 1, paging transceiver 2 and decoder 3 are shown separately to disclose the signal flow and interaction of the elements comprising the pager and decoder unit which are shown as a single block in FIGURE 4. Such description in FIGURE 5 is intended to enable one skilled in the art to utilize elements 1, 2 and 3 separately if so desired.

The paging receiver portion of paging transceiver 2 receives the FSK (Frequency Shift Keyed) signal from the antenna 1 and provides the encoded binary baseband signal 5 to the paging decoder 3. Signal formats other than FSK can be used and in such case transceiver 2 and decoder 3 are configured for such format. The encoded baseband signal 5 is the modulating signal. Decoder 3 receives the encoded baseband signal 5 from receiver 2, decodes it, checks and corrects errors, determines that the programmed Receiver Identification Code (RIC) is correct, stores the received data in the built-in Random Access Memory (RAM) buffer of the decoder 3, and communicates the data to the microcontroller 4 which invokes the activation of the transmitter portion of paging transceiver 2. The transmitter portion of paging transceiver 2 then transmits an acknowledgment over the two-way paging network employed to confirm the receipt of an acceptable signal to initiate the foiling mode desired.

The external data input port is connected to microcontroller 4 and utilized for optionally inputting data from external sources for transmission by the transmitter portion of paging transceiver 2. The external data is conveyed by microcontroller 4 to the transmitter portion of paging transceiver 2. For such optional data transmission, microcontroller 4 also provides a control line to turn the transmitter portion of paging transceiver PTR on and off as required. External data can include information from other vehicular systems during emergencies or data from mobile terminals during normal use of
the vehicle or GPS data related to the exact location of the vehicle during emergencies and for other requirements.

Decoder 3 minimizes power consumption by turning off the paging transceiver 2 periodically via the control line between Decoder 3 and paging transceiver PTR in the flow line portion of FIGURE 5 and located between decoder 3 and transceiver 2. Decoder 3 also provides "real time" timing for the system as needed and a built-in Electrically Erasable Programmable Read Only Memory (EEPROM) for storage of critical data in the event of power loss. In this embodiment of the invention, the encoded baseband signal 5 conforms to the internationally recognized CCIR Radio Paging Code #1 commonly referred to as POCSAG (Post Office Code Standardization Advisory Group) encoding. Any other accepted standard could be substituted in the same configuration as FIGURE 5 by simply substituting transceiver 2 and decoder 3 while maintaining the same scheme and other elements of FIGURE 5.

The heart and uniqueness of the system, often described as the "personality" of the system of the invention, is defined by microcontroller 4 and its embedded programmable software instructions 29. The programmable software 29 used in the system of the invention, also referred to as source code and object code, imparts instructions to microcontroller 4 and governs the system of the invention according to the directives it contains. The programmable software 29 infrastructure used for the system of the invention is attached as further disclosure to allow anyone skilled in the art to recreate the system of the invention without undue effort and customize it as desired. The software capabilities and features, as fully described for the preferred embodiment under FIGURE 2, are identical for the second embodiment of the invention employing a transceiver PTR for all vehicle control functions.

Microcontroller 4 communicates with decoder 3 to acquire the decoded POCSAG data and compares it with what it is programmed to expect. If the data communicated to microcontroller 4 is correct per embedded software 29 instructions in the microcontroller 4, it executes the corresponding programmed action. Actions that require interaction with the vehicle subsystems are communicated to the logic line to relay interface unit 6 which performs the role of an interface for the vehicular system control devices shown as
relays 7, 8, 9, 11, 13, 14 and 18 in FIGURE 5. After weighing the pros and cons of using other types of control devices, in this preferred embodiment, heavy duty relays are employed for controlling the vehicular subsystems. Other solid state devices could be substituted in lieu of the electromechanical relays 7, 8, 9, 11, 13, 14 and 18 within the same scheme. All holding coils (energizing electromagnetic coils) for relays 7, 8, 9, 11, 13, 14 and 18 are labeled HC in FIGURE 5. All contacts of the relays have a parallel bypass capacitor C to smooth the electrical make and break of the contacts and related current flow and interruption. The logic to relay interface 6 includes inherent means (diodes) of providing further smoothing of the make and break function of the contacts of relays 7, 8, 9, 11, 13, 14 and 18, minimizing related sparking that may cause pitting of the contacts over a period of time. Microcontroller 4 also controls the transmission of data from external sources by the transmitter portion of transceiver 2 through the control lines shown in FIGURE 5 from microcontroller 4 to the transmitter data input port at point XMTR DATA of transceiver 2 and the control line from microcontroller 4 and transmitter keying control XMTR KEY of transceiver 2. Transceiver 4 can also allow direct data entry into its own serial data input port, in which case transmitter data can be directly input into the transmitter data port XMTR DATA.

Relay 7 is reserved for activating any ready made vehicular loudhailer unit (also known as a megaphone) with a built-in verbal announcement, providing warning to the public during the getaway foiling activation of the system of the invention, that the vehicle is stolen and police need to be notified. Relay 7 is a single pole single throw device (SPST) with normally open contacts. Relay 8 is a double pole single throw device (DPST) with normally open contacts, although it can also be a SPST relay if so desired. One set of the contacts of relay 8 as shown, controls the hazard lights of the vehicle in which the system of the invention is installed. In special cases, there may be two hazard light control lines or it may be desirable to program the software to provide a different pattern for each hazard line or use an extra set of contacts to turn on the interior lights or other vehicular device. In such cases, the second set of contacts of relay 8 can be used to control the second control line of the hazard lights or another vehicular device. Relays 7 and 8 are both activated with the start of the getaway theft foiling programmed sequence during
which they maintain a steady closure of their contacts. In FIGURE 5, the connections to the hazard lights are made through control lines 20 of relay 8 and connections to secondary hazard lights or to another vehicular device are made through control lines 21. Control lines 19 from relay 7 are used to operate a vehicular loudhailer or similar device as desired.

Relay 11 controls the fuel supply system to the engine through the control of the fuel pump or injector control line of the vehicle. In its de-energized state, its contacts are closed and the electrical supply to the fuel pump or fuel injectors of the vehicle is not interrupted. When energized, the contacts of relay 11 progressively interrupt the electrical supply line to the fuel pump or the injectors of the vehicle at points 22 and 22A per programmed software instructions of the microcontroller 4 until the stolen vehicle is incapacitated to very low speed and cannot be used for a getaway.

Relay 18 controls the ignition line of the vehicle. In its de-energized state, the ignition control line of the vehicle is not interrupted because relay 18 has normally closed contacts. However, during a car theft getaway situation when the getaway foiling mode of the Vindicator is activated and the engine is incapacitated by fuel starvation to its lowest programmed state, relay 18 is instructed by microcontroller 4, to open its contacts and interrupt the ignition line every time an attempt is made to turn ignition on. This prevents the vehicle from starting once the ignition is turned off. Also, at the point of minimum fuel supply, microcontroller 4 instructs the activation of relay 18 interrupting ignition and preventing turning it on. Ignition sensor status line 30 in conjunction with ignition status reporting network 31 provides the status of the ignition to microcontroller 4. When ignition is interrupted, relay 18, does not remain energized because that would leave a powered relay when the engine is stopped. Rather, ignition sensor status line 30 in conjunction with ignition status reporting network 31 sense any attempts to turn on the ignition and microcontroller 4 in such case energizes relay 18 which opens its contacts only as long as there is an attempt to turn the ignition on, preventing the ignition from being turned on. This status is maintained until a reset mode signal is sent by the legitimate owner of the vehicle and relay 18 then maintains its contacts closed and normal operation.
becomes possible. In a situation when the vehicle is unattended, the legitimate owner can access the vehicular system of the invention by calling a designated pager number, then enter a valid user identification code, followed by dialing a specific code for invoking the unattended protection mode of the vehicle. In this case relay 18 gets energized per programmed software 29 instructions of the microcontroller 4 and interrupts the ignition line every time an attempt is made to turn on ignition. This prevents starting or driving of the car when it is unattended and in the unattended protective mode. Ignition sensor status line 30 in conjunction with ignition status reporting network 31 provides the status of the ignition to microcontroller 4. Activation of this mode also starts a cycle of activation of all the other relays to disable the fuel supply and draw attention to the vehicle as already explained under a description of the available modes.

The system of the invention, through minor changes in its programmed software 29 instructions, can be made to react differently or with different timing sequences or invoke other modes of shut down if so required. However, from a safety standpoint, the preferred embodiment of the invention with standard software instructions 9 draws attention to the stolen vehicle and effects a gradual shut down of the fuel pump, followed by disabling the ignition at minimum fuel point, thus effectively preventing any use of the vehicle by the criminal for a getaway.

Relay 14 controls the electrical lines that activate the horns of the vehicle in which the system of the invention is installed. When relay 14 is energized per instructions of the microcontroller 4, its contacts bypass the normally open contacts that control the horn and make the horns to sound. This allows the energizing of the horn in a specific programmed pattern during a theft and attempted getaway situation. In the software 29 program disclosed for the preferred embodiment of the invention, an SOS (three short on/off commands followed by three longer on and off commands, followed by three on/off commands analogous to a "dit dit dit - dah dah dah - dit dit dit" morse signal) pattern for the horn is programmed and thus the horns produce an SOS pattern to draw public attention and discourage the criminal. SOS is a universally recognized sign of distress.
Other patterns of horn activation can be easily implemented by modifying the software 29 program statements attached in the program portion pertaining to the horn.

Relay 9 and relay 13 are used to control the high beam of the vehicle during the getaway foiling mode activation of the system of the invention. In its de-energized mode, relay 9 and 13 allow normal operation of the high beam of the vehicle. However, during the getaway foiling mode activation of the system of the invention, relay 9 bypasses the normal high beam control system 10 of the lights and the normally closed contacts of relay 13 are opened and closed according to the pattern programmed. Such pattern is an SOS pattern command in the preferred embodiment of the invention.

When the microcontroller 4 is inactive, it is programmed to shut itself down to minimize power consumption and possible Radio Frequency Interference (RFI).

The 12 Volt nominal supply voltage 25 of the vehicle is connected to the vehicular system of the invention through power line wire 26 which includes an inline fuse and is connected to voltage regulator 27 and. Voltage regulator 27 provides the system power of 3.3 Volts for the vehicular system of the invention to paging receiver 2, decoder 3 and microcontroller 4. Logic line to relay interface 6 is connected to the 12 Volt DC nominal vehicle power and controls power to the holding coils of relays 7, 8, 9, 11, 13, 14 and 18.

**FIGURE 6** is the down to the component implementation of the second embodiment of the invention which is identical to that of the preferred embodiment of the system with the exception of a substitution of a transceiver and its control and input lines in lieu of the receiver alone in the preferred embodiment of the invention. Thus all of the components employed in **FIGURE 3** for the preferred embodiment, aside from the substitution of a transceiver in lieu of receiver PR, remain the same. Transceiver pagers, by their inherent nature, incorporate means of transmitting a confirmation from the transmitter portion of the transceiver pager (used in two-way paging) after receipt and decoding of a valid signal. Thus, the use of a transceiver in the front portion of the Vindicator does not affect the rest of the system which would be transparent to the use of a two-way paging unit in the front portion of the system. Thus, upon substituting a transceiver pager unit already equipped for sending a confirmation signal, essentially the
same down to the component circuitry as for the preferred embodiment shown in
FIGURE 3 can be used by one skilled in the art to build the second embodiment of the
invention. Accordingly, the description that follows will principally describe most of the
elements already disclosed in FIGURE 3 plus what particularly applies to the second
embodiment that is different.

FIGURE 6 is thus a disclosure of the actual detailed, down to component level,
electrical schematic of the second embodiment of the system of the invention, disclosing
details of all the ancillary components related to the system blocks and the total circuitry
of the preferred embodiment of the system of the invention. The theory of operation of the
system, its subsystems and interaction of the subsystems are disclosed under the
description of FIGURE 5. The software functions, modes and operating sequences are as
described under FIGURE 2. The following additional disclosure is intended to allow
anyone skilled in the art, without guessing or trial, to construct the second the preferred
embodiment of the invention.

In FIGURE 6, paging transceiver PTR comprises a receiver portion and a
transceiver portion. Initial two-way paging networks operate in the 1930 to 1990 MHz
frequency range. However, any frequency range where spectrum allocation for two-way
paging is available may be used. One type of two-way pager which incorporates a
transceiver is Motorola’s Tango two-way pager for which information is available from
Motorola Paging Products Group. The designation IC of an item in FIGURE 6 indicates
it being an integrated circuit chip or a module comprising integrated circuits chips.
Decoder IC4, as in some commercial two-way pagers, can be obtained in combination
with the paging transceiver PTR as one module with internal printed circuit
interconnections. It is shown here separately as a guidance to those skilled in the art who
would prefer utilizing separate transceiver and decoder chips to construct the second
embodiment of the invention. IC3 is a Linear Technologies LT1086 voltage regulator or
its equivalent with a direct current (DC) output providing the system power requirements
for the paging transceiver PTR, decoder IC4, microcontroller IC1, and logic line interface
IC2. Logic interface IC2 is an Allegro ULN2803 chip or equivalent. It controls the 12
Volt DC nominal vehicle voltage to the holding coils of control relays RY1, RY2, RY3.
RY4, RY5, RY6 and RY7. IC2 provides a logic line interface between microcontroller IC1 and the relays RY1, RY2, RY3, RY4, RY5, RY6 and RY7.

Other FIGURE 6 elements include capacitors, resistors and inductor with ratings as shown. These components can be readily purchased from any electronics supply house, including the Radio Shack chain. C1 and R1 capacitor and resistor respectively provide a network for the reset timing pulse for microcontroller IC1 while resistor R4 is a pullup resistor for that circuit for compensating for variations in current. Capacitors C12, C13 and C14 provide bypassing for switching transients of microcontroller IC1, decoder IC4 and paging transceiver PTR. R3 is a pullup resistor for the serial clock line and makes output adjustments for variations in current to the decoder integrated circuit IC4. Capacitors C6 and C9 are filters for the vehicle electrical power input line to voltage regulator IC3. Capacitors C7 and C8 are filters for the output side of voltage regulator IC3. C7 is for filtering the higher power, lower speed transients, while C8 is for filtering higher speed transients. CG is the common ground for the analog and digital grounds shown in FIGURE 6 with different symbols for analog and digital grounds. C4 is a second bypass capacitor for IC1.

Resistor R6, inductor L1 and zener diode D1 are for suppressing voltage transients on the electric power line from the vehicle. C5 is a line bypass capacitor for high power, high voltage transients on the electric power line of the vehicle. 12.0 Megahertz crystal Y1 and capacitors C2 and C3 interacting with microcontroller IC1 constitute an oscillator circuit providing a clocking signal for IC1. 76.8 Kilohertz crystal Y2, resistor R5 and capacitor C11 interacting with decoder IC4 constitute an oscillator circuit providing a clocking signal for decoder IC4. Resistors R7, R8, R9, R10, R11, R12, R13, and R14 are pullup resistors on the control lines from microcontroller IC1 to logic line interface IC2. Jumper JMP1 is a provision for testing. Capacitors C20, C28, C16, C15, C30, C21 and C29 are bypass capacitors for the contacts of relays RY6, RY4, RY2, RY1, RY3, RY5, and RY7 respectively. The number designations on the integrated circuits, such as on IC2, represent pin numbers on the chip. As an example, pin 17 on IC2 is connected to relay RY7, whereas pin 18 on the same integrated circuit is free and can be used for driving another relay if so desired.
One of the relays, RY5 in FIGURE 6 is reserved for operating a vehicular loudhailer (megaphone) audio alarm, preferably one with a 'canned', prerecorded repeating verbal alarm such as, "This is a stolen vehicle. Call the police". The system of the invention is designed to control such a device, because it represents an additional means of helping locate a stolen vehicle. Such a loudhailer is a separate controlled vehicular subsystem and somewhat outside the scope of the invention. It can be purchased from a number of sources or custom constructed by a combination of a voice recording solid state chip with playback capability such as chip number ISD-VM1110A from Information Storage Devices of San Jose, California. The chip comes with full instructions on how to apply it and may be combined with any suitable megaphone including the Radio Shack Catalog Number 32-2037 Powerhorn.

Although the functions of the other relays have been discussed under a description of FIGURE 5, the specific functions of relays in FIGURE 6 will be further discussed. The 51ND12-N 12 Volt relays are made by Fujitsu. Other 12 Volt equivalent vehicular relays may be used. Relay RY7 is used for controlling the hazard lights. Relays RY5 and RY7 both activated with the start of the getaway theft foiling programmed sequence during which they maintain a steady closure of their contacts. When the hazard light control lines of the vehicle are powered by relay RY7, the built-in flasher of the vehicle produces flashing as in any emergency.

Relay RY6 controls the fuel supply to the engine by controlling the operation of the fuel pump or fuel injectors. In its de-energized state its contacts are closed and the electrical supply to the fuel controlling system of the vehicle is not interrupted. When relay RY6 is energized, its contacts interrupt the electrical supply line to the fuel pump or fuel injector bank per programmed software instructions of the microcontroller IC1 until the stolen vehicle is incapacitated and cannot be used for a getaway.

Relay RY4 controls the ignition of the vehicle. In the de-energized state of relay RY4, the ignition of the vehicle is not interrupted because relay 18 has normally closed contacts. However, during a car theft getaway situation when the getaway foiling mode of the Vindicator is activated, relay RY4 is instructed by microcontroller IC1, to open its contacts and interrupt the ignition when the fuel supply has reached its minimum
programmed stage and turning on ignition is attempted after it is turned off. This will prevent the vehicle from starting once the ignition is turned off and engine incapacitation stage is reached. Engine incapacitation in the context of the invention is reached when the fuel supply has progressively been interrupted so that the vehicle is moving only at a slow speed and cannot be used for a getaway. Transistor Q1, resistor R15 and resistor R16 form an ignition status reporting network. R15 is a collector loading resistor. Resistor R16 is a bias resistor on the base of transistor Q1.

When Vindicator is in the getaway foiling mode and the fuel supply has progressed to the minimum programmed state, upon ignition being turned off, the vehicle engine will not be allowed to restart. To accomplish this, the ignition status reporting network is required, so that when ignition is turned on at the point of minimum fuel supply, then resistor R16 located at the base of transistor Q1 goes to a low state at point 13 on microcontroller IC1. This in turn results in microcontroller IC1 commanding logic line interface IC2 to energize relay RY4. This results in relay RY4 opening its contacts and interrupting ignition. After a period where the vehicle fuel supply has been at its minimum, microcontroller IC1 is programmed to disable the ignition system and maintaining a status of ordering relay RY4 to open and interrupt ignition any time it senses that an attempt is being made to turn ignition on. This state remains until the legitimate owner of the vehicle commands a reset activation mode by calling the pager number, entering a valid identification code and entering a code for the Vindicator to go to a reset mode.

In a situation when the vehicle is unattended, the legitimate owner can access the vehicular system of the invention by calling a designated pager number, then entering the private user identification code, followed by dialing a specific code for invoking the unattended protection mode of the vehicle. In this case relay RY4 gets energized per programmed software instructions of the microcontroller IC1 every time the ignition status sensor line reports an attempt to turn on ignition. When energized by such an attempt, relay RY4 interrupts the ignition line, preventing starting or driving of the car when it is unattended and the unattended protection mode is activated. Also upon an
attempt being made to turn on ignition in the unattended protection mode. All other relays are activated to disable the fuel supply and draw attention to the vehicle, as already explained in a description of the available modes.

Relay RY3 controls the electrical lines that activate the horns of the vehicle in which the system of the invention is installed. When relay RY3 is energized per instructions of the microcontroller IC1, its contacts bypass the normally open contacts that control the horn and cause the horns to sound. This allows the energizing of the horn in a specific programmed pattern during a theft and attempted getaway situation.

Relay RY1 and relay RY2 are used to control the high beam of the vehicle during the getaway foiling mode activation of the system of the invention. In its de-energized mode, relay RY1 and RY2 allow normal operation of the high beam of the vehicle. However, during the getaway foiling mode activation of the system of the invention, relay RY1 bypasses the normal high beam control system of the lights and the normally closed contacts of relay RY2 are opened and closed according to the pattern programmed. Such pattern is an SOS pattern command in the preferred embodiment of the invention.

With all the components and their functions identified for the full schematic of FIGURE 6, a further detailed disclosure of the system of said schematic follows. The paging receiver portion of paging transceiver PTR provides a demodulated binary baseband signal to the paging decoder IC4. Decoder IC4 decodes the baseband data received from the receiver, determines that it has the correct RIC (Receiver Identification Code), stores the data in its internal RAM (Random Access Memory), and notifies microcontroller IC1 that data is waiting by making the "Interrupt" output LOW. Transceiver PTR is turned off by decoder IC4 while the controller is reading the received data. The data is passed to microcontroller IC1 over an industry standard Inter-Integrated Circuit (I2C) Bus (shown and identified in the flow chart in FIGURE 5 between Decoder 3 and microcontroller 4). Decoder IC4 in FIGURE 6 can be reset by microcontroller IC1 as required. Crystal Y2, resistor R4, and capacitor C11 interact with decoder IC4 to form an oscillator, operating at 76.8 KHz, to provide the decoder clock. Pin 8 of IC4 input
allows decoder IC4 to interface to microcontroller IC1. To minimize power consumption, decoder IC4 periodically turns off transceiver PTR for short periods of time.

When microcontroller IC1 is ready to receive the data, it turns off decoder IC4 via the "Decoder On" input (IC4 pin 3), and reads the data from the internal RAM of decoder IC4. It then compares the data to the expected customer identification number, and code for the mode desired and, if correct, performs the requested mode. To provide long term storage of some data, microcontroller IC1 makes use of extra internal EEPROM in decoder IC4. This data is thus preserved in the event power to the Vindicator system is interrupted.

Microcontroller IC1 provides overall control of the system including the transmitter portion of the second embodiment of the invention. IC1 decodes data received from the paging decoder IC4, determines the course of action via the embedded program instructions and provides control via the output lines to the logic line interface relay driver IC2. In the case of receipt of a Mode 6 (Code 6), microcontroller IC1 controls the activation of the transmitter portion of paging transceiver PTR and the sending of data from external sources. To accomplish this, one line from pin 39 on microcontroller IC1 in FIGURE 6 controls the transmitter on-off (control) in the transmitter portion of transceiver PTR. Another line in FIGURE 6, between pin 11 of microcontroller IC1 and the transmitter portion of transceiver PTR one sends data received from the external data input port connected to pin 2 of microcontroller IC1 in FIGURE 6. This arrangement allows this embodiment of the invention to optionally transmit desired custom external data from the external data input port of transceiver PTR per custom software instructions, such as transmission of vehicle systems data or GPS data pertaining to the position coordinates of the vehicle.

12.0 MHz crystal Y1, capacitor C2, and capacitor C3 form part of the oscillator circuit providing clocking for Microcontroller IC1. Resistor R1 and capacitor C1 provide a power up reset for microcontroller IC1. A Phillips Semiconductor 83CL51 microcontroller used as IC1 provides 4 kilobytes (4Kb) of internal Electrically Programmable Read Only Memory (EPROM) for storage of the program and 128 bytes of
internal Random Access Memory (RAM) for temporary storage of data and variables. The 83CL51 permits its internal processor to be stopped until action by the processor is required. Stopping the internal processor minimizes power consumption and electromagnetic noise.

Logic line to relay interface IC2 provides the interface between microcontroller IC1 and the relays. IC2 accepts a standard logic level signal from microcontroller IC1 and provides 12 Volt DC nominal vehicle power to control the activation of relays RY6, RY4, RY2, RY1, RY3, RY5, and RY7. IC2 also internally provides transient suppression for the relays to protect them from switching transients.

A Linear Technologies LT1086 or equivalent voltage regulator is used as IC3 to provide system power. The voltage regulator IC3 reduces and regulates the supplied 12 VDC (Volts Direct Current) +/- 20% voltage from the vehicle. IC3 provides the regulated operating voltage required by the receiving, transmitting, decoding, and control circuitry. Diode D1 provides transient protection for the regulator. Capacitors C5, C6, C9, and inductor L1 filter the incoming vehicle power. Capacitors C7 and C8 act to filter out transients appearing at the output of voltage regulator IC3.

Paging decoder IC4 is a Phillips Semiconductor PCD5003 or equivalent. It decodes the received POCSAG encoded baseband paging signal, checks the RIC (Receiver Identification Code), notifies microcontroller IC1 of a valid received message, stores the received data, and provides Electrically Erasable and Programmable Read Only Memory (EEPROM). Decoder IC4 receives the POCSAG encoded data from the paging transceiver PTR, removes the synchronization bits, checks the paging system unit identification code, checks for and corrects any errors, stores the data in its RAM, and notifies microcontroller IC1 that the data is waiting to be read. Decoder IC4 notifies microcontroller IC1 when the last of the data has been read. Microcontroller IC1 then commands the transmitter portion of paging transceiver PTR to transmit an acknowledgment via the two-way paging network that a valid decoded command has been received by the Vindicator. The crystal circuit comprised of 76.8 KHz crystal Y2, resistor
R5 and capacitor C11 interacting with decoder IC4 form an oscillator circuit and provide clocking for decoder IC4.

The embodiments of the invention and their customized versions can operate over paging networks in the Very High Frequency Band or the Ultra High Frequency Band or Super High Frequency Bands, as well as over the new paging and messaging networks designated as Narrowband Personal Communications Services (Narrowband PCS), Metropolitan Trading Area (MTA) networks and Basic Trading Area (BTA) networks, using respective receivers, transceivers and matching decoders within the architecture of the embodiments of the system of the invention. In the future, paging may be carried out over other frequency bands and other mediums such as portions of the radio and TV bands in various schemes, as well as with satellite systems. In such cases, the system of the invention can be implemented as described with the substitution of pager receivers, transceivers and decoders conforming to the new mediums and their signaling, leaving the system building blocks of FIGURES 1 and 4 and the interaction and hardware interfacing with the pager portion essentially the same.

While particular embodiments of the invention have been described, the architecture, the infrastructure and the open, accessible and modular programmable software of the invention is versatile and can be customized by those skilled in the art to produce a variety of actions, timings sequences, expansions, reductions, adaptations to fleet requirements, specific requirements, hardware substitutions, logical hardware changes and hardware combinations within the scope infrastructure and architecture of the system of the invention. All such modifications, variations and other applications are deemed within the spirit and scope of the invention.
Vindicator Source File

>Title (Vindicator)
$Date (3/25/96)
$PAGEWIDTH (72)
$MOD91
$DEBUG
$EJECT
Definitions

12CPOC EQU 4Eh ; POCASAG decoder address
BITCNT EQU 08h ; no. of bits to be transfd
STATUS EQU 25h ; loc. of EEPROM status bits
RMBUF EQU 48h ; loc. of uP RAM data buffer
MESSAGE1 EQU 4Bh ; loc. of first msg. byte
LASTID EQU 51h ; location of last ID byte
ACTION EQU 51h ; loc. of action byte-1 nib.
SETALARM EQU 8h ; action -- sets the alarm
RESETALARM EQU 4h ; action -- resets the alarm
IGNOFF EQU 0Ch ; action -- disable ignition
ENEXTDATA EQU 06h ; action -- en. ext data dev.
DISEXTDATA EQU 0Eh ; action -- disable "
RESTART EQU 0Ah ; action -- restore to
; factory state

SPF00 EQU 00h
SPF01 EQU 0A0h ; 1200bps, voltage cnvtr. en.
SPF02 EQU 22h ; D2 & D3 control clk timing
SPF03 EQU 23h ; D6 sets int. polarity
Alarm1Ctr EQU R7 ; Counter for stage 1 alarm
TxBuff EQU 10h
TimerCtrl1 EQU 1Dh ; 2.5 sec interval counter
TimerCtrl2 EQU 1Eh ; 10 min interval counter
SEJECT
; subaddresses of the EEPROM rows
E200    EQU     00h
SUBAD0C EQU     00h
RCBYTE1  EQU     10h
RCBYTE2  EQU     18h
RCBYTE3  EQU     20h

; subaddress of the receiver RAM
INDEXRAM EQU     09h ; RAM index address
E2Pointer EQU     07h ; Index- EEPROM Add. Ptr.
E2IO     EQU     0Ah ; Index- EEPROM data I/O
INDEXCTRL EQU     00h ; Index of Control reg.
INDEXStatus EQU     00h ; " Status "
INDEXPerInt EQU     05h ; EN/DISable Periodic Interrup

READ     EQU     01h ; sets decoder read mode

WRITE     EQU     00h ; sets decoder write mode

SELECT
the following RAM locations for the EEPROM data consist of the data (6 bytes):

RAM00 EQU 20h ; start of 6 status bytes
RAM50 EQU 6Eh ; start of 6 bytes EE50-55
RAM60 EQU 74h ; start of 6 bytes EE60-65
RAM70 EQU 7Ah ; start of 6 bytes EE70-75

SELECT
; Status flags from the EEPROM

NEW EQU STATUS.6h ; new startup flag
NEWCNFRMD EQU STATUS.5h ; codeword is confirmed
IGNITIONOFF EQU STATUS.4h ; opens the ignition circuit
EXTDATAENABLE EQU STATUS.3h ; enable ext. data device
ALARM EQU STATUS.0h ; an alarm is in effect
NEWALARM EQU STATUS.1h ; alarm not acted on yet
FACTORY EQU STATUS.7h ; factory new or reset to it

$EJECT
data storage locations

<table>
<thead>
<tr>
<th>Variable</th>
<th>Type</th>
<th>Location</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BitCount</td>
<td>DATA</td>
<td>8h</td>
<td>Bit counter for I2C routines</td>
</tr>
<tr>
<td>SByteCnt</td>
<td>DATA</td>
<td>9h</td>
<td>Byte counter for send routines</td>
</tr>
<tr>
<td>RByteCnt</td>
<td>DATA</td>
<td>0Ah</td>
<td>Byte counter for I2C rec. routines</td>
</tr>
<tr>
<td>RcvDat</td>
<td>DATA</td>
<td>18h</td>
<td>I2C rec. buff, 8 bytes max</td>
</tr>
<tr>
<td>Flags</td>
<td>DATA</td>
<td>28h</td>
<td>Location for the bit flags</td>
</tr>
<tr>
<td>Flags1</td>
<td>DATA</td>
<td>29h</td>
<td>more bit flags</td>
</tr>
<tr>
<td>NoAck</td>
<td>BIT</td>
<td>Flags.0</td>
<td>I2C no acknowledge flag</td>
</tr>
<tr>
<td>BusFault</td>
<td>BIT</td>
<td>Flags.1</td>
<td>I2C bus fault flag</td>
</tr>
<tr>
<td>I2CBusy</td>
<td>BIT</td>
<td>Flags.2</td>
<td>I2C busy flag</td>
</tr>
<tr>
<td>Finished</td>
<td>BIT</td>
<td>Flags.7</td>
<td>operations fin.; shutdown</td>
</tr>
<tr>
<td>Interrupt</td>
<td>BIT</td>
<td>P3.2</td>
<td>interrupt port INTO'</td>
</tr>
<tr>
<td>Interrupt1</td>
<td>BIT</td>
<td>P3.3</td>
<td>interrupt port INT1'</td>
</tr>
<tr>
<td>ItsMe</td>
<td>BIT</td>
<td>Flags.4</td>
<td>customer ID is correct</td>
</tr>
<tr>
<td>NewStart</td>
<td>BIT</td>
<td>Flags.5</td>
<td>after power up init alarm</td>
</tr>
<tr>
<td>NoError</td>
<td>BIT</td>
<td>Flags1.0</td>
<td>data has no error</td>
</tr>
<tr>
<td>TempFlag</td>
<td>BIT</td>
<td>PSW.5</td>
<td>gen. purpose temp. flag</td>
</tr>
<tr>
<td>AlarmStage1</td>
<td>BIT</td>
<td>PSW.5</td>
<td>first stage of alarm</td>
</tr>
<tr>
<td>NewDataFlag</td>
<td>BIT</td>
<td>Flags1.3</td>
<td></td>
</tr>
<tr>
<td>BufFullFlag</td>
<td>BIT</td>
<td>Flags1.4</td>
<td></td>
</tr>
<tr>
<td>MoreDataFlag</td>
<td>BIT</td>
<td>Flags1.5</td>
<td></td>
</tr>
<tr>
<td>ALARMTIME</td>
<td>BIT</td>
<td>Flags1.6</td>
<td>used during s/d alarm</td>
</tr>
</tbody>
</table>
$EJECT

; I2C pin connections
SCLPin BIT P1.0 ; I2C serial clock line
SDAPin BIT P3.0 ; I2C serial data line

; Paging Receiver Control
DecCtrl BIT P1.3 ; Receiver ON/OFF control
DecReset BIT P3.5 ; reset the paging decoder

; Decoder Timing pulses
REFIN BIT P1.6 ; timing pulses from decoder

; Ignition power sense input
IGNSENSE BIT P3.3 ; if LOW the ignition is powered

; External Data Device I/O
ExtDataCtrl BIT P0.0 ; control the ext. data device
ExtDataPort BIT P3.1 ; asynchronous data I/O

; Microcontroller Output pins to relay interface
FUEL EQU P2.0
IGN EQU P2.1
LITSW EQU P2.2
LIGHTS EQU P2.3
HORN EQU P2.4
LOUDHAILER EQU P2.5
HAZARD EQU P2.6

; Equates for the assembler
REG0 EQU 00h ; these EQUs get around assembler limitations.
REG1 EQU 01h
REG2 EQU 02h
REG3 EQU 03h
REG5 EQU 05h
REG6 EQU 06h

$EJECT
;******************************************************************************
; Reset and Interrupt Vectors
;******************************************************************************

ORG 0000h
LCMP Start1

;-----------------------------
; INTO Interrupt Vector
; Interrupt 0 is the interrupt from the paging decoder. It may indica
; one or more of the following:
; 1. out of range
; 2. data received
; 3. decoder reset is complete
; 4. periodictimeout

ORG 0003h
CLR EX0 ; disable decoder interrupt
RETI

;-----------------------------
; INT1 Interrupt Vector
; Int.1 is the ignition control input line -- when this line goes
; LOW it indicates the car is being started. If this occurs during th
; s/d alarm state the processor will wake up and disable the ignition
; until the ign. control line goes HIGH and an interrupt is generated
; again.

ORG 0013h
NOP
RETI

$EJECT
; Factory Setup Constants

<table>
<thead>
<tr>
<th>ORG</th>
<th>0060h</th>
</tr>
</thead>
<tbody>
<tr>
<td>BegEEPROM0X:</td>
<td>DB</td>
</tr>
</tbody>
</table>

; this row sets the factory new condn

| RIC1: | DB | 01h,00,05h,00,00,00,00 |
| RIC2: | DB | 85h,4Dh,12h,00,00,00,00 |
| RIC3: | DB | 0A6h,26h,0CEh,00,00,00,00 |

; these 3 rows are th ID code from the paging system.

; Alarm Time Constants
; These are the constants used to calculate the fuel off times during the alarm sequence. The fuel off time = 25-FUELONTIME
; so for FUELONTIME = 25; fuel off time = 0

| FUELONTIME: | DB | 25,10,05,04,04,03,03,02,02,01,01,00 |

SEJECT
Subroutines

ORG 100h

BitDly -- insures minimum high and low clock times on I2C bus.
It is not really required for this project due to the low clock speeds.

BitDly:    NOP
           RET

SCLHigh -- sets the SCLPin high and waits for it to go HIGH.

SCLHigh:   SETB SCLPin   ; sets the pin high
            JNB SCLPin, $  ; waits for it to go high
            RET

SCLLow -- clears the SCLPin and waits for it to go LOW.

SCLLow:    CLR SCLPin
            JB SCLPin, $  
            RET
InitPort:  
| CLR | FUEL  |
| CLR | IGN   |
| CLR | LITSW |
| CLR | LIGHTS|
| CLR | HORN  |
| CLR | HAZARD|
| CLR | LOUDHAILER |
| RET |        |
; SendStart -- sends an I2C start sequence via the SDA and SCL pins

SendStart:  SETB SDAPin
            ACALL SCLHigh
            ACALL BitDly
            CLR SDAPin
            ACALL BitDly
            ACALL SCLLow
            ACALL BitDly
            RET

; SendStop -- sends an I2C stop sequence, thereby releasing the bus

SendStop:   CLR SDAPin ; get SDA ready for stop
            ACALL BitDly
            ACALL SCLHigh ; set clock for stop
            ACALL BitDly
            SETB SDAPin ; send I2C stop
            ACALL BitDly
            CLR I2CBusy ; clear I2C busy status flag
            RET

; SendAck -- sends an I2C acknowledge

SendAck:    CLR SDAPin
            ACALL BitDly
            ACALL SCLHigh
            ACALL BitDly
            ACALL SCLLow
            ACALL BitDly
            SETB SDAPin
            ACALL BitDly
            RET

;
; SendByte -- sends one byte of data to I2C slave device.
; enter with:   ACC = data byte to be sent
;             SCL = LOW, SDA = HIGH
; uses: carry bit (previous carry is lost)

SendByte:
  MOV     BitCnt, #BITCNT ; set bit count
SBLoop:
  RLC     A                ; get bit to be sent to carry
  MOV     SDAPin, C       ; send the carry to the pin
  ACAI.I  SCLHigh         ; send the clock
  ACAI.I  BitDly          
  CLR     SCLPin           ; clear the clock pin
  ACAI.I  BitDly          
  DJNZ    BitCnt, SBLoop   ; repeat until all bits sent
  SETB    SDAPin           ; release data line for ack.
  ACAI.I  SCLHigh         ; send clock for ack.
  ACAI.I  BitDly          
  JNB     SDAPin, SBEX    ; check for valid ack. bit
  ACAI.I  BitDly          ; wait and check again
  ACAI.I  BitDly          
  JNB     SDAPin, SBEX    
  SETB    NoAck            ; set no ack. flag
SBEX:
  CLR     SCLPin           ; finish the ack. bit
  ACAI.I  BitDly          
  RET

$EJECT
GoMaster -- sends an I2C start and the slave address
enter with: ACC = Read or Write.

GoMaster:
  SETB I2CBusy ; indicate that I2C frame is in progress.
  CLR BusFault
  SETB SDAPin ; request the bus
  JNB SDAPin, $ ; wait til it is granted
  ACALL SendStart ; begin I2C start
  ORL ACC, #I2CPOC ; enter the slave address
  ACALL SendByte ; send the slave add. & get a
  RET

$EJECT
; SendData -- sends 6 bytes of data to the EEPROM
; enter with: A = E2 address to start
; R0 = point to data to be sent.
; exit with: A & R0 unchanged

SendEEPROM:  PUSH ACC
              PUSH REG0
              MOV SByteCnt, #06 ; must always write 6 bytes
              ACALL E2RNLLeader ; send slave address
              JB NoAck, SDEX ; ck for slave not respondin

SDLoop:     MOV A, @R0 ; get data byte from the buf
              ACALL SendByte ; send the next data byte
              INC R0 ; point to the next data byte
              JB NoACK, SDEX ; ck for slave not respondin
              DJNZ SByteCnt, SDLoop ; repeat loop until all byte.
                                 ; sent

SDEX:       POP ACC ; restore registers & stack
            POP REG0
            RET

$EJECT
;/ RecByte -- gets one byte of data from an I2C slave device and sends an ACK.
; Returns: ACC = data byte received

RcvByte:
   MOV BitCnt, #BITCNT ; get the number of bits
   SETB SDAPin

RBLoop:
   ACALL SCLHigh ; read one data bit
   ACALL BitDly
   MOV C, SDAPin ; get bit from pin
   RLC A ; move data from carry bit to
   CLR SCLPin
   ACALL BitDly
   DJNZ BitCnt, RBLoop ; repeat until all gotten
   SETB SDAPin

RBAOut:
   RET

$EJECT
RcvData -- gets one or more bytes of data from the E2 device
and sends an ACK (the last byte to be gotten is not
followed by an ACK).
Enter with:   ACC = EEPROM Register to read
             RByteCnt = number of bytes to get +1
Returns:   @R0 = data received

RcvEEPROM:
  CLR        NoAck
  ACALL      E2RWLeader ; send slave subadd.
  JB         NoAck, RDEX ; is slave responding
  MOV        A, #READ ; setup decoder to
  ACALL      GoMaster ; read
  JB         NoAck, RDEX

RDLoop:
  ACALL      RcvByte ; rec. next data byte
  MOV        @R0, A   ; store data byte
  INC        R0        ; increment pointer
  MOV        A, RByteCnt ; check if it is the last
  DEC        A         ; byte to be read, if
  JZ         RDEX ; so don't send ack
  ACALL      SendAck
  DJNZ       RByteCnt, RDLoop ; do until all bytes received

RDEX:
  ACALL      SendStop ; send an I2C stop bit
  RET

SEJECT
; routine generates the leading dialog to read from or write to an
; EEPROM register. Enter with the register to be read from or
; written to in the Acc.
; Enter with: ACC = desired EEPROM register
; Return with: n/a

E2RNLd:              PUSH     ACC
                     MOV     A, #WRITE
                     ACALL   GoMaster
                     MOV     A, #E2Pointer
                     ACALL   SendByte ; write to address pointer
                     POP      ACC
                     ACALL   SendByte ; set the EEPROM reg. to rd
                     MOV     A, #WRITE
                     ACALL   GoMaster
                     MOV     A, #E210  ; EEPROM Data Read/Write
                     ACALL   SendByte ;
                     RET
; GetEEPROM reads the data from the EEPROM and stores it in the
; assigned computer RAM locations as follows:
;     STATUS - EEPROM 00 - 05 == RAM 20h - 25h
;     EEPROM 50 - 55 == RAM 6Ch - 71h
;     EEPROM 60 - 65 == RAM 73h - 78h
;     EEPROM 70 - 75 == RAM 7Ah - 7Fh
; enter with: n/a
; return with: EEPROM Data in RAM

GetEEPROM: CLR DecCntrl ; disable the paging decoder
Get0X: MOV A, #00h ; point to EEPROM 00-05
       MOV RByteCnt, #06h ; number of bytes to get+1
       MOV R0, #RAM00 ; load R0 with 20
       ACA LL RcvEEPROM ; send slave add.& subadd.
       JB NoAck, Get0X ; if NoAck try again
Get50: MOV A, #28h
       MOV RByteCnt, #06h
       MOV R0, #RAM50
       ACA LL RcvEEPROM
       JB NoAck, Get50
GetRAM60: MOV A, #30h
       MOV RByteCnt, #06h
       MOV R0, #RAM60
       ACA LL RcvEEPROM
       JB NoAck, GetRAM60
GetRAM70: MOV A, #38h
       MOV RByteCnt, #06h
       MOV R0, #RAM70
       ACA LL RcvEEPROM
       JB NoAck, GetRAM70
GetEEEnd: SETB DecCntrl ; enable the decoder
            RET

SEJECT
; This routine writes 6 bytes to the decoder EEPROM (E2) and waits
; at least 10 msec to assure that the decoder write time is allowed.
; Enter with: A = starting E2 address
; R0 = computer RAM starting address of data to be
; sent
; Exit with: R0 pointing to byte after last one written.
; uses: R5 (previous data lost)

;-------------------------------

WriteE2XX: ACALL    E2WrEnable
        JB       NoAck, WriteE2XX
E2Sender: ACALL    SendEEPROM   ; send the data
        JB       NoAck, E2Sender ;
        ACALL    WrWait        ; wait for > 7.5 msec
WriteE2End: ACALL    E2WrDisable
        JB       NoAck, WriteE2End
        ACALL    SendStop      ; done, send an I2C stop
        RET

$EJECT
; The DoNew routine sets up the alarm codes for the decoder per the
; pager communication from the purchaser.
; enter with: n/a
; return with: n/a

DoNew:    CLR        Finished
        ACALL      DoComm
        JNB        NewDataFlag, DoNewEnd
        JNB        NoError, DoNewEnd
        ACALL      DoSecondCall
        ACALL      DoFirstCall
                      ; dec. RAM data to Buf
                      ; any data available?
                      ; if NoError proceed
                      ; 2nd call? if yes DO
                      ; first call? if so DO

DoNewEnd: SETB       Finished
        RET

$EXIT
CkForErrors:  -- checks the received. POCSAG data from the
decoder for errors and clears or sets the NoError flag
  Bit 0 of the first byte of each POCSAG header code word (3 by:
  long) is the Data Fail bit; if bit is SET data is bad
  BITs 1 through 3 of third byte of a POCSAG msg. code word
  are error bits. This program will reject data which
  causes bit 3 to be SET, i.e. more than one bit err.
  enter with: received data in RAMBUF
  uses: R0 (previous data is preserved)
  ACC (previous data lost) sets/clears NoError flag

CkForErrors:  PUSH  REG0
CkHeader:     MOV   R0, #RAMBUF
               MOV   A, @R0
               SETB  NoError
               JB    ACC.0, DataFail ; header datafail bit
CkMsg1:       MOV   R0, #RAMBUF+5 ; pt. to msg. err. bits
               MOV   A, @R0
               ANL   A, #08h
               JNZ   DataFail    ; select bit 3
               MOV   A, @R0
               ANL   A, #0F0h
               MOV   @R0, A
CkMsg2:       MOV   R0, #RAMBUF+8 ; ck next msg block
               MOV   A, @R0
               ANL   A, #08h
               JNZ   DataFail    
               MOV   A, @R0
               ANL   A, #0F0h
               MOV   @R0, A
CkMsg3:       MOV   R0, #RAMBUF+11 ; ck next msg block
               MOV   A, @R0
               ANL   A, #08h
               JNZ   DataFail    
               MOV   A, @R0
               ANL   A, #0F0h
               MOV   @R0, A
               JMP   CkErrEnd
DataFail:     CLR   NoError
CkErrEnd:     POP   REG0
              RET

SEJECT
; CheckData -- checks that the customer ID received is the
; same as was previously stored in the
; appropriate area of the decoder
; uses: R0, R1, R2 (previous data is lost)
; clears/sets ItsMe flag

CheckData:  NOP
            MOV   R0, #MESSAGE1   ; point to 1st. msg. wrd.
            MOV   R1, #RAM50      ; point to first Id. byte

CkLoop:    MOV   A, @R0     ; get first msg codeword
            MOV   REG2, @R1     ; get to a reg. location
            CJNE  R0, #LASTID, ByMask ; need to get rid of the
            ANL   A, #0F0h      ; action nibble in the
            ANL   REG2, #0F0h   ; last ID byte

ByMask:    CJNE  A, REG2, NotMe
            CJNE  R0, #LASTID, DoNext ; not last ID byte

NoMore:    SETB   ItsMe ; no error by last bit
            JMP   CkDataFin

DoNext:    INC   R0     ; point to next Id. byte
            INC   R1
            JMP   CkLoop

NotMe:     CLR   ItsMe   ; ID does not match mine

CkDataFin: RET

;----------------------------------------------------------

SEJECT
; routine generates the leading dialog to read from the Status Reg.
; and the RAM. Enter with the register to be read from or written to
; in the Acc.
;
; Enter with: ACC = register to be read
; Return with: n/a

ReadRLeader:
    PUSH ACC
    MOV A, #WRITE
    ACALL GoMaster ; send SA, WR, & gets ack
    POP ACC
    ACALL SendByte ; get reg. to write to
    MOV A, #READ
    ACALL GoMaster ; sends byte and gets ack
    RET

$EJECT
; routine sets bit 1 of the decoders Index Control Register which
; enables writing to the EEPROM
; uses: ACC (previous data preserved)

E2WrEnable:
  PUSH ACC
  MOV A, #WRITE ; setup to write
  ACALL GoMaster
  MOV A, #INDEXCtrl ; write to control reg.
  ACALL SendByte ; of the INDEX register
  MOV A, #00000010b ; E2 prog enable
  ACALL SendByte
  JB NoAck, E2WrEnable ; no ack try again
  POP ACC ; restore ACC & stack
  RET

SEJECT
; routine clears bit 1 of the decoders Index Control Register which
; disables writing to the EEPROM
; uses: ACC (previous data preserved)

E2WrDisable:   PUSH ACC
                   MOV A, #WRITE       ;
                   ACALL GoMaster
                   MOV A, #INDEXCtrl
                   ACALL SendByte
                   MOV A, #00000000b  ; E2 program Disable
                   ACALL SendByte
                   JB NoAck, E2WrDisable    ; monitor BAT
                   POP ACC
                   RET

$EJECT
; This routine configures the decoder to provide a wake up call to the
; controller every 2.5 seconds during a shutdown alarm state. The uP
; keeps a tally of the wakeup calls to determine when the next
; alarm is due.
; uses: ACC (previous data is preserved)
; sets/clears NewStart Flag

SDTimerEnable: PUSH ACC
               MOV A, #WRITE
               ACALL GoMaster
               MOV A, #INDEXPerInt
               ACALL SendByte
               MOV A, #0fh
               ACALL SendByte
               JB NoAck, SDTimerEnable
               ACALL SendStop
               MOV TimerCtrl2, #06h
               MOV TimerCtrl1, #05h
               CLR ALARMTIME
               POP ACC
               CLR NewStart
               RET

SEJECT
; routine changes the configuration of the decoder to stop the wake
; up calls to the controller.
; uses: ACC (previous data is preserved)

SDTimerDisable: PUSH ACC
    MOV A, #WRITE
    ACALL GoMaster
    MOV A, #INDEXPerInt
    ACALL SendByte
    MOV A, #00h
    ACALL SendByte
    JB NoAck, SDTimerDisable
    ACALL SendStop
    POP ACC
    RET

; ------------------------------------------------------

$EJECT
; setup to read the status register of the decoder
;    uses: ACC (previous data lost)

ReadStatusLdr:  MOV  A, #WRITE           ; setup to wr to dec.
                ACALL GoMaster
                JB NoAck, ReadStatusLdr   ; no ack? try again
                MOV A, #INDEXStatus      ; send address of the
                ACALL SendByte           ;    INDEX status reg.
                MOV A, #READ             ; setup to read decoder:
                ACALL GoMaster           ;
                JB NoAck, ReadStatusLdr  ; no ack? start over.
                RET

SELECT
; routine reads the decoder Status Register and sets the appropriate
; flag.
; uses ACC (previous data lost)
; sets/clears: NewDataFlag, BufFullFlag, MoreDataFlag, ALARMTIME

RDStatus:
CLR NewDataFlag
CLR BufFullFlag
CLR MoreDataFlag
MOV RByteCnt, #01h
MOV A, #INDEXstatus
ACALL ReadStatusLdr

ACALL RcvByte
ACALL SendStop
PUSH ACC
ANL A, #80h
JZ CkComm
DJNZ TimerCtrl1, CkComm
MOV TimerCtrl1, #05
DJNZ TimerCtrl2, CkComm
SETB ALARMTIME

CkComm:
POP ACC
PUSH ACC
ANL A, #0Fh
JZ RdStatEnd
ANL A, #00000011b
 CJNE A, #01h, NextCk
SETB NewDataFlag

NextCk:
POP ACC
PUSH ACC
ANL A, #00001100b
 CJNE A, #0Ch, NotFull
SETB BufFullFlag
CLR NewDataFlag
JMP RdStatEnd

; get stat. reg data
; keep stat reg data
; keep comm bits
; No Communication
; keep last 2 bits
; call data? no? next

; get status reg data
; save it
; keep RAM ptr & Buff
; should not happen
; probable bad data
; finish
; read status routine continued

NotFull:    CJNE    A, #00h, Ck8          ; if 0; no data to be
            CLR    NewDataFlag       ; no data so clear fla
            JMP    RdStatEnd         ; finish

Ck8:       CJNE    A, #08h, Ck4          ; pointers =?; no? ck
            CLR    NewDataFlag       ; rd/write ptrs =. no
            JMP    RdStatEnd         ; finish

Ck4:       CJNE    A, #04h, RdStatEnd    ; ptrs diff; yes? get
            SETB   MoreDataFlag

RdStatEnd:  POP     ACC                ; restore ACC & stack
            RET

$EJECT
; ReadRAM - gets one or more bytes of data from an I2C device RAM and
; sends an ACK (the last byte to be gotten is not followed by an ACK)
; Enter with: RByteCnt = number of bytes to get
;
; Returns: RAMBuf = First byte of data received
; uses: ACC (previous data lost)
; Note: to get with a subaddress, use "SendData" to set the subaddress
; first. There is no provision for repeated starts.

ReadRAM:    CLR     NoAck
            MOV A, #INDEXRAM ; pt. to Index of RAM data ou
            ACALL ReadRLeader ; setup to read the RAM
            JB NoAck, RAMRdex ; ck for slave not responding

RDRAMLoop:  ACALL RcvByte ; receive next data byte
            CJNE R0, #58h, NextI ; C = 1 if R0 < 57h +1h
            JNC BypassSt ; if C<>0 don't store data
            MOV @R0, A ; save data byte
            INC R0 ; increment pointer
            DEC RByteCnt ; decrement receive byte coun
            BypassSt: JNB Interrupt, CkCause ; if interrupt, check why
            ACALL SendAck ; send an ack.; data not ck'd
            JMP RDRAMLoop ; get the next byte

CkCause:    ACALL SendStop ; send an I2C stop
            ACALL RdStatus ; read the status byte
            JB NoAck, RAMRdex ; this NoAck from stat rd lea
            JB MoreDataFlag, ReadRAM ; get rest of data

RAMRdex:    RET

;---------------------------------------------------------------
Clear the RAMBUF locations to avoid possible misinterpretation of the previous communication. This is primarily for the programmer during testing, since it can be hard to be sure that the data in memory is most recent communication.

uses: ACC, R0 (previous data lost in both)

ClearBuff:
CLR A ; ACC=0
MOV R0, #RAMBUF ; point to RAMBUF
MOV RByteCnt, #16 ; clear 16 locations

ClearBufLoop:
MOV @R0, A ; write 0 to the loca
INC R0 ; pointed to by R
DJNZ RByteCnt, ClearBufLoop ; point to next locat
RET ; if more left, do lo

SEJECT
; The DoComm routine gets the data from the paging decoder, checks
; errors, checks to assure that the comm is for this unit, and sets
; appropriate communication flags.
; sets/clears: NoError, newDataFlag

DoComm:
  SETB NoError
  CLR DecCntrl
  ACALL RDStatus
  ; disable the receive
  ; get the status byte
  ; check for data available
  JB NewDataFlag, GetData
  JB BufFullFlag, GetData
  ; get data
  ; empty the buffer
  JMP ExitDoComm
  ; no comm

GetData:
  MOV R0, #RAMBuf
  ; pt to data destination
  MOV RByteCnt, #20h
  ; get up to 20 bytes
  ACALL ReadRAM
  ; read the decoder
  ACALL CkForErrors
  ; check for tx. error
  SETB newDataFlag
  ; needed for new stuff
  JNB NoError, ExitDoComm
  ; if error, exit
  JB FACTORY, ExitDoComm
  ; if new, exit
  ACALL CheckData
  ; is the ID correct
  JNB NEWCNFRMD, ExitDoComm
  ; no action while new
  JNB ItsMe, ExitDoComm
  ; if not right ID,
  ACALL ActOnComm
  ; do the required action
  ACALL ClearBuff
  ; clear the RAM Buf

ExitDoComm:
  SETB DecCntrl
  ; enable the receive
  RET
; this routine checks to see if this is the first call, updates the E2
; register 00-05, and the customer ID section of the decoder E2. When
; controller has detected valid data it sends an "S" via the horn and
; bright lights.
; uses: ACC, R0 (previous values lost)
; set/clear: FACTORY, NEW flags

DoFirstCall:
  JNB   FACTORY, FirstOut ; if the factory flag is not
  ; its not the first call
  CLR   FACTORY
  SETB  NEW ; go from factory to new
ReWrite1:
  MOV   A, #00h ; point to EEPROM 00-05
  MOV   R0, #RAM00 ; point to E2 status in RAM
  ACALL  WriteE2XX ; change E2 Status; factory to
  JB    NoAck, ReWrite1 ; if NoAck try again
  ACALL  ResetDecoder ; reset decoder to activate st
ReWrite2:
  MOV   A, #28h ; point to EEPROM 50-55
  MOV   R0, #4Bh ; point to start of ID
  ACALL  WriteE2XX ; write first part of ID
  JB    NoAck, ReWrite2 ; if NoAck try again
ReWrite3:
  MOV   A, #30h ; point to EEPROM 60-65
  MOV   R0, #51h ; point to next part of ID
  ACALL  WriteE2XX ; write next part of ID
  JB    NoAck, ReWrite3 ; if NoAck try again
  MOV   A, #25 ; set S timer
  ACALL  Sends ; tell customer "data received
FirstOut:
  RET

SEJECT
; This routine confirms or rejects the data from the first call, by
; comparing it with the data from the second call during the initial
; customer configuration. If the data is confirmed it sends an "S", if
; it is not confirmed it sends an "SOS" via the horn and lights and th
; configuration is restored to factory state and starts waiting for th
; "first" call again.
; uses: ACC, R0 (previous data lost)
; sets/clears: NEW, FACTORY, NEWCNFRMD

DoSecondCall:  JNB     NEW, SecOut       ; if not the second call, exit
       ACALL    CheckData     ; check the ID
       JNB     ItsMe,NotRight ; if ID correct, go on
       MOV     A, #25        ; set S timer
       ACALL    SendS        ; send S to ack. receipt
       CLR     NEW
       SETB    NEWCNFRMD    ; change E2 status to confirm
       MOV     A, #00h      ; ACC= address of E2 Status reg.
       MOV     R0, #RAM00   ; RAM data to be sent to E2
       ACALL    WriteE2XX    ; write to the E2 Status reg.
       JB     NoAck, SecCallWr ; if no acknowledge, try again
       ACALL    ResetDecoder ; reset to activate new E2 Sta
       CLR     NEWCNFRMD    ; clears last call condition
       SETB    NEW           ; after EEPROM is loaded

SecCallWr:
       MOV     A, #00h      ; ACC= address of E2 Status reg.
       MOV     R0, #RAM00   ; RAM data to be sent to E2
       ACALL    WriteE2XX    ; write to the E2 Status reg.
       JB     NoAck, SecCallWr ; if no acknowledge, try again

NotRight:
       CLR     NEW           ; section rejects the last ID
       SETB    FACTORY       ; word stored & returns to
       MOV     A, #25        ; factory condition
       ACALL    SendSOS       ; & sends SOS to signal fail
       JMP     SecCallWr     ; condition. therefore we s

SEJECT
; Update the status row (00-05) of the EEPROM
; enter with: n/a
; return with: n/a
; uses: ACC, R0 (previous data lost)
; sets/clears: TempFlag

WriteStatus:  NOP
            CLR
            MOV R0, #RAM00 ; R0= add of data to write
            MOV SByteCnt, #06h ; EEPROM row length
            MOV A, #SUBAD00 ; E2 address to write
            ACALL WriteE2XX ; write to E2
            CPL TempFlag ; note we have tried <
            JB NoAck, TryTwice ; if NoAck try again

TryLoop:  WR StatEnd:  ACALL SendStop ; done, send stop
          ACALL ResetDecoder ; activate the new state
          RET

TryTwice:  JB TempFlag, TryLoop ; if 2nd try quit
          JMP WR StatEnd
; This routine determines what the communication is telling the device
to do. At present the only options are ALARM = 01h (ie write x00000001
to STATUS.0), RESETALARM = 02h, NOIGNITION = 03h and RESTART = 05h.
uses: ACC, R0 (previous data lost)

ActOnComm: JNB ALARMTIME, Around
            ACALL DoAlarm ; if time, DoAlarm
            MOV R0, #ACTION
            MOV A, @R0 ; do the s/d alarm
            ANL A, #0Fh ; point to the action
            CJNE A, #SETALARM, CkReset ; get the action byte
            SETB NEWALARM ; keep the action nibb
            UpdateStatus: ACALL DoAlarm ; if not is it reset?
                        ; if alarm; activate i
                        ; do the alarm

ActOnEnd: RET

CkReset: CJNE A, #RESETALARM, Rest ; if not RESET ;RESTART
            CLR ALARM ; clear alarm states a
            CLR NEWALARM ; restore the vehicle
            CLR IGNITIONOFF ; to normal status
            ACALL InitPort ; restore I/O ports
            ACALL WriteStatus ; write the new status
            ACALL SDTimerDisable ; disable the s/d alar
            CLR HAZARD ; turn off hazard ligh
            CLR EX1 ; disable Ign interrup
            JMP ActOnEnd ; exit

Rest: CJNE A, #RESTART, DoIgnition ; if not RESTART; Ign?
            JB ALARM, NoAction ; must RESET alarm fir
            MOV STATUS, #80h ; restores to factory
            ACALL WriteStatus ; condition.
            ACALL InitPort
            JMP ActOnEnd ; exit

DoIgnition: CJNE A, #IGNOFF, DoExtData ; disable IGN?
            JNB IGNSENSE, IgnOut ; can't disable if Ign
            SETB IGNITIONOFF ; on
            ACALL WriteStatus ; set the Ign. Off bit
            SETB EX1 ; update the EEPROM
            JMP ActOnEnd ; enable the Ignition

IgnOut: JMP ActOnEnd
; ActOn routine continues

DoExtData:
    CJNE A, #ENEXTDATA, ExtDataOff ; enable ext. data p
    SETB EXTDATAENABLE
    ACALL WriteStatus
    JMP ActOnEnd

ExtDataOff:
    CJNE A, #DISEXTDATA, NoAction ; disable ext. data p
    CLR EXTDATAENABLE
    ACALL WriteStatus

NoAction:
    JMP ActOnEnd ; do nothing just retu

SEJECT
; The Set50Hz subroutine sets the decoder to provide 50 Hz pulses at i
; REF output.
;
Set50Hz:
    NOP
    SETB SPF02.2 ; set decoder timer = 50Hz
    CLR SPF02.3
    ACALL WriteStatus ; update the E2 status registe
    RET

SEJECT
; This routine gives a delay of approximately 7.5 msec to allow the dec
; to complete the EEPROM write cycle.
;_wrWait:
    PUSH    REG5                 ; save R5 on the stack
    MOV     R5, #00h             ; initialize R5
    DJNZ    R5, $                ; Next 5 instructions
    DJNZ    R5, $                ; a delay > 7.5 msec
    DJNZ    R5, $                ;
    DJNZ    R5, $                ;
    DJNZ    R5, $                ;
    POP     REG5                 ; restore stack & reg.
    RET

$EJECT
; This routine gives a delay based on the value passed in R5
; when the REFIN is pulsed at a 50Hz rate; example 25 x 20msec = 500ms
; (20 msec/cycle -- 10 msec high & 10 msec low). +/- 10msec
; enter with: R5 = number of 20msec delays
; return with: R5 unchanged
;
Wait:    PUSH REG5 ; save R5 on the stack
           JNB REFIN, $ ; get known state of R
           JB REFIN, $ ;
           CJNE R5, #0, WaitLoop ; start the wait loop
WaitEnd: POP REG5 ; restore stack & regi
           RET

WaitLoop: JNB REFIN, $ ; wait 10 msec.
           JB REFIN, $ ; wait 10 msec.
           DJNZ R5, WaitLoop ; repeat until R5 = 0
           JMP WaitEnd

;-----------------------------------------

SEJECT
; FuelOff calculates the off time for the fuel and delays for that time
;
FuelOff:
   PUSH ACC ; save the ACC
   CPL A ; compute the 2's complement of t:
   INC A ; fuel off time.
   ADD A, #25 ; add the negative of the fuel off
time to the normal cycle time.
   MOV R5, A ; R5 is the counter for the wait
   ACALL WAIT ; wait for the time the fuel should
               ; be off.
   POP ACC ; restore the ACC and the stack
FuelOffEnd:
   RET

;;END
; Sends an Inter-word Space of Seven(7) Dits. This is the normal morse spacing.

SendIW:    PUSH  REG6                ; save R6 on the stack
            MOV    R6, #07                ; R6 is the Dit counter
SendIWLoop: MOV    R5, A                ; A x 20msec = time on(msec)
            ACA      Wait               ; uses R5 to set duration
            JB      AlarmStage1, ContIWLoop ;
            JNB     NEWALARM, ContIWLoop  ;
            CPL     FUEL                ; cycles fuel during stage 2
            ACA      FuelOff             ; (25-A) x 20msec = time
            CPL     FUEL                ; off(msec)
ContIWLoop: DJNZ    R6, SendIWLoop      ; restores fuel
            POP     REG6                ; repeat 7 times
            RET                     ; restore R6 and stack

$EJECT
; Sends an Intercharacter Space of one(l) Dit

SendIC:    PUSH REG6           ; save R6 on stack
           MOV R6, #01         ; Dit counter

SndICLoop: MOV R5, A           ; A x 20msec = fuel time on (m
           ACALL Wait         ; uses R5 to set duration
           JB AlarmStage1, CntICLoop ; if stage 1, no fuel
                ; action
           JNB NEWALARM, CntICLoop ; if new, no fuel chan
           CPL FUEL           ; shutoff fuel flow
           ACALL FuelOff     ; (25-A) x 20msec = time off( 
           CPL FUEL          ; restore fuel flow

CntICLoop: DJNZ R6, SndICLoop  ; sent 1 Dit? no? do again
           POP REG6           ; restore R6 and stack
           RET

$EJECT
; this routine sends an "O"
; expects to enter with Fuel ON and ACC= fuel on time
; exits with Fuel ON

SendO:
  PUSH REG6     ; save R6 on the stack
  MOV R6, #03   ; send 3 dahs and spaces

SendOLoop:
  PUSH REG6     ; save new R6 value on stack
  MOV R6, #02   ; 1 Dah = 4 Dit times
  CPL LIGHTS    ; turn on lights
  CPL LITSW     ; take control of the hibeam
  CPL HORN      ; turn on horn

SendDahLoop:
  MOV R5, A     ; A x 20msec = fuel time on (ms)
  ACALL Wait    ; wait for fuel off time
  JB             ; stage 1? don't affect fuel
                 ; new alarm? exit
  JNB NEWALARM, DahLoopEnd ; stage 1? don't affect fuel
  CPL FUEL      ; turn off fuel flow
  ACALL FuelOff ; wait fuel of time
  CPL FUEL      ; turn on fuel flow
  DJNZ R6, SendDahLoop ; cont. til dah length

; send dit length space
  CPL LIGHTS    ; turn off lights
  CPL HORN      ; turn off horn
  MOV R5, A     ; 1 Dit between Dahs
  ACALL Wait    ; wait for Dit time
  JB AlarmStage1, DahContinue ; if stage 1 bypass fuel
  JNB NEWALARM, DahContinue ; if new alarm bypass fuel
  CPL FUEL      ; turn off fuel
  ACALL FuelOff ; wait for fuel off time

DahContinue:
  POP REG6      ; get stored dah count
  DJNZ R6, SendOLoop ; last dah? yes? exit
  POP REG6      ; restore R6 and stack
  RET
; cut fuel to XY x 20msec on and (25 - XY) x 20msec off
; lights and horn unchanged
; expects to enter with the fuel ON and fuel on time in the ACC
; exits with the fuel ON

SendS:  PUSH REG6
MOV R6, #06          ; save R6 on the stack
                ; R6= Dit counter
FirstLoop: NOP
CPL LIGHTS         ; turn on lights
CPL HORN           ; turn on horn
MOV R5, A          ; fuel on time = A x 2
ACALL Wait         ; wait til time to turn fuel
JB AlarmStage1, ContinueFLoop ; stage 1 bypass fuel
JNB NEWALARM, ContinueFLoop ; if new bypass fuel
CPL FUEL           ; turn off fuel flow
ACALL FuelOff      ; fuel off time
CPL FUEL           ; fuel on
ContinueFLoop: DJNZ R6, FirstLoop ; all Dits sent? yes?
POP REG6           ; restore R6 and the s
RET

SEJECT
; this routine turns on the loudhailer for 5.1 seconds

VoiceAlarm:

    PUSH REG5
    SETB LOUDHAILER ; turn on loudhailer
    MOV R5, #0ffh ; on time = R5 x 20msec
    ACALL Wait ; wait till time to turn fuel
    CLR LOUDHAILER ; turn off loudhailer
    POP REG5
    RET

; END
; this routine sends "SOS" via the horn and the lights.
; It follows the standard Morse Code spec. (time between characters ha
; shortened, to 1 dit time, so that it sounds correct)
; S = 7 dit times
; O = 3 dah times and 3 dit times (Dah = 3 Dit times)

SendSOS:
   NOP
   ACALL SDTimerDisable ; to enable REF
   ACALL SendS
   ACALL SendIC
   ACALL SendO
   ACALL SendIC
   ACALL SendS
   ACALL SendIW
   ACALL VoiceAlarm
   ACALL SendIW
   JNB ALARM, SOSEnd
   ACALL SDTimerEnable

SOSEnd:
   RET

SELECT
; This routine causes Horn to sound "SOS" and the bright lights to fla
; the same. When first stage is done, the fuel is incrementally reduce
; while the lights and horn signals SOS. When the value read from memc
; is zero the fuel is shutoff and the ignition line is opened.
; enter with:
; return with: Fuel & Ignition shutoff

DoAlarm1: MOV Alarm1Ctr, #7 ; send SOS 7 times in stage
            SETB AlarmStage1 ; stage 1 flag
            SETB LITSW ; turn off brights if
            JNB REFIN, $ ; wait 1 cycle
            JB REFIN, $ ; get to a known state
            JNB REFIN, $
            MOV DPTR, #FUELONTIME ; loc. for fuel on tire

NextStage: CLR A ; ACC = 0
            MOVC A, @A+DPTR ; ACC = Fuel On Time
            JZ EndIt ; when ACC=0, exit

Alarm1Loop: NOP
            ACALL SendSOS ; Send SOS via Horn/light:
            DJNZ Alarm1Ctr, Alarm1Loop ; repeat for 130 sec.
            CLR AlarmStage1 ; go to stage 2
            INC DPTR ; go to next stage
            MOV Alarm1Ctr, #2
            JMP NextStage

EndIt: CLR LITSW ; enable bright light switch
        SETB FUEL ; shutoff fuel
        SETB IGN ; shutoff ignition

$EJECT
; This routine flashes the lights, sounds the horn at set intervals
; after the car has been shutdown.

DoAlarm2:

    CLR    AlarmStage1
    MOV    A, #25          ; clear stage1 flag
    MOV    R5, #25         ; set for zero FUEL ON time
    ACALL  SendSOS         ; set for 500msec wait periods
                          ; send SOS to identify self to people

    RET
The DoAlarm routine does all of the alarm functions. The system will stay in the alarm condition until the pager receives an "alarm clear". The reduction of fuel flow is slow and incremental until it is finally stopped. After the fuel has been stopped for 2 minutes the ignition is opened. After the ign. has been open for 2 min. the sys. switches to shutdown (s/d) alarm during which it blows the horn and and flashes the lights every 15 minutes. The ign & fuel are off.

DoAlarm:

```
JB ALARM, DoSDAAlarm ; if bit set do alarm
SETB HAZARD ; turn on hazard light
CLR NEWALARM ; clears new alarm flas
SETB ALARM ; Set alarm:
ACALL WriteStatus ; update the E2 status
SETB NEWALARM ; restore the present
CLR ALARM ; for the SOS routine
JNB REFIN, $ ; wait till its high
JB REFIN, $
JNB REFIN, $
ACALL DoAlarm1 ; horn/lights/fuel/Ign
CLR NEWALARM ; ready for s/d alarm
ACALL SDTimerEnable ; enable timer interru
; in the decoder
```

DoSDAAlarm:

```
JNB ALARMTIME, AlarmEnd ; not time for s/d ala
CLR ALARMTIME ; exit
SETB HAZARD ; turn on hazard light
JNB REFIN, $ ; wait till high
JB REFIN, $
JNB REFIN, $
ACALL DoAlarm2 ; shutdown alarm
```

AlarmEnd:

```
CLR NEWALARM ;
CLR ALARM ;
RET
```

Sject
; this routine checks to see if an alarm is active or if the car ign
; has been secured. If either of these is true it checks to see if the
; ignition switch is turned OFF; if it is the IGN and FUEL relays are
; deenergized; this reduces the power consumption during an alarm or
; when the vehicle is in the secured state.

CkIgnition:         MOV    A, STATUS        ; ck for alarm or the I
                   ANL    A, #13h          ;  off flag if neither
                   JZ     CkIgnEnd         ;  exit
                   SETB   EX1             ;  this routine
                   JNB    IGSENSE, KillIt ;  enable the ign. int.
                   CLR    IGN             ;  ign powered -- kill it
                   CLR    FUEL            ;  de-energize FUEL and :
                   JMP    CkIgnEnd        ;  relays (ie restore
                                    ;  F&I)
 KillIt:            SETB   IGN             ;  jump out
                   SETB   FUEL            ;  cut the fuel and ign :
                   ACall  DoAlarm2        ;  energizing their rel
                   CkIgnEnd:           RET  ;  sound the alarm sequen.
; determine if an alarm state exists and if so is it time to alarm.

CheckAlarm:  
    MOV  A, #03h     ; mask for alarm bits
    ANL  A, STATUS  ; keep alarm bits in the ACC
    JZ   CkAlOut    ; no alarm so go around
    SETB HAZARD    ; turn on hazard lights
    JNB  NewStart, CkAlOut ; if not new must be s/d
    SETB ALARMTIME
    ACALL DoAlarm
    ACALL SDTimerEnable ; enable s/d timer
    CLR NewStart     ; indicates the power was jus
                      ; reset
    RET

;----------------------------------

SEJECT
; this routine resets the decoder and sets the shutdown timer if the D
; reset flag is set.

CkDecoderReset: CLR EA ; disable all interrupts
    JNB DecReset, OK ; if decoder reset is set then
    ACALL ResetDecoder ; has been lost so we need to
    MOV TimerCtrl, #05 ; restart the s/d timer.
    MOV TimerCtrl2, #06 ; perform decoder reset
    SETB NewStart ; reinitialize s/d timer

OK:

SEJECT
; CodeToData moves data from the upPROM to the upRAM
; enter with: DPTR = code location to be moved
; R0 = upRAM destination
; SByteCnt = number of bytes to be moved

CodeToData:
  PUSH ACC ; save the ACC on the stack
  PUSH SByteCnt ; save SByteCnt on stack
  MOV SByteCnt, #06h ; setup to get 6 bytes

CDLoop:
  CLR A ; ACC=0
  MOVC A, @A+DPTR ; data from code to RAM
  MOV @R0, A ; mov the PROM byte to RAM
  INC R0 ; point to next RAM loc.
  INC DPTR ; point to next PROM loc.
  DJNZ SByteCnt, CDLoop ; last byte? no? do loop
  POP SByteCnt ; restore SByteCnt
  POP ACC ; restore ACC

;-----------------------------

$EJECT
; This routine moves data from the uPCode to the decoder EEPROM;
;      Enter with: SByteCnt = no. of bytes to move
;      DPTR = uPCode address to move
;      ACC = decoder dest. EEPROM address (first byte)

CodeToEEPROM:  MOV R0, #TxBuff          ; move to TxBuff
                ACALL CodeToData       ; move PROM byte to RAM
                MOV R0, #TxBuff        ; get data from TxBuff
                CLR DecCntrl          ; disable decoder
                ACALL WriteE2XX        ; move TxBuff to EEPROM
                SETB DecCntrl         ; enable decoder
                RET

$EJECT
; this routine resets the decoder

ResetDecoder:  CLR  DecCntrl            ; disable the decoder
               CLR  EA               ; disable 8051 interrupts
               SETB DecReset        ; set the decoder reset bit
               MOV  R0, #50          ; set reset pulse duration
               DJNZ  R0, $           ; wait for timeout
               CLR  DecReset         ; clear the decoder reset bit
               JNB  Interrupt, $     ; wait for osc. startup done
               ACALL  RdStatus  ; initialize timer & wait til
               MOV  R1, #0FFh        ; the decoder reset cycle
               MOV  R2, #8           ; is completed.
               MOV  R0, #0FFh
               DJNZ  R0, $
               DJNZ  R1, ResetHold
               MOV  R1, #0FFh
               DJNZ  R2, ResetHold
               SETB  DecCntrl         ; enable the decoder
               RET

SEJECT
; This routine is used only by the factory to initialize the decoder.  
; It sets up the control word and the Receiver Identification Code (F

DoFactory:

MOV A, STATUS                  ; get E2 status byte
ANL A, #0F0h                  ; get rid of alarm bits
JNZ FactOut                   ; if = 0 do factory setup
MOV DPTR, #BeginEPROM         ; point to E2 stat. reg &
                             ; data
MOV SByteCnt, #06             ; move 6 bytes to upRAM
MOV A, #E200                  ; point to decoder E2 sta
ACALL CodeToEEPROM            ; move data from EPROM to
MOV DPTR, #R1C1               ; point to E2 R1C1 PROM
MOV SByteCnt, #06             ;
MOV A, #R1CBYTE1              ;
ACALL CodeToEEPROM            ;
MOV DPTR, #R1C2               ; point to E2 R1C2 PROM
MOV SByteCnt, #06             ;
MOV A, #R1CBYTE2              ;
ACALL CodeToEEPROM            ;
MOV DPTR, #R1C3               ; point to E2 R1C3 PROM
MOV SByteCnt, #06             ;
MOV A, #R1CBYTE3              ;
ACALL CodeToEEPROM            ;
ACALL SendStop                ;

FactOut:

ACALL ResetDecoder            ; reset the decoder

SEJECT
;************************************************************
; * MAIN PROGRAM
;************************************************************

Start1:
    MOV    SP, #30h ; stack pointer = 30h
    MOV    R0, #00h
    MOV    R1, #00h

Hold1:
    DJNZ   R0, $
    DJNZ   R1, Hold1
    ACALL  InitPort ; set P2 to all low
    SETB   NewStart ; tell program that power
    CLR    IT0 ; decoder int = low level
    CLR    IT1 ; ignition int = low level
    ACALL  ResetDecoder ; reset the decoder
    SETB   EX0 ; enable the decoder int.
    MOV    R0, #00h
    MOV    R1, #00h
    MOV    R2, #10

Hold2:
    DJNZ   R0, $
    DJNZ   R1, Hold2
    DJNZ   R2, Hold2

Start:
    NOP

Init:
    CLR    Finished ; clear the finished flag
    ACALL  E2WrDisable ; init. the dec. Control reg.
    ACALL  GetEEPROM ; move EEPROM data to uP RAM
    ACALL  CkIgnition ; config. IGN and FUEL relays
    ACALL  CheckAlarm ; if alarm & time -- Do it
    ACALL  DoFactory ; this is a factory only rout
    JB     FACTORY, NuSetup ; indicates new from the fact
    JB     NEW, NuSetup ; if NEW set do new setup
           ; needs to verify ID

Comm:
    ACALL  DoComm ; get and respond to the comm

MainEnd:
    SETB   EX0 ; enable INT0' (pin 12)
    SETB   EA ; enable interrupts
    MOV    PCON, #01h ; idle the uP until next int.
    JMP    Start ; startup after being idle

NuSetup:
    ACALL  DoNew ; customer setup routine
    JB     Finished, MainEnd

END
We claim as our invention:

1. A vehicular control system for the control of vehicular systems over radio paging networks, comprising:
   (a) a paging receiver and decoder portion including a paging receiver for receiving paging data signals over said radio paging networks and a paging decoder for decoding said paging data signals received by said paging receiver;
   (b) a microcontroller portion including a combination of a microcontroller with embedded programmable software instructions and memory, said microcontroller interacting with said programmable software instructions, said memory, said paging receiver and said paging decoder to control said paging receiver, said paging decoder and said vehicular control system and
   (c) a vehicular systems control interface portion with a vehicular systems controller and a control interface, said control interface interacting with said microcontroller, said programmable software instructions and said vehicular systems controller for said control of vehicular systems.

2. The system of Claim 1, wherein said control interface is a logic line interface interacting with said microcontroller and said vehicular systems controller.

3. The system of Claim 1, wherein said vehicular systems controller is a relay bank.

4. The system of Claim 1, wherein said vehicular system controller is a solid-state switching device bank.

5. The system of Claim 1, wherein said paging data signals are in POCSAG format.

6. The system of Claim 1, wherein said paging decoder is a POCSAG format paging decoder.

7. The system of Claim 1, wherein said control of vehicular systems includes flashing of vehicle lights.

8. The system of Claim 1, wherein said control of vehicular systems includes intermittent sounding of the vehicle horn.
9. The system of Claim 1, wherein said control of vehicular systems includes activation of a vehicular loudhailer.

10. The system of Claim 1, wherein said control of vehicular systems includes control of the vehicle ignition system.

11. The system of Claim 1, wherein said control of vehicular systems includes control of the vehicle engine fuel supply.

12. The system of Claim 1, wherein said control of vehicular systems includes control of the vehicle hazard lights.

13. The system of Claim 1, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, control of the vehicle engine ignition system, control of the vehicle engine fuel supply and control of the vehicle hazard lights.

14. The system of Claim 1, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer, control of the vehicle engine ignition system, control of the vehicle engine fuel supply and control of the vehicle hazard lights.

15. The system of Claim 1, wherein said programmable software instructions include a plurality of vehicle control instructions.

16. The system of Claim 1, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, variable control of the vehicle engine ignition system and variable control of the vehicle engine fuel supply.
17. The system of Claim 1, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling control of the vehicle engine ignition system and progressive diminishing of the engine fuel supply.

18. The system of Claim 1, wherein said programmable software instructions include instructions for causing combinations of SOS pattern flashing of the vehicle lights, SOS intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling of the vehicle engine ignition system and progressive disabling of the vehicle engine fuel supply.

19. The system of Claim 1, wherein said vehicular systems controller interacting with said control interface, said microcontroller and its programmable software instructions provides means for enabling and disabling vehicular systems per said programmable software instructions.

20. The system of Claim 1, wherein said paging data signals include user identification and mode request signals for said vehicular control system.

21. The system of Claim 1, wherein said paging data signals include reset signals, factory test mode signals and initialization signals for said vehicular control system.

22. The system of Claim 1, wherein said microcontroller portion interacting with said paging decoder portion provides validation of said paging data signals in accordance with said programmable software instructions.

23. The system of Claim 1 wherein said radio paging networks operate in the Very High Frequency Band.

24. The system of Claim 1, wherein said radio paging networks operate in the Ultra High Frequency Band.

25. The system of Claim 1, wherein said radio paging networks operate in the Super High Frequency Band.
26. A vehicular control system for the control of vehicular systems over radio paging networks, comprising:

   a) a paging receiver portion for receiving paging data signals over said radio paging networks;

   b) a decoder portion including a paging decoder for decoding said paging data signals received by said paging receiver;

   c) a microcontroller portion including a combination of a microcontroller with embedded programmable software instructions and memory, said microcontroller interacting with said programmable software instructions, said memory, said paging receiver and said paging decoder to control said paging receiver, said paging decoder and said vehicular control system;

   d) a control interface portion with a control interface, said control interface interacting with said microcontroller and said programmable software instructions and

   e) a vehicular systems controller for said control of vehicular systems interacting with said control interface portion.

27. The system of Claim 26, wherein said control interface is a logic line interface interacting with said microcontroller and said vehicular systems controller.

28. The system of Claim 26, wherein said vehicular systems controller is a relay bank.

29. The system of Claim 26, wherein said vehicular system controller is a solid-state switching device bank.

30. The system of Claim 26, wherein said paging data signals are in POCSAG format.

31. The system of Claim 26, wherein said paging decoder is a POCSAG format paging decoder.

32. The system of Claim 26, wherein said control of vehicular systems includes flashing of vehicle lights.
33. The system of Claim 26, wherein said control of vehicular systems includes intermittent sounding of the vehicle horn.

34. The system of Claim 26, wherein said control of vehicular systems includes activation of a vehicular loudhailer.

35. The system of Claim 26, wherein said control of vehicular systems includes control of the vehicle ignition system.

36. The system of Claim 26, wherein said control of vehicular systems includes control of the vehicle engine fuel supply.

37. The system of Claim 26, wherein said control of vehicular systems includes control of the vehicle hazard lights.

38. The system of Claim 26, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, control of the vehicle engine ignition system, control of the vehicle engine fuel supply and control of the vehicle hazard lights.

39. The system of Claim 26, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer, control of the vehicle engine ignition system, control of the vehicle engine fuel supply and control of the vehicle hazard lights.

40. The system of Claim 26, wherein said programmable software instructions include a plurality of vehicle control instructions.

41. The system of Claim 26, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, variable control of the vehicle engine ignition system and variable control of the vehicle engine fuel supply.
42. The system of Claim 26, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling control of the vehicle engine ignition system and progressive diminishing of the engine fuel supply.

43. The system of Claim 26, wherein said programmable software instructions include instructions for causing combinations of SOS pattern flashing of the vehicle lights, SOS intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling of the vehicle engine ignition system and progressive disabling of the vehicle engine fuel supply.

44. The system of Claim 26, wherein said vehicular systems controller interacting with said control interface and said microcontroller with programmable software instructions provides means for enabling and disabling vehicular systems per said programmable software instructions.

45. The system of Claim 26, wherein said paging data signals include user identification and mode request signals for said vehicular control system.

46. The system of Claim 26, wherein said paging data signals include reset signals, factory test mode signals and initialization signals.

47. The system of Claim 26, wherein said microcontroller portion interacting with said paging decoder provides validation of said paging data signals in accordance with said programmable software instructions.

48. The system of Claim 26, wherein said radio paging networks operate in the Very High Frequency Band.

49. The system of Claim 26, wherein said radio paging networks operate in the Ultra High Frequency Band.

50. The system of Claim 26, wherein said radio paging networks operate in the Super High Frequency Band.
51. A vehicular control system for the control of vehicular systems over radio paging networks, comprising:

(a) a paging transceiver and decoder portion including a paging transceiver with a receiver for receiving of paging data signals and a transmitter for transmitting data signals over said radio paging networks and a paging decoder for decoding said paging data signals received by said paging transceiver;

(b) a microcontroller portion including a combination of a microcontroller with embedded programmable software instructions and memory, said microcontroller interacting with said programmable software instructions, said memory, said paging transceiver and said paging decoder to control said paging transceiver, said paging decoder and said vehicular control system and

(c) a vehicular systems control interface portion with a vehicular systems controller and a control interface, said control interface interacting with said microcontroller, said programmable software instructions and said vehicular systems controller for said control of vehicular systems.

52. The system of Claim 51, wherein said control interface is a logic line interface interacting with said microcontroller and said vehicular systems controller.

53. The system of Claim 51, wherein said vehicular systems controller is a relay bank.

54. The system of Claim 51, wherein said vehicular system controller is a solid-state switching device bank.

55. The system of Claim 51, wherein said microcontroller controls said transmitting of data signals from said transmitter.

56. The system of Claim 51, wherein said transmitting of data signals includes transmitting data from external sources.

57. The system of Claim 51, wherein said transmitting of data signals includes transmitting of vehicle position coordinates.
58. The system of Claim 51, wherein said transmitting of data signals includes transmitting data signals from a Global Positioning System.

59. The system of Claim 51, wherein said paging data signals are in POCSAG format.

60. The system of Claim 51, wherein said paging decoder is a POCSAG format paging decoder.

61. The system of Claim 51, wherein said control of vehicular systems includes flashing of vehicle lights.

62. The system of Claim 51, wherein said control of vehicular systems includes intermittent sounding of the vehicle horn.

63. The system of Claim 51, wherein said control of vehicular systems includes activation of a vehicular loudhailer.

64. The system of Claim 51, wherein said control of vehicular systems includes control of the vehicle ignition system.

65. The system of Claim 51, wherein said control of vehicular systems includes control of the vehicle engine fuel supply.

66. The system of Claim 51, wherein said control of vehicular systems includes control of the vehicle hazard lights.

67. The system of Claim 51, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, control of the vehicle engine ignition system, control of transmitting data signals, control of the vehicle engine fuel supply and control of the vehicle hazard lights.

68. The system of Claim 51, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer, control of the vehicle engine ignition system, control of the vehicle engine fuel supply, control of said transmitting of data signals and control of the vehicle hazard lights.
69. The system of Claim 51, wherein said programmable software instructions include a plurality of vehicle control instructions and for control of said transmitting of data signals.

70. The system of Claim 51, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, variable control of the vehicle engine ignition system and variable control of the vehicle engine fuel supply.

71. The system of Claim 51, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling control of the vehicle engine ignition system and progressive diminishing of the engine fuel supply.

72. The system of Claim 51, wherein said programmable software instructions include instructions for causing combinations of SOS pattern flashing of the vehicle lights, SOS intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling of the vehicle engine ignition system and progressive disabling of the vehicle engine fuel supply.

73. The system of Claim 51, wherein said vehicular systems controller interacting with said control interface, said microcontroller and its programmable software instructions provides means for enabling and disabling vehicular systems per said programmable software instructions.

74. The system of Claim 51, wherein said receiving paging data signals include receiving user identification data signals and mode request data signals for said vehicular control system.

75. The system of Claim 51, wherein said receiving paging data signals include receiving reset data signals, factory test mode data signals and initialization data signals for said vehicular control system.
76. The system of Claim 51, wherein said microcontroller portion interacting with said paging decoder portion provides validation of said paging data signals in accordance with said programmable software instructions.

77. The system of Claim 51, wherein said radio paging networks operate in the Very High Frequency Band.

78. The system of Claim 51, wherein said radio paging networks operate in the Ultra High Frequency Band.

79. The system of Claim 51, wherein said radio paging networks operate in the Super High Frequency Band.

80. The system of Claim 51, wherein said radio paging networks are radio paging networks with messaging.

81. The system of Claim 51, wherein said radio paging networks are Narrowband Personal Communication Services networks.

82. The system of Claim 51, wherein said radio paging networks are Metropolitan Trading Area networks.

83. The system of Claim 51, wherein said radio paging networks are Basic Trading Area networks.

84. A vehicular control system for the control of vehicular systems over radio paging networks, comprising:

   a) a paging transceiver portion including a paging transceiver with a receiver for receiving of paging data signals and a transmitter for transmitting data signals over said radio paging networks;

   b) a paging decoder portion for decoding said paging data signals received by said paging transceiver;

   c) a microcontroller portion including a combination of a microcontroller with embedded programmable software instructions and memory, said microcontroller interacting with said programmable software instructions, said
memory, said paging transceiver portion and said paging decoder portion to
control said paging transceiver, said paging decoder and said vehicular control
system;
d) a control interface portion including a control interface with said control
interface interacting with said microcontroller portion and said programmable
software instructions for said control of vehicular systems and
e) a vehicular systems controller portion including a vehicular systems controller
interacting with said control interface portion and said microcontroller portion
for said control of vehicular systems.

85. The system of Claim 84, wherein said control interface is a logic line interface
interacting with said microcontroller portion and said vehicular systems controller.

86. The system of Claim 84, wherein said vehicular systems controller is a relay bank.

87. The system of Claim 84, wherein said vehicular system controller is a solid-state
switching device bank.

88. The system of Claim 84, wherein said microcontroller controls said transmitting of
data signals from said transmitter.

89. The system of Claim 84, wherein said transmitting of data signals includes transmitting
data from external sources.

90. The system of Claim 84, wherein said transmitting of data signals includes transmitting
of vehicle position coordinates.

91. The system of Claim 84, wherein said transmitting of data signals includes transmitting
data signals from a Global Positioning System.

92. The system of Claim 84, wherein said paging data signals are in POCSAG format.

93. The system of Claim 84, wherein said paging decoder is a POCSAG format paging
decoder.

94. The system of Claim 84, wherein said control of vehicular systems includes flashing of
vehicle lights.
95. The system of Claim 84, wherein said control of vehicular systems includes intermittent sounding of the vehicle horn.

96. The system of Claim 84, wherein said control of vehicular systems includes activation of a vehicular loudhailer.

97. The system of Claim 84, wherein said control of vehicular systems includes control of the vehicle ignition system.

98. The system of Claim 84, wherein said control of vehicular systems includes control of the vehicle engine fuel supply.

99. The system of Claim 84, wherein said control of vehicular systems includes control of the vehicle hazard lights.

100. The system of Claim 84, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, control of the vehicle engine ignition system, control of transmitting data signals, control of the vehicle engine fuel supply and control of the vehicle hazard lights.

101. The system of Claim 84, wherein said control of vehicular systems includes combinations of flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer, control of the vehicle engine ignition system, control of the vehicle engine fuel supply control of said transmitting data signals and control of the vehicle hazard lights.

102. The system of Claim 84, wherein said programmable software instructions include a plurality of vehicle control instructions and instructions for control of said transmitting of data signals.

103. The system of Claim 84, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, variable control of the vehicle engine ignition system and variable control of the vehicle engine fuel supply.
104. The system of Claim 84, wherein said programmable software instructions include instructions for causing flashing of the vehicle lights, intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling control of the vehicle engine ignition system and progressive diminishing of the engine fuel supply.

105. The system of Claim 84, wherein said programmable software instructions include instructions for causing combinations of SOS pattern flashing of the vehicle lights, SOS intermittent sounding of the vehicle horn, the activation of a vehicular loudhailer with stored announcement, enabling and disabling of the vehicle engine ignition system and progressive disabling of the vehicle engine fuel supply.

106. The system of Claim 84, wherein said vehicular systems controller interacting with said control interface, said microcontroller and its programmable software instructions provides means for enabling and disabling vehicular systems per said programmable software instructions.

107. The system of Claim 84, wherein said receiving paging data signals include receiving user identification data signals and mode request data signals for said vehicular control system.

108. The system of Claim 84, wherein said receiving paging data signals include receiving reset data signals, factory test mode data signals and initialization data signals for said vehicular control system.

109. The system of Claim 84, wherein said microcontroller portion interacting with said paging decoder portion provides validation of said paging data signals in accordance with said programmable software instructions.

110. The system of Claim 84, wherein said radio paging networks operate in the Very High Frequency Band.

111. The system of Claim 84, wherein said radio paging networks operate in the Ultra High Frequency Band.
112. The system of Claim 84, wherein said radio paging networks operate in the Super High Frequency Band.

113. The system of Claim 84, wherein said radio paging networks are radio paging networks with messaging.

114. The system of Claim 84, wherein said radio networks are Narrowband Personal Communication Services networks.

115. The system of Claim 84, wherein said radio paging networks are Metropolitan Trading Area radio networks.

116. The system of Claim 84, wherein said radio paging networks are Basic Trading Area networks.
INTERNATIONAL SEARCH REPORT

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) : B60R 25/10
US CL. : 340/426

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)


Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to claim No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Y,P</td>
<td>US, A, 5,588,038 (SNYDER) 24 DECEMBER 1996, abstract, lines 1-5</td>
<td>1-116</td>
</tr>
</tbody>
</table>

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

Date of the actual completion of the international search: 24 MAY 1997
Date of mailing of the international search report: 14 AUG 1997

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