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(54) Title: AN IMPROVED HIGH CAPACITY LOW COST MULTI-STATE MAGNETIC MEMORY

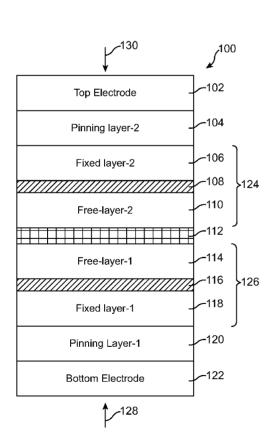


FIG. 1

(57) Abstract: One embodiment of the present invention includes multi-state current-switching magnetic memory element including a stack of two or more magnetic tunneling junctions (MTJs), each MTJ having a free layer and being separated from other MTJs in the stack by a seeding layer formed upon an isolation layer, the stack for storing more than one bit of information, wherein different levels of current applied to the memory element causes switching to different states.

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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) PubWest (PGPB,USPT,USOC,EPAB,JPAB), Google Scholar, Google Patent Search terms: magnetic, memory, tunnel, junction, MTJ, two, second, multiple, bits, stack, multi-state, four, pinning, barrier, composition, thickness, isolation, cells, current, switching			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where app	propriate, of the relevant passages	Relevant to claim No.
х	US 2005/0167657 A1 (NICKEL et al.) 04 August 2005 (para [0013]-[0014], [0017], [0019]-[0023], [0025]-[0026]	04.08.2005), , [0036], [0039]-[0040], Fig. 1	1-18
Α	US 6,169,689 B1 (NAJI) 02 January 2001 (02.01.2001) entire document	,	1-18
A	US 7,109,539 B2 (LU) 19 September 2006 (19.09.2006 entire document	5).	1-18
Further documents are listed in the continuation of Box C.			
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