

19



Europäisches Patentamt  
European Patent Office  
Office européen des brevets



11 Publication number:

**0 214 472 B1**

12

### EUROPEAN PATENT SPECIFICATION

- 49 Date of publication of patent specification: 27.12.91
- 51 Int. Cl.<sup>5</sup>: H04M 9/08, H03G 3/00
- 21 Application number: 86110924.7
- 22 Date of filing: 07.08.86

54 Control circuit for use with electronic attenuators and method for providing a control signal proportional to absolute temperature.

30 Priority: 04.09.85 US 772482

43 Date of publication of application:  
18.03.87 Bulletin 87/12

49 Publication of the grant of the patent:  
27.12.91 Bulletin 91/52

84 Designated Contracting States:  
DE FR GB IT NL SE

58 References cited:  
EP-A- 0 082 755  
WO-A-84/03407  
DE-A- 3 148 202  
FR-A- 2 500 698

IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, vol. CE-31, no. 3, August 1985, pages 552-559, IEEE, New York, US; G. KLOESZ et al.: "Hands free telephone conversations are simplified using new integrated circuits"

73 Proprietor: MOTOROLA, INC.  
1303 East Algonquin Road  
Schaumburg, IL 60196(US)

72 Inventor: Pace, Wilson David  
1845 E. Tulane  
Tempe Arizona 85283(US)  
Inventor: Welty, Dennis L.  
1050 So. Longmore No.27  
Mesa Arizona 85202(US)

74 Representative: Hudson, Peter David et al  
Motorola Ltd. Patent and Licensing Operations - Europe Jays Close Viablies Industrial Estate  
Basingstoke Hampshire RG22 4PD(GB)

Note: Within nine months from the publication of the mention of the grant of the European patent, any person may give notice to the European Patent Office of opposition to the European patent granted. Notice of opposition shall be filed in a written reasoned statement. It shall not be deemed to have been filed until the opposition fee has been paid (Art. 99(1) European patent convention).

## Description

### Background of the Invention

The present invention relates to electronic attenuators and, more particularly, to a circuit and method for producing a control signal proportional to absolute temperature for controlling the attenuation of such circuits.

The automatic control of attenuator circuits is frequently required in the design of telephones and other audio products. For example, contemporary speakerphones are typically operated in a half-duplex mode of operation wherein transmission and reception of speech audio signals is not permitted simultaneously. Hence, at any particular time, the speakerphone is either in a transmit mode, idle mode or receive mode as is well understood. In order to provide the required half-duplex operation the speakerphone includes a pair of attenuators, a transmit attenuator circuit placed in the transmit signal path of the speakerphone and a receive attenuator circuit in the receive signal path thereof. The transmit and receive attenuators are controlled by a single circuit and are complimentary in function, i.e., in response to a control signal supplied from the control circuit, one is operated at maximum gain while the other is operated at maximum attenuation and vice versa. The two attenuators are never both on or both off. Thus, for instance, if the user is speaking, the speakerphone is placed in the transmit mode wherein the transmit attenuator is operated at maximum gain while the receive attenuator is operated at maximum attenuation to inhibit a receive signal from adversely affecting the transmission of the speech signal from the user. Similarly, in response to a controlling received audio signal the receive attenuator is automatically switched to maximum gain while the transmit attenuator is operated at maximum attenuation. In the idle mode, whenever audio signals are neither being transmitted or received, the gains of both the transmit and receive channels are reduced to some mid value thereof.

In at least one prior art speakerphone the transmit and receive attenuators, as well as, the attenuator control circuit, are manufactured in integrated circuit form. The transmit and receive attenuators in this prior art speakerphone are actually a pair of gain controlled two or four quadrant multiplier circuits which are well known to those skilled in the art. The two attenuators have a commonly connected gain control input to which the gain control signal is applied. The gain control inputs of the two multiplier circuits are such that, in the absence of a gain control signal, the transmit attenuator is operated at maximum gain while the receive attenuator is operated at maximum attenu-

ation.

A problem arises due to the temperature dependency of the multiplier circuits, i.e., the gain of the transmit and receive attenuators varies as a function of temperature. This is undesirable as the gains of each attenuator should remain constant in order to provide high quality speakerphone operation.

At least one manufacturer, in an attempt to provide temperature compensation, uses an external thermistor to produce a current internally to the integrated speakerphone circuit which has a predetermined temperature coefficient. This internally generated current produces a control voltage that drives the two attenuators. In this manner the control voltage has a temperature characteristic which approximates the gain verses temperature characteristics of the two attenuators. However, a problem exists with this temperature compensation scheme in that the control voltage does not precisely track the variations and the gain of the attenuators over temperature because the temperature characteristic of the external thermistor does not track that of the integrated circuit attenuator circuits. This can produce an undesirable offset between the relative gains of each of the two attenuators whereby the log gains thereof do not remain constant.

DE-A-3148202 discloses a control circuit in which two current sources are utilised separately to vary the gains of respective amplifiers. WO-A-403407 discloses a voice switch control for switching a current steering circuit and FR-A-2500698 discloses a differential amplifier in a hands free telephone circuit.

### Summary of the Invention

The present invention seeks to provide an attenuator control circuit suitable to be used in a speakerphone or the like having a pair of complimentary operated attenuators for producing a control voltage to vary the gains of the attenuators in which the above described problem is mitigated.

In accordance with the invention there is provided a control circuit for a half-duplex operated communication system including a pair of attenuators the gain/attenuation of which are varied in a complementary manner and which are a function of temperature; the control circuit providing a voltage control signal that is supplied to the pair of attenuators characterised by the voltage control signal having a temperature coefficient proportional to absolute temperature, the control circuit including:

a single current source which supplies a current (I) having a predetermined temperature coefficient;

a first steering circuit coupled to said current

source and being responsive to a direct current (dc) control signal (VLC) for proportionally steering said current to an output of the control circuit to generate the voltage control signal when said first steering circuit is rendered operative responsive to the system being in a first mode of operation;

a second current steering circuit being rendered operative responsive to the system being in a second mode of operation and which is coupled to said current source to proportionally steering current to said output to generate the voltage control signal in response to a control signal (XD) supplied to said second steering circuit;

a switch coupled between said first and second steering circuit and to said current source which is responsive to an applied control signal (T-R) for rendering said second steering circuit operative while rendering said first steering circuit inoperative so that the voltage control signal is developed by independent operation of said first and second steering circuits; and

a load coupled to the output of the control circuit across which voltage control signal is developed.

#### Brief Description of the Drawings

FIG. 1 is a partial block and schematic diagram illustrating a speakerphone including an attenuator control circuit of the present invention;

FIG. 2 is a partial schematic and block diagram illustrating the attenuator control circuit of the present invention; and

FIG. 3 is a detailed schematic diagram illustrating the attenuator control circuit of FIG. 2.

#### Detailed Description of the Preferred Embodiment

Turning to FIG. 1 there is shown a simplified block diagram of an integrated speakerphone circuit within dashed outline 10 which incorporates attenuator control circuit 12 of the present invention. As understood, the speakerphone provides hand free communication in a half-duplex mode operation. Half-duplex operation is provided by controlling the gain-attenuation of attenuators 14 and 16 which are respectively situated in transmit and receive signal paths of the speakerphone. The speakerphone, in a typical application, is coupled to telephone lines at terminals 18 and 20 to provide voice transmission and reception thereover. Attenuator control circuit 12 controls the gain of the two attenuators and is voice activated to place the speakerphone in either a transmit or receive mode accordingly. The transmit signal path comprises a microphone (not shown) coupled externally at input 22 to audio amplifier 24. The output of audio amplifier 24 is connected both to a background level

detector 26 and the input of transmit attenuator 14, the output of the latter being connected to output terminal 18. Likewise, the receive signal path includes receive attenuator 16 coupled between terminal 20 and audio amplifier 28. A speaker (not shown) is externally connected at terminal 30 to the output of audio amplifier 28. A source of operating potential  $V+$  is provided to the speaker phone at terminal 32 and is utilized to derive the power supply voltage  $V_{CC}$  for biasing the internal circuitry of the speakerphone. As will be described in more detail later, the speakerphone is voice switched to be operated in either a transmit mode or receive mode. Depending on the particular mode of operation, attenuator control circuit 12 provides a voltage via lead 34 that commonly drives transmit and receive attenuators 14 and 16 accordingly. As previously mentioned, transmitter and receive attenuators 14 and 16 are complementary in function, i.e., when one is operated at maximum gain the other is operated at maximum attenuation and vice versa.

The operation of attenuator control circuit 12 is controlled by the three control signals supplied thereto. These signals are, (1) a background level signal XDC supplied via lead 36, (2) a volume level control signal VLC supplied via lead 37, and (3) a control signal applied over lead 38 from T-R comparator 40. Volume control is provided by varying the potentiometer wiper arm of external potentiometer 42 and is connected at terminal 44 to the speakerphone. Thus, by increasing VLC by moving the wiper arm towards  $V_B$ , a modulated control voltage  $V$  is produced at the output of attenuator control circuit 12 via lead 34 to increase the gain of receive attenuator 16 while reducing the relative gain of transmit attenuator 14. The maximum gain/attenuation values of transmit and receive attenuators 14 and 16 are determined respectively by adjusting variable resistors 46 and 48 which are coupled to the attenuators at terminals 50 and 52.

In addition to operating in a transmit and receive mode, the speakerphone is operated in an idle mode. In the idle mode, no controlling speech signals are coupled to inputs 54 and 56. In this condition the outputs of transmit and receive level detectors 58 and 60, which are coupled respectively to terminals 54 and 56, cause the output of comparator 40 to be in a high level state. Simultaneously, the voltage at terminal 62 (established across capacitor 64 and resistor 66) is sufficiently low which in conjunction with the output of detector 26 also being in a low level state causes the output of attenuator control circuit 12 to be at a value that reduces the gain of the two attenuators to some midrange value thereof.

The speakerphone is placed in a transmit mode when there is sufficient transmit signal, such as speech signal, present over and above any

receive signal. In this event the relative outputs of transmit and receive level detectors 58 and 60 cause T-R comparator 40 to go to a high output level state. Simultaneously, background level detector 26 determines whether the transmit signal is a result of background noise or speech. If the signal is due to background noise, the attenuators are placed in the idle mode as described above. However, if the signal consists of speech, transmit and receive attenuators 14 and 16 will be switched to a transmit mode by background level detector 26 producing the appropriate control signal at the output of attenuator control circuit 12 to maximize the gain of transmit attenuator 14 while minimizing the gain of receive attenuator 16.

In a like manner, the speakerphone is placed in the receive mode by the output of attenuator control circuit 12 being switched accordingly when the output of T-R comparator 40 goes low. This occurs whenever there is sufficient receive signal supplied at terminal 20 to overcome any background noise and any speech signals appearing at output 18. In this situation the outputs of transmit and receive level detectors 58 and 60 are such to cause the output of T-R comparator 40 to go to its low level state. Hence, the gain of the receive attenuator 16 is maximized simultaneously with the gain of transmitted attenuator 14 being minimized by the output of attenuator control circuit 12.

As it is generally understood, transmit and receive attenuators 14 and 16 may be realized by conventional 2 or 4 quadrant multiplier circuits which are known to those skilled in the art. The gains of the attenuators are varied by varying the DC voltage drive supplied thereto accordingly. It is important, that the relative gains of attenuators 14 and 16 do not change over temperature. For the gain/attenuation of the multiplier circuits to have a zero temperature coefficient, the voltage which drives these multipliers must have a voltage temperature characteristics that varies in the same manner as that of the multipliers themselves. Attenuator control circuit 12 of the present invention provides such a voltage as will be hereinafter explained.

Turning now to FIG. 2 there is illustrated a simplified schematic and block diagram of attenuator control circuit 12 of the present invention. Thermal current reference 70 produces a current  $I$  having a predetermined temperature coefficient. The current  $I$  flows through either receive volume control circuit 74 or transmit gain control circuit 80 depending upon the state of electronic switch 72. For example, in response to the output of T-R comparator 40 being low, the current  $I$  flows through receive voltage control circuit 74 to develop the corresponding voltage  $V$  across resistor 76 which is proportional to absolute temperature.

Thus, the voltage at node 78 that appears over lead 34 to drive transmit and receive attenuators 14 and 16 has the same voltage-temperature characteristics as the attenuators. By varying the voltage VLC at input terminal 44 a proportional amount of current  $I$  is steered through resistor 76, i.e., the greater the value of VLC the greater amount of the current  $I$  that flows through resistor 76.

In response to the speakerphone being in a transmit mode, switch 72 renders receive volume control circuit 74 nonoperative while transmit gain control circuit 80 is rendered operative to permit the current  $I$  to flow through the latter. In the transmit mode the signal appearing on lead 36 is high such that none of the current  $I$  is steered to node 78, to flow through resistor 76. Hence, there is no voltage developed across resistor 76 and the voltage  $V$  developed is equal to zero. This results in the transmit and receive attenuators 14 and 16 being operated at maximum gain and maximum attenuation respectively as the voltage appearing at lead 34 is the quiescent operating voltage  $V_{CC}$ .

In the idle mode, the current  $I$  flows through transmit gain control circuit 80 as during the transmit mode. However, in this event a current is steered to node 78 to produce a voltage drop across resistor 76 that is proportional to the value of the voltage appearing at node 62 produced across resistor 66 and capacitor 64. The voltage drop across resistor 76 in the idle mode is also proportional to absolute temperature such that the gains of the respective transmit and receive attenuators is not temperature dependent.

Referring now to FIG. 3, attenuator control circuit 12 is shown in full detail. Thermal current source 70 includes PNP transistor 82 interconnected with NPN transistors 84, 86, 88 and 90 to provide the thermal current  $I$  at node 92. Current source 70 is conventional in structure and the operation thereof is understood by those skilled in the art. Briefly, however, the emitter of transistor 82 is coupled via resistor 94 and 96 to  $V_{CC}$ , a source of substantially constant potential having a zero temperature coefficient. Transistor 82 is biased on by its base being biased at a potential less than  $V_{CC}$  whereby equal currents flow from the two collectors thereof to the respective collectors of NPN transistors 84 and 86. Transistor 84 is connected as a diode and forms with transistor 86 a conventional current mirror circuit whereby the current flowing through transistor 86 is substantially equal to the current flowing through the diode connected transistor 84. The emitters of transistors 84 and 86 are coupled respectively through resistors 98 and 100 to ground reference. By area ratioing the effective emitter areas of transistors 84 and 86 the current densities of the two are substantially different. For example, by making the effective area of transistor

84 larger than the effective emitter area of transistor 86 a smaller base to emitter voltage is developed in the former which causes a delta voltage to be developed across resistor 98 which has a positive temperature coefficient. This voltage produces a current through resistor 98 which flows through Darlington configured transistors 88 and 90 to node 92. This current will also have a positive temperature coefficient due to the fact that it was produced by a positive temperature voltage established across resistor 98. Darlington configured transistors 88 and 90 also provide feedback from the collector of transistor 86 to the emitter of transistor 84 to ensure that the two collector currents flowing through transistors 84 and 86 are maintained substantially constant. Thus, a thermal current  $I$  is produced at node 92 which is steered to either receive volume control circuit 74 or transmit gain control circuit 80.

Transistor 102 which is coupled between receive volume control circuit 74 and transmit gain control circuit 80 to node 92 functions as an electronic switch whereby current is steered to one or the other of the two aforementioned circuits. For example, if the output of T-R comparator 40 is high, thermal current  $I$  flows through the current mirror comprising PNP transistors 106 and 108 and resistors 110 and 112 as transistor 102 is turned on. In this condition, the voltage developed at the emitter of transistor 102, which is one diode voltage drop below the voltage applied at the base thereof, is sufficient to render receive volume control circuit 74 nonoperative. Transmit gain control circuit 80 is now rendered operative whereby the current at node 92 flows through transistor 108 of the current mirror to produce a current through transistor 106 which is substantially equal in magnitude to the current flowing at node 92 and has the same temperature coefficient thereas. The current flowing through transistor 106 provides the "tail" current required by the comparator comprising differentially connected transistors 114 and 116, the emitters of which are commonly connected to the collector of transistor 106. Depending upon the output state of background level detector circuit 26 supplied via lead 36 to the input of the comparator at the base of transistors 118, and the voltage established at terminal 62, the collector current of transistor 106 is steered proportionally between transistors 114 and 116. As previously mentioned, with the speakerphone in a transmit mode, a high level output signal is supplied over lead 36 which will reverse bias PNP transistors 118 and 120, which are connected in a Darlington configuration and via resistor 122 to the base of transistor 114. Since these transistors are reverse biased, transistor 114 is rendered nonconductive as there is no path for base current flow therefrom. Therefore all

of the tail current supplied from transistor 106 is steered through transistor 116 to ground reference through the collector of this transistor. Transistor 116 is biased in a conducting state since its base is connected at node 124 to which a substantially constant bias potential is established. The bias potential is developed through diodes 126 and 128 which are series connected between  $V_{CC}$  and the base of transistor 116. Hence, current flows through the two diodes and via resistor 130 through Darlington configured PNP transistors 132 and 134 to ground reference. Since all of the tail current supplied at the common emitters of transistors 114 and 116 of the comparator is steered to ground, in this case one output of the comparator, there is no current flowing through node 78 and resistor 76 (FIG. 2). Thus, transmit attenuator 14 will be operated at maximum gain as previously described with receive attenuator 16 being set at maximum attenuation.

When the speakerphone is in a receive mode, the output of T-R comparator 40 is low which turns off transistor 102. Hence, no tail current is supplied to the transmit comparator. Thus, transmit gain control circuit 80 does not have any control over attenuator control circuit 12.

With the speakerphone operated in an idle mode, the input to the comparator, at the base of transistor 118, is a function of the voltage XDC. If XDC is less than the voltage established at node 124 transistors 118, 120 and 114 are turned on as a path for base current drive is now provided to transistor 114 through resistor 122. Diode 126 provides current flow therethrough from diode 136 through resistor 122. As transistor 114 is turned on one-half of the tail current supplied by transistor 106 is steered through the current mirror comprising NPN transistors 138 and 140. Transistor 138 is connected as a diode and has its emitter connected through resistor 142 to ground reference. Similarly, transistor 140 has its base connected to the collector-base of transistor 138 and its emitter coupled through resistor 144 to ground. The current steered through transistor 114 is mirrored at the collector of transistor 140 such that this current now flows through node 78 and resistor 76 to produce the modulated voltage  $V$  thereacross. The current flowing through node 78 has substantially the same temperature coefficient as the current produced by thermal current reference source 70. The magnitude of current steered at the output of the comparator (the collector of transistor 140) is a function of magnitude of the voltage  $VXDC$ .

As illustrated, receive volume control circuit 74 includes a differential amplifier circuit comprising NPN transistors 146 and 148 the emitters of which are commonly connected via resistors 150 and 152. A source of tail current is supplied to this differen-

tial amplifier at the interconnection of the two resistors by transistor 154 which has its collector emitter path coupled therebetween to ground. The base of transistor 154 is biased at a bias potential that is developed across diode 156. The bias current in diode 156 will have a zero temperature coefficient since the current through the diode is provided by transistor 158 via resistor 96 which are series coupled between  $V_{CC}$  and the diode assuming that the current through resistor 94 is negligibly small in comparison to the current flow through resistor 96. Since the voltage  $V_{CC}$  has a zero temperature coefficient the current through resistor 96, transistor 158 and diode 156 also has a zero temperature coefficient. The base of transistor 158 is connected with the base of PNP transistor 82 to the interconnection between the emitter of transistor 160 and resistor 162, the other side of which is connected to ground reference. The voltage resistor divider comprising resistor 164 and 166 connected between  $V_{CC}$  and ground reference produces the potential,  $V_B$ , which is supplied at the base of transistor 160 and is substantially constant having a zero temperature coefficient. As illustrated, the base of transistor 146 is also connected to  $V_B$  with the base of transistor 148 being connected at terminal 44 to the potentiometer 42. The external bias voltage VLC, applied to the base of transistor 148, also has a zero temperature coefficient such that the operation of the differential amplifier is independent of temperature. The outputs of the differential amplifier (the collectors of transistors 146 and 148) are connected through respective diodes 174 and 176 to  $V_{CC}$  through diode 178.

Receive volume control circuit 74 also includes differential amplifier 168 formed by NPN transistors 170 and 172 the emitters of which are commonly connected to node 92 to thermal current reference source 70. The base electrodes of these two transistors are connected respectively to the collectors of transistors 146 and 148. The outputs of the differential amplifier are taken at the collectors of transistors 170 and 172 with the former being connected to node 78 to resistor 76 and the latter being connected to  $V_{CC}$ .

With transistor 102 in a nonconducting state the current I appearing at node 92 is steered to the common emitters of transistors of 170 and 172 of differential amplifier 168. This renders receive volume control 74 operative whereby the current flowing through transistors 170 and 172 is proportionally steered therethrough to node 78 and  $V_{CC}$  depending on the magnitude of VLC. By varying VLC the magnitude of the current flowing through transistor 170 can be varied with respect to the current flowing through transistor 172. For example, as VLC is increased, diode 176 is turned on harder to supply more collector current to transistor 148 which, in

turn, causes transistor 172 to be rendered less conductive. This will allow more of the current I to be steered through transistor 170 to node 78 to increase the voltage V developed across resistor 76. Conversely, as VLC is decreased, diode 174 conducts harder which reduces the degree of conduction of transistor 170 as well as the magnitude of current I that is steered to node 78. It is understood that the proportional amount of current I steered to node 78 can be varied by ratioing resistors 150 and 152 accordingly.

The speakerphone is therefore operated in a receive mode whenever transistor 102 is turned off. Also, by varying VLC the volume of the receive signal path is increased as the voltage drive signal produced at node 78 and via lead 34 to transmit and receive attenuators 14 and 16 decreases which increases the gain of receive attenuator 16 while increasing the attenuation of the transmit attenuator 14.

Hence, what has been described above is a novel control circuit for producing a voltage drive signal that is proportional to absolute temperature. The control circuit can be used in many applications including controlling the gain/attenuation of the receive and transmit signal paths of a half-duplex operated speaker phone system.

#### Claims

1. A control circuit for a half-duplex operated communication system including a pair of attenuators the gain/attenuation of which are varied in a complementary manner and which are a function of temperature; the control circuit (12) providing a voltage control signal that is supplied to the pair of attenuators characterised by the voltage control signal having a temperature coefficient proportional to absolute temperature, the control circuit including:

a single current source (70) which supplies a current (I) having a predetermined temperature coefficient;

a first steering circuit (74) coupled to said current source and being responsive to a direct current (dc) control signal (VLC) for proportionally steering said current to an output of the control circuit to generate the voltage control signal when said first steering circuit is rendered operative responsive to the system being in a first mode of operation;

a second current steering circuit (80) being rendered operative responsive to the system being in a second mode of operation and which is coupled to said current source for proportionally steering current to said output to generate the voltage control signal in response to a control signal (XD) supplied to said sec-

ond steering circuit;

a switch (72) coupled between said first and second steering circuit and to said current source which is responsive to an applied control signal (T-R) for rendering said second steering circuit operative while rendering said first steering circuit inoperative so that the voltage control signal is developed by independent operation of said first and second steering circuits; and

a load coupled to the output of the control circuit across which voltage control signal is developed.

2. The circuit of claim 1 wherein said switch circuit (72) includes a transistor (102) having first, second and control electrodes, said first electrode being coupled both to said current source (70) and said first current steering circuit (74), said second electrode being coupled to said second current steering circuit (80), said control electrode receiving said control signal, said transistor (102) providing said current to said second current steering circuit and rendering said first current steering
3. The circuit of claim 2 including a first power supply conductor at which is supplied an operating potential; and a resistor (76) coupled between said first power supply conductor and the output (34) of the circuit.
4. The circuit of claim 3 wherein said first current steering circuit (74) includes:
  - a first differential amplifier (146, 148) having first and second inputs and outputs, said first input being coupled to an input terminal (44) of the circuit (12) at which is supplied a second control signal, said second input being supplied a substantially constant bias potential ( $V_B$ ); and
  - a second differential amplifier (168) having first and second inputs and outputs and a common terminal, said common terminal being coupled to said first electrode of said transistor (102) and to said current source (70), said first and second inputs being coupled respectively to said first and second outputs of said first differential amplifier, said first output being said output of said first current steering circuit and said second output being coupled to a said first power supply conductor.
5. The circuit of claim 4 wherein said first current steering circuit (74) includes first (174) and second (176) diodes coupled between said first power supply conductor and said first and second inputs respectively of said second dif-

ferential amplifier (168).

6. The circuit of claim 5 wherein said first current steering circuit includes a third diode (178) connected between said first power supply conductor and said first and second diodes.
7. The circuit of claim 2 wherein said second current steering circuit includes:
  - a first current mirror circuit (106, 108) coupled to said second electrode of said transistor (102) which is responsive to said transistor (102) being turned on for providing current at an output thereof the magnitude and temperature coefficient of which are substantially the same as said current provided by said current source (70); and
  - a comparator (114, 116) having first and second inputs and outputs and a common terminal, said common terminal being coupled to said output of said first current mirror circuit, said first and second outputs being said first and second outputs of said second current steering circuit respectively, said first input being biased at a first bias potential, said second input receiving a second control signal such that current related to said first current mirror circuit output current is steered to said first output when said second control signal is at a first level and steered to said second output when said second control signal is at a second level.
8. The circuit of claim 7 wherein said comparator includes second current mirror circuit (138, 140) for providing said related current to said first output of said comparator.
9. The circuit of claim 8 including:
  - first and second power supply conductors at which are supplied an operating potential and ground reference signal respectively;
  - a resistor (76) connected between said first power supply conductor and the output (34) of the circuit (12); and
  - said second output of said comparator being coupled to said second power supply conductor;

## Revendications

1. Circuit de commande pour un système de communication qui fonctionne en semi-duplex, ce circuit incluant une paire d'atténuateurs dont le gain et l'atténuation sont amenés à varier selon une manière complémentaire et qui sont une fonction de la température ; le circuit de commande (12) qui fournit un signal

de commande de tension qui est acheminé à la paire d'atténuateurs étant caractérisé en ce que le signal de commande de tension a un coefficient de température qui est proportionnel à la température absolue, le circuit de commande comprenant :

une unique source de courant (70) qui achemine un courant (I) qui a un coefficient de température prédéterminé ;

un premier circuit d'aiguillage (74) qui est couplé à ladite source de courant et qui est sensible à un signal de commande de courant continu (VLC) pour aiguiller proportionnellement ledit courant jusqu'à une sortie du circuit de commande afin de générer le signal de commande de tension lorsque ledit premier circuit d'aiguillage est rendu actif en réponse au système qui est dans un premier mode de fonctionnement ;

un second circuit d'aiguillage de courant (80) qui est rendu actif en réponse au système qui est dans un second mode de fonctionnement et qui est couplé à ladite source de courant pour aiguiller proportionnellement un courant jusqu'à ladite sortie afin de générer le signal de commande de tension en réponse à un signal de commande (XD) qui est acheminé jusqu'audit second circuit d'aiguillage ;

un commutateur (72) qui est couplé entre lesdits premier et second circuits d'aiguillage et à ladite source de courant, ce commutateur étant sensible à un signal de commande qui lui est appliqué (T-R) pour rendre ledit second circuit d'aiguillage actif tout en rendant ledit premier circuit d'aiguillage non actif de telle sorte que le signal de commande de tension soit développé par un fonctionnement indépendant desdits premier et second circuits d'aiguillage ; et

une charge qui est couplée à la sortie du circuit de commande aux bornes duquel le signal de commande de tension est développé.

2. Circuit selon la revendication 1, dans lequel ledit circuit de commutation (72) comprend un transistor (102) qui a des première et seconde électrodes ainsi qu'une électrode de commande, ladite première électrode étant couplée à la fois à ladite source de courant (70) et audit premier circuit d'aiguillage de courant (74), ladite seconde électrode étant couplée audit second circuit d'aiguillage de courant (80), ladite électrode de commande recevant ledit signal de commande, ledit transistor (102) fournissant ledit courant audit second circuit d'aiguillage de courant et rendant ledit premier circuit d'aiguillage de courant non opérationnel lorsque

5

ledit transistor est rendu conducteur par ledit signal de commande, ledit circuit de commutation rendant ledit second circuit d'aiguillage de courant non opérationnel lorsque ledit transistor est rendu non conducteur et ce faisant, ledit courant délivré par ladite source de courant circule au travers dudit premier circuit d'aiguillage de courant.

10

3. Circuit selon la revendication 2, comprenant un premier conducteur d'alimentation auquel est appliqué un potentiel de fonctionnement ; et une résistance (76) qui est couplée entre ledit premier conducteur d'alimentation et la sortie (34) du circuit.

15

4. Circuit selon la revendication 3, dans lequel ledit premier circuit d'aiguillage de courant (74) comprend :

20

un premier amplificateur différentiel (146, 148) qui a des premières et secondes entrées et sorties, ladite première entrée étant couplée à une borne d'entrée (44) du circuit (12) auquel est acheminé un second signal de commande, ladite seconde entrée se voyant appliquer un potentiel de polarisation sensiblement constant ( $V_B$ ) ; et

25

un second amplificateur différentiel (138) qui a des premières et secondes entrées et sorties ainsi qu'une borne commune, ladite borne commune étant couplée à ladite première électrode dudit transistor (102) et à ladite source de courant (70), lesdites première et seconde entrées étant couplées respectivement auxdites première et seconde sorties dudit premier amplificateur différentiel, ladite première sortie étant ladite sortie dudit premier circuit d'aiguillage de courant et ladite seconde sortie étant couplée audit premier conducteur d'alimentation.

30

35

5. Circuit selon la revendication 4, dans lequel ledit premier circuit d'aiguillage de courant (74) inclut des première (174) et seconde (176) diodes qui sont couplées entre ledit premier conducteur d'alimentation et lesdites première et seconde entrées respectives dudit second amplificateur différentiel (168).

45

50

6. Circuit selon la revendication 5, dans lequel ledit premier circuit d'aiguillage de courant inclut une troisième diode (178) qui est connectée entre ledit premier conducteur d'alimentation et lesdites première et seconde diodes.

55

7. Circuit selon la revendication 2, dans lequel ledit second circuit d'aiguillage de courant inclut :

8

un premier circuit miroir de courant (106, 108) qui est couplé à ladite seconde électrode dudit transistor (102) et qui est sensible au fait que ledit transistor (102) est passant pour fournir un courant au niveau d'une de ses sorties, l'amplitude et le coefficient de température de ce courant étant sensiblement les mêmes que ceux dudit courant fourni par ladite source de courant (70) ; et

un comparateur (114, 116) qui a des premières et secondes entrées et sorties ainsi qu'une borne commune, ladite borne commune étant couplée à ladite sortie dudit premier circuit miroir de courant, lesdites première et seconde sorties étant respectivement lesdites première et seconde sorties dudit second circuit d'aiguillage de courant, ladite première entrée étant polarisée selon un premier potentiel de polarisation, ladite seconde entrée recevant un second signal de commande de telle sorte qu'un courant relatif au courant de sortie dudit premier circuit miroir de courant soit aiguillé vers ladite première sortie lorsque ledit second signal de commande est à un premier niveau et soit aiguillé vers ladite seconde sortie lorsque ledit second signal de commande est à un second niveau.

8. Circuit selon la revendication 7, dans lequel ledit comparateur inclut un second circuit miroir de courant (138, 140) pour fournir ledit courant relatif à ladite première sortie dudit comparateur.
9. Circuit selon la revendication 8, incluant :
  - des premier et second conducteurs d'alimentation auxquels sont respectivement appliqués un potentiel de fonctionnement et un signal de référence de masse ;
  - une résistance (78) qui est connectée entre ledit premier conducteur d'alimentation et la sortie (34) du circuit (12) ; et
  - ladite seconde sortie dudit comparateur étant couplée audit second conducteur d'alimentation.

#### Patentansprüche

1. Steuerschaltung für ein im Halbduplexbetrieb arbeitendes Nachrichten-Übertragungssystem mit zwei Dämpfungsgliedern, deren Verstärkungs-/Dämpfungsmaß komplementär abgeändert werden kann, und die temperaturabhängig sind, wobei das von der Steuerschaltung (12) gelieferte Spannungssteuersignal den beiden Dämpfungsgliedern zugeleitet wird, dadurch gekennzeichnet, daß das Spannungssteuersignal einen Temperatur-Koeffizienten aufweist, der der absoluten Temperatur proportional ist, und daß die Steuerschaltung die folgenden Bestandteile enthält:
  - eine einzelne Stromquelle (70), die einen Strom (I) mit einem vorgegebenen Temperatur-Koeffizienten zuführt,
  - eine erste an der Stromquelle angeschlossene Leitschaltung (74), die auf ein Gleichstromsteuersignal (VLC) anspricht, um den Strom proportional zu einer Ausgangsklemme der Steuerschaltung zu leiten und das Spannungssteuersignal zu erzeugen, wenn die erste Leitschaltung auf das sich in einer ersten Arbeitsweise befindende System ansprechend gemacht wird,
  - eine zweite Stromleitschaltung (80) die auf das sich in einer zweiten Arbeitsweise befindende System ansprechend gemacht wird, und die an die Stromquelle angeschlossen ist, um Strom proportional zu der Ausgangsklemme zu leiten und auf ein der zweiten Leitschaltung zugeführtes Steuersignal (XD) hin das Spannungssteuersignal zu erzeugen,
  - einen zwischen den beiden Leitschaltungen an der Stromquelle angeschlossenen Schalter (72), der auf ein angelegtes Steuersignal (T-R) anspricht, um die zweite Leitschaltung in Betrieb zu nehmen, während die erste Leitschaltung abgeschaltet wird, so daß das Spannungssteuersignal bei einer unabhängigen Arbeitsweise der ersten und zweiten Leitschaltung entwickelt wird, und
  - eine Last, die an der Ausgangsklemme der Steuerschaltung angeschlossen ist, und an der das Spannungssteuersignal entwickelt wird.
2. Schaltung des Anspruches 1, bei der der elektronische Schalter (72) einen Transistor (102) mit zwei Elektroden und einer Steuerelektrode enthält, unter denen die erste Elektrode sowohl an der Stromquelle (70) als auch an der ersten Stromleitschaltung (74) und die zweite Elektrode an der zweiten Stromleitschaltung (80) angeschlossen ist, während die Steuerelektrode das Steuersignal empfängt, und bei der der Transistor (102) den Strom an die zweite Stromleitschaltung liefert und die erste Stromleitschaltung abschaltet, wenn er von dem Steuersignal leitend gemacht ist, und der elektronische Schalter die zweite Stromleitschaltung abschaltet, wenn der Transistor nicht leitend gemacht ist, so daß der von der Stromquelle gelieferte Strom durch die erste Stromleitschaltung fließt.
3. Schaltung des Anspruches 2 mit einem ersten stromführenden Leiter, dem ein Arbeitspotential zugeleitet wird, und mit einem Widerstand

- (76), der zwischen dem ersten stromführenden Leiter und der Ausgangsklemme (34) der Schaltung angeschlossen ist.
4. Schaltung des Anspruches 3, bei der zu der ersten Stromleitschaltung (74) gehören:
- ein erster Differenz-Verstärker (146, 148) mit zwei Eingangsklemmen und einer Ausgangsklemme, von denen die erste Eingangsklemme an einer Eingangsklemme (44) der Schaltung (12) angeschlossen ist, der ein zweites Steuersignal zugeleitet wird, und die zweite Eingangsklemme mit einem im wesentlichen konstanten Potential ( $V_B$ ) einer Vorspannungsquelle gespeist wird, und
  - ein zweiter Differenz-Verstärker (168) mit zwei Ein-/Ausgangsklemmen und einer gemeinsamen Anschlußklemme, die an der ersten Elektrode des Transistors (102) und an der Stromquelle (70) angeschlossen ist, während die beiden Eingangsklemmen jeweils mit den beiden Ausgangsklemmen des ersten Differenz-Verstärkers verbunden sind, die erste Ausgangsklemme die Ausgangsklemme der ersten Stromleitschaltung ist und die zweite Ausgangsklemme an dem ersten stromführenden Leiter angeschlossen ist.
5. Schaltung des Anspruches 4, bei der die erste Stromleitschaltung (74) zwei Dioden (174 und 176) enthält, die zwischen dem ersten stromführenden Leiter und jeweils der ersten bzw. zweiten Eingangsklemme des zweiten Differenz-Verstärkers (168) angeschlossen sind.
6. Schaltung des Anspruches 5, bei der die erste Stromleitschaltung eine dritte Diode (178) enthält, welche zwischen dem ersten stromführenden Leiter und den beiden Dioden angeschlossen ist.
7. Schaltung des Anspruches 2, bei der die zweite Stromleitschaltung die folgenden Bestandteile enthält:
- eine erste an der zweiten Elektrode des Transistors (102) angeschlossene Stromreflexionsschaltung (106, 108), die auf den gerade eingeschalteten Transistor (102) anspricht, um einen Strom an seiner einen Ausgangsklemme abzugeben, dessen Größe und Temperatur-Koeffizient im wesentlichen dieselben wie die des von der Stromquelle (70) gelieferten Stromes sind, und
  - einen Komparator (114, 116) mit zwei Ein- und Ausgangsklemmen und einer gemeinsamen Klemme, die an der Ausgangsklemme der ersten Stromreflexionsschaltung angeschlossen
- sen ist, wobei seine beiden Ausgangsklemmen jeweils die beiden Ausgangsklemmen der zweiten Stromleitschaltung sind und seine erste Eingangsklemme auf ein Potential einer ersten Vorspannungsquelle gebracht ist und seine zweite Eingangsklemme ein zweites Steuersignal empfängt, so daß der Strom, der zu dem von der ersten Stromreflexionsschaltung abgegebenen Strom in Beziehung steht, zu der ersten Ausgangsklemme geleitet wird, wenn sich das zweite Steuersignal auf einem ersten Niveau befindet, und zu der zweiten Ausgangsklemme geleitet wird, wenn sich das zweite Steuersignal auf einem zweiten Niveau befindet.
8. Schaltung des Anspruches 7, bei der der Komparator eine zweite Stromreflexionsschaltung (138, 140) zur Lieferung des in Beziehung stehenden Stromes an die erste Ausgangsklemme des Komparators enthält.
9. Schaltung des Anspruches 8 mit zwei stromführenden Leitern, denen jeweils ein Arbeitspotential bzw. ein Bezugssignal an Erde zugeleitet werden, und mit einem Widerstand (76), der zwischen dem ersten stromführenden Leiter und der Ausgangsklemme (34) der Schaltung (12) angeschlossen ist, wobei die zweite Ausgangsklemme des Komparators mit dem zweiten stromführenden Leiter verbunden ist.

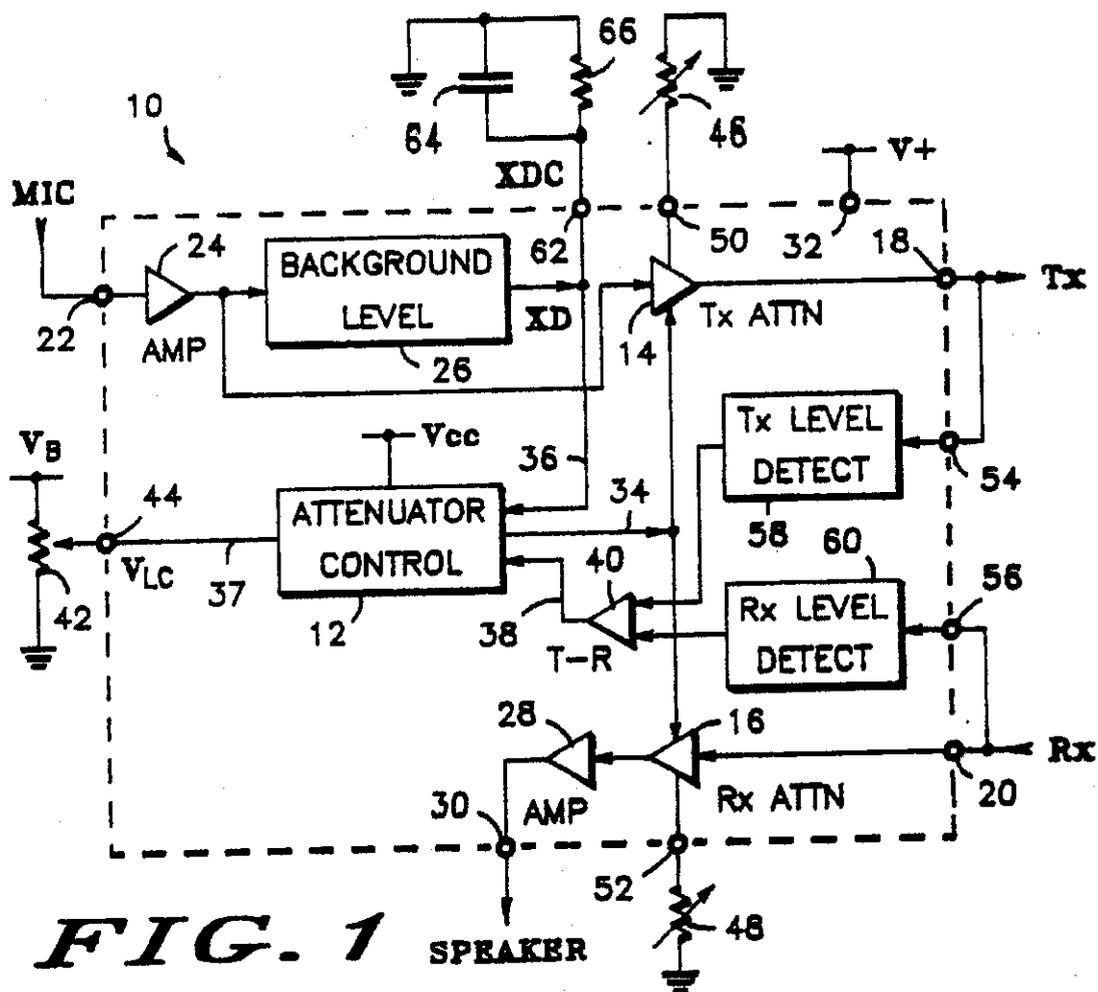


FIG. 1

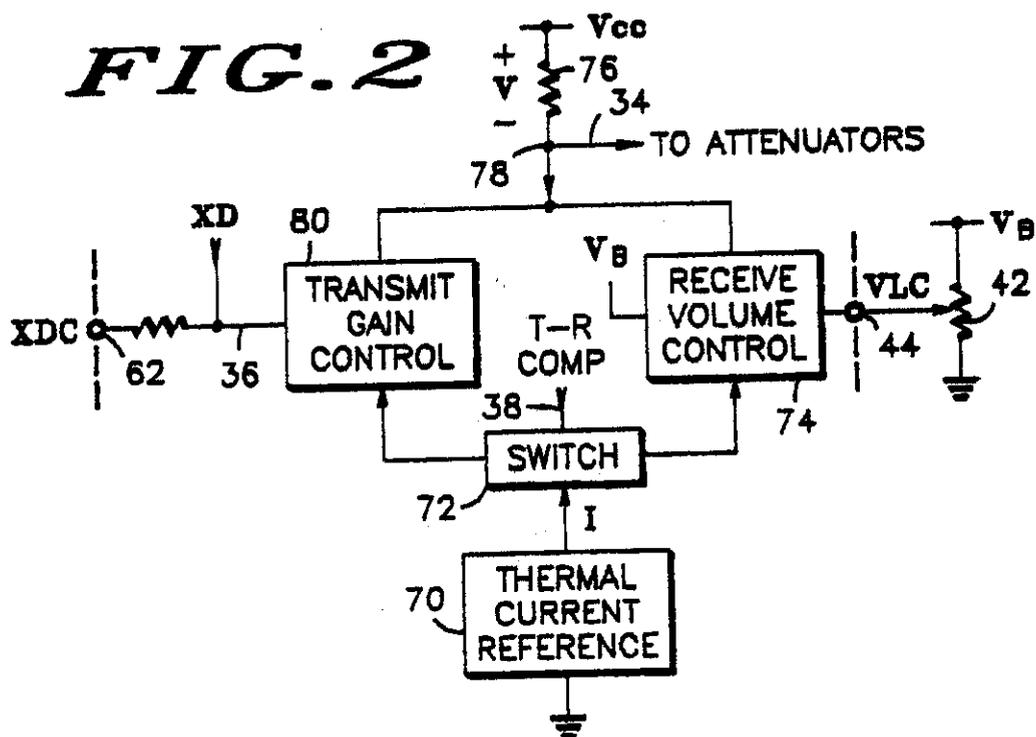


FIG. 2

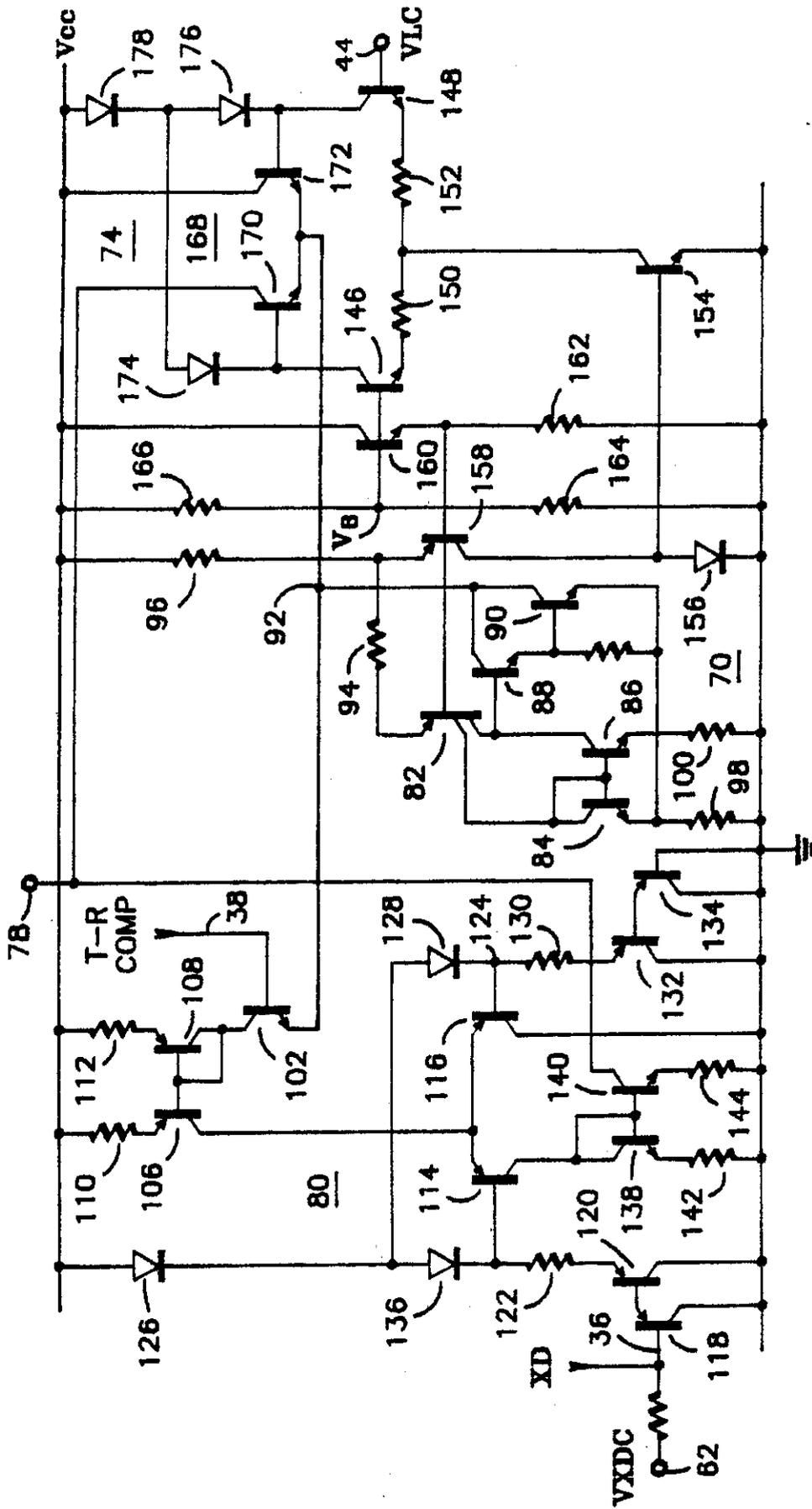


FIG. 3

REGISTER ENTRY FOR EP0214472

European Application No EP86110924.7 filing date 07.08.1986

Priority claimed:

04.09.1985 in United States of America - doc: 772482

Designated States DE FR GB IT NL SE

Title CONTROL CIRCUIT FOR USE WITH ELECTRONIC ATTENUATORS AND METHOD FOR PROVIDING A CONTROL SIGNAL PROPORTIONAL TO ABSOLUTE TEMPERATURE

Applicant/Proprietor

MOTOROLA, INC., 1303 East Algonquin Road, Schaumburg, Illinois 60196,  
United States of America [ADP No. 50019769003]

Inventors

WILSON DAVID PACE, 1845 E. Tulane, Tempe Arizona 85283, United States of  
America [ADP No. 54668553001]

DENNIS L. WELTY, 1050 So. Longmore No.27, Mesa Arizona 85202, United  
States of America [ADP No. 54668561001]

Classified to

H3G

H04M H03G

Address for Service

MOTOROLA, European Intellectual Property Operation, Jays Close, Viabes  
Industrial Estate, BASINGSTOKE, Hampshire, RG22 4PD, United Kingdom  
[ADP No. 00001180001]

EPO Representative

PETER DAVID HUDSON, Motorola Ltd. Patent and Licensing Operations - Europe  
Jays Close Viabes Industrial Estate, Basingstoke Hampshire RG22 4PD,  
United Kingdom [ADP No. 50756667001]

Publication No EP0214472 dated 18.03.1987

Publication in English

Examination requested 07.08.1986

Patent Granted with effect from 27.12.1991 (Section 25(1)) with title CONTROL  
CIRCUIT FOR USE WITH ELECTRONIC ATTENUATORS AND METHOD FOR PROVIDING A  
CONTROL SIGNAL PROPORTIONAL TO ABSOLUTE TEMPERATURE.

---

15.07.1988 EPO: Search report published on 20.07.1988

Entry Type 25.11 Staff ID.

Auth ID. EPT

23.07.1991 MOTOROLA, European Intellectual Property Operation, Jays Close,  
Viabes Industrial Estate, BASINGSTOKE, Hampshire, RG22 4PD, United  
Kingdom [ADP No. 00001180001]

registered as address for service

Entry Type 8.11 Staff ID. AC2

Auth ID. AA

25.11.1991 Notification from EPO of change of Applicant/Proprietor details  
from  
MOTOROLA, INC., 1303 East Algonquin Road, Schaumburg, Illinois  
60196, United States of America [ADP No. 50019769003]  
to  
MOTOROLA, INC., 1303 East Algonquin Road, Schaumburg, IL 60196,  
United States of America [ADP No. 50019769003]  
Entry Type 25.14 Staff ID. RD06 Auth ID. EPT

\*\*\*\* END OF REGISTER ENTRY \*\*\*\*

OA80-01  
EP

OPTICS - PATENTS

14/12/93 10:39:42  
PAGE: 1

RENEWAL DETAILS

PUBLICATION NUMBER

EP0214472

PROPRIETOR(S)

MOTOROLA, INC., 1303 East Algonquin Road, Schaumburg, IL 60196  
United States of America

DATE FILED	07.08.1986
DATE GRANTED	27.12.1991
DATE NEXT RENEWAL DUE	07.08.1994
DATE NOT IN FORCE	
DATE OF LAST RENEWAL	15.07.1993
YEAR OF LAST RENEWAL	08
STATUS	PATENT IN FORCE