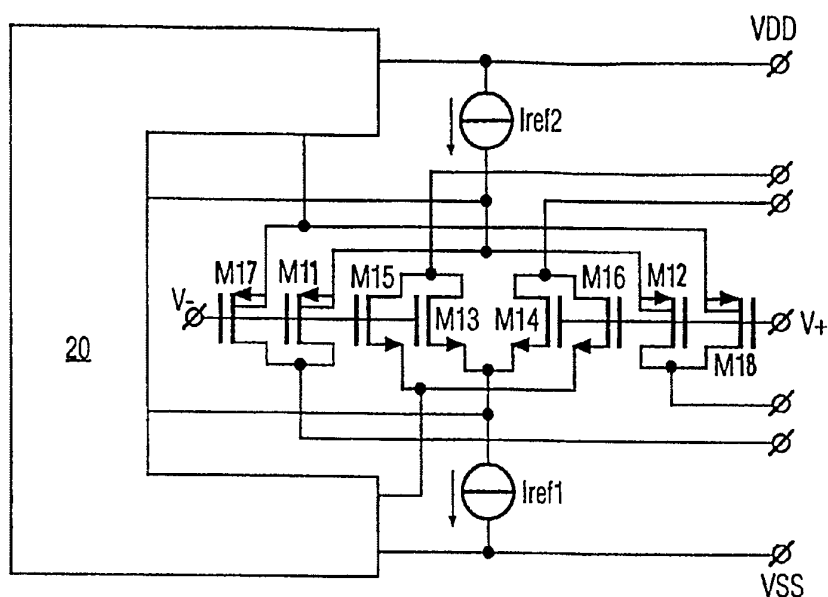




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(71) Applicant: PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).		(88) Date of publication of the international search report: 16 January 1997 (16.01.97)	
(71) Applicant (for SE only): PHILIPS NORDEN AB [SE/SE]; Kottbygatan 5, Kista, S-164 85 Stockholm (SE).			
(72) Inventors: HOGERVORST, Ronald; Koperwieklaan 100, NL-2251 NZ Voorschoten (NL). HUIJSING, Johan, Hendrik; 't Wandt 10, NL-2636 HD Schipluiden (NL). TERO, John, P.; 19234 Bellwood Drive, Saratoga, CA 95070 (US).			
(74) Agent: HESSELMANN, Gerardus, J., M.; Internationaal Octrooibureau B.V., P.O. Box 220, NL-5600 AE Eindhoven (NL).			

(54) Title: RAIL-TO-RAIL INPUT STAGES WITH g_m -CONTROL BY MULTIPLE INPUT PAIRS

(57) Abstract

A transconductance (g_m) control circuit for bipolar or CMOS rail-to-rail input stages is provided. The g_m is controlled by the use of multiple input pairs and can be used in CMOS or bipolar technology. In CMOS the g_m -control works regardless of the operating region of the MOS transistor, whether it is weak, moderate or strong inversion.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/IB 96/00179

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H03F 3/45

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

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IPC6: H03F, H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EDOC, JAPIO

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 4555673 A (JOHAN H. HUIJSING ET AL), 26 November 1985 (26.11.85) --	1
A	IEEE International Solid-State Circuits Conference, Volume, February 1994, Ron Hogervorst et al, "A Compact Power-Efficient 3V CMOS Rail-to-Rail Input/Output Operational Amplifier for VLSI Cell Libraries" page 244 - page 245 -- -----	1-6



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Date of the actual completion of the international search

Date of mailing of the international search report

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Name and mailing address of the ISA/
Swedish Patent Office
Box 5055, S-102 42 STOCKHOLM
Facsimile No. +46 8 666 02 86

Authorized officer

Bo Gustavsson
Telephone No. +46 8 782 25 00

Information on patent family members

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