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(12) **United States Design Patent**
Reynard et al.

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- (54) **SEMI-AUTOMATED INTERCONNECTOR**
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- (73) Assignee: **Duraloc Limited**, New Yorkshire (GB)
- (**) Term: **14 Years**
- (21) Appl. No.: **29/443,415**
- (22) Filed: **Jan. 17, 2013**

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- (30) **Foreign Application Priority Data**
- Nov. 14, 2012 (GB) PCT/GB2012/052821
- (51) **LOC (10) Cl.** **08-07**
- (52) **U.S. Cl.**
- USPC **D8/343**; D8/382
- (58) **Field of Classification Search**
- USPC D8/330, 331, 343, 344, 349, 382;
70/431, 466; 292/2, 209, 346; 411/511,
411/337; 24/287; 403/315, 316
- See application file for complete search history.

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(57) **CLAIM**
The ornamental design for a semi-automated interconnector, as shown and described.

(56) **References Cited**

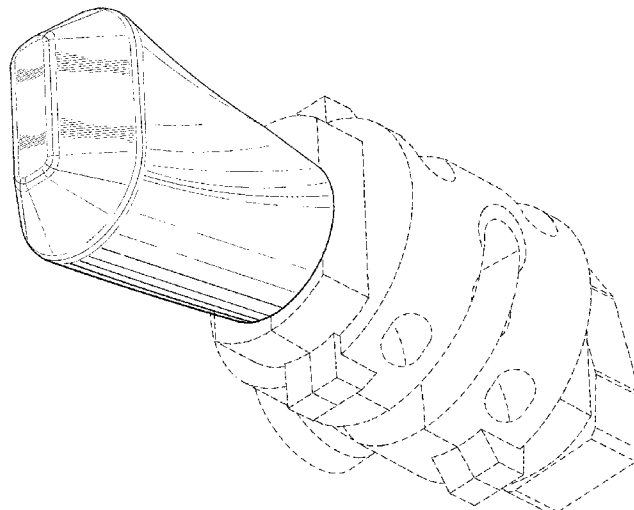
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DESCRIPTION

FIG. 1 is a perspective view of a semi-automated interconnector showing the our design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a right elevation view thereof;
FIG. 5 is a left elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
The broken lines environment is for illustrative purpose only and forms no part of the claimed design.

1 Claim, 4 Drawing Sheets



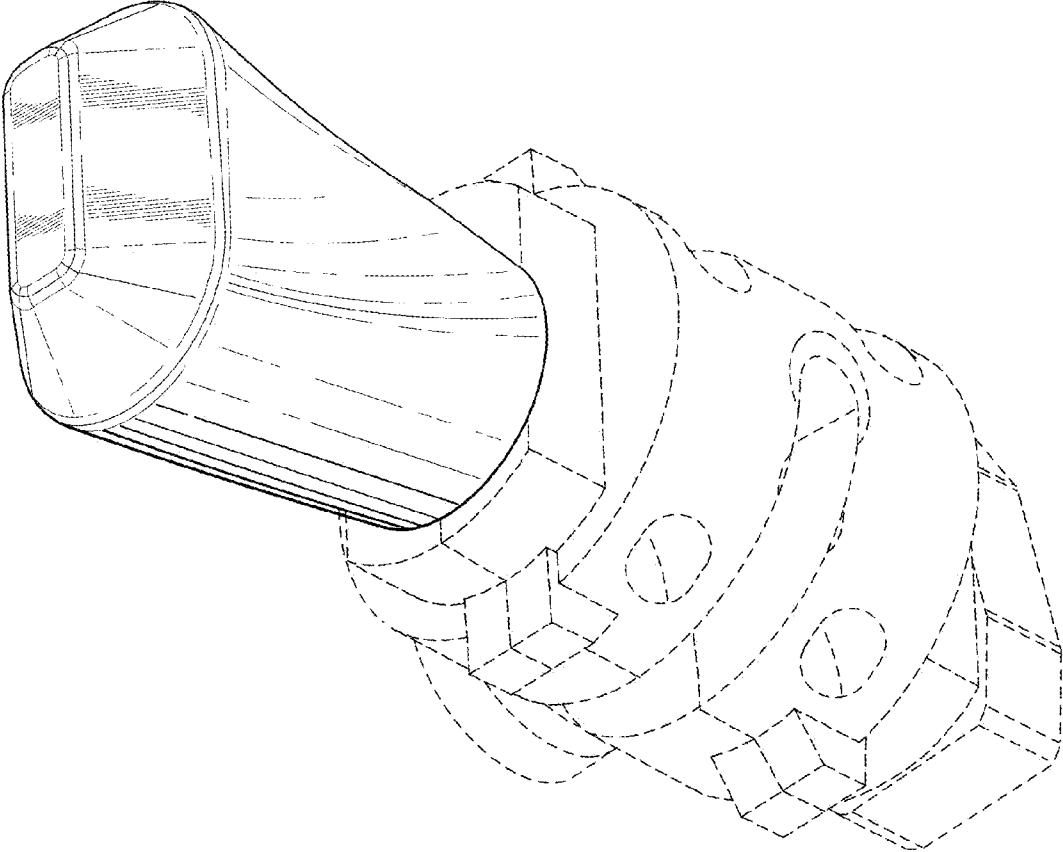


FIG. 1

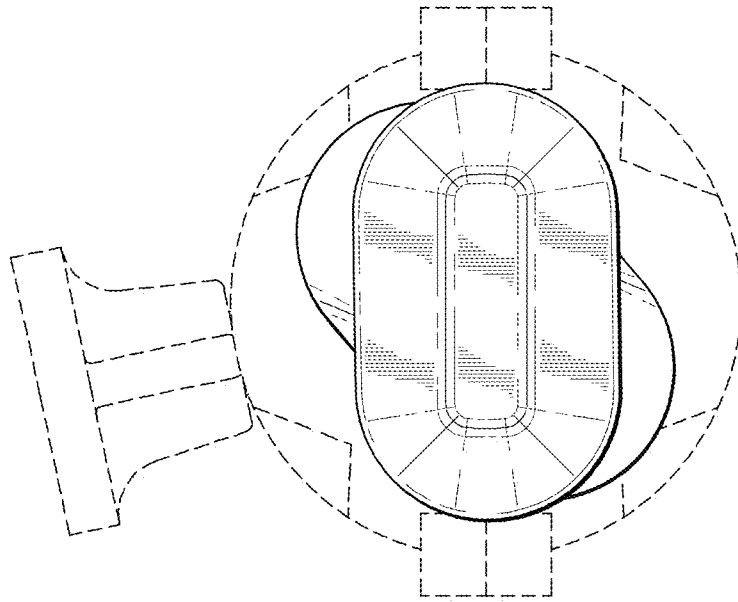


FIG. 2

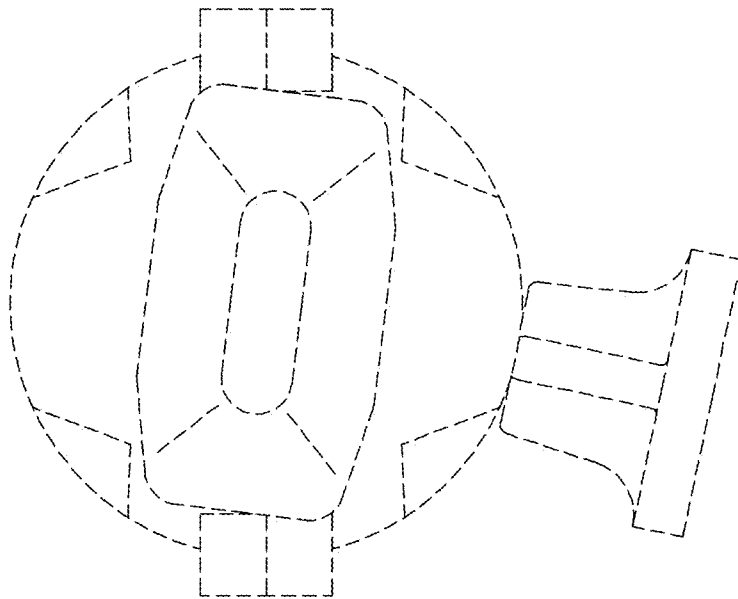


FIG. 3

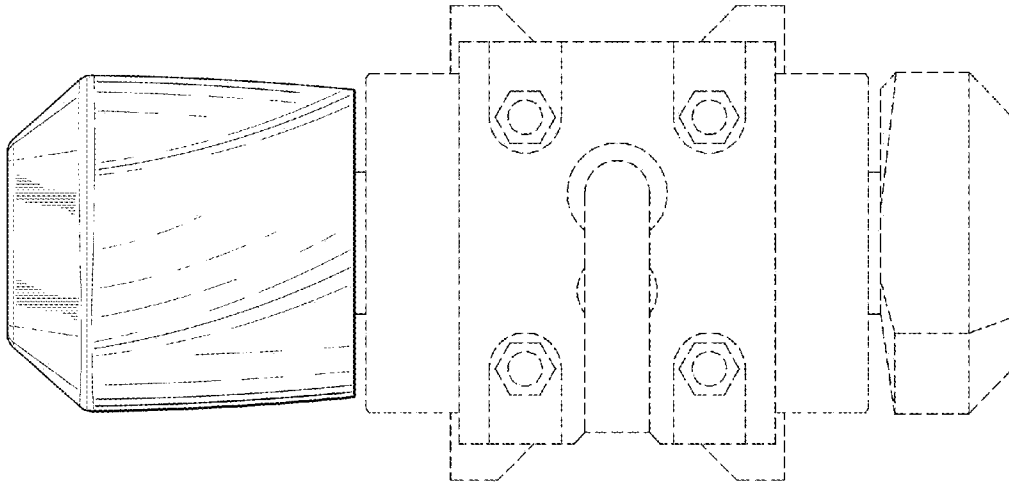


FIG. 4

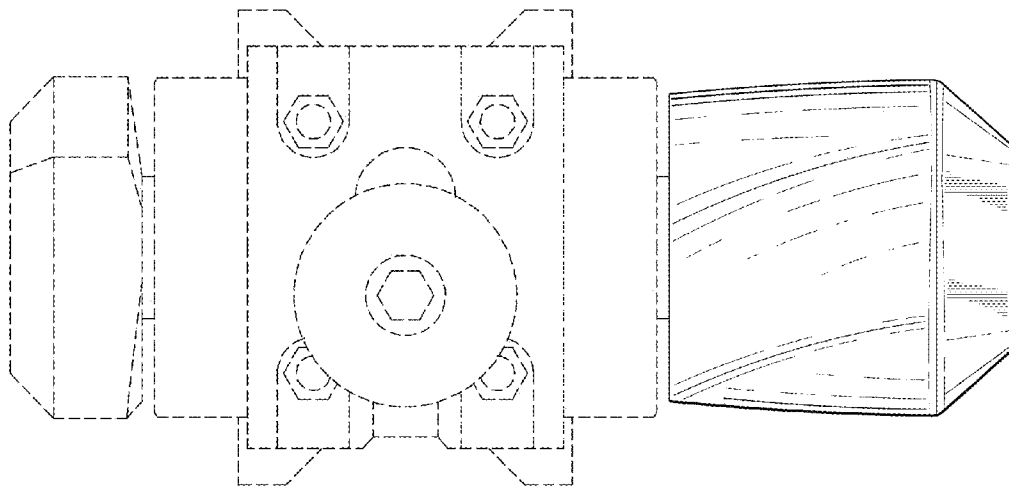


FIG. 5

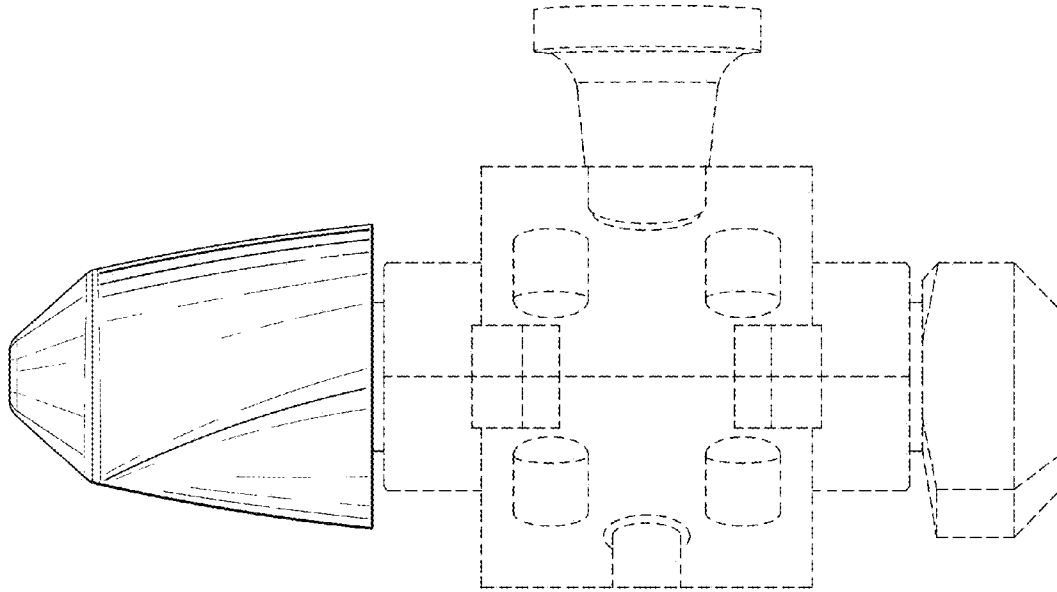


FIG. 6

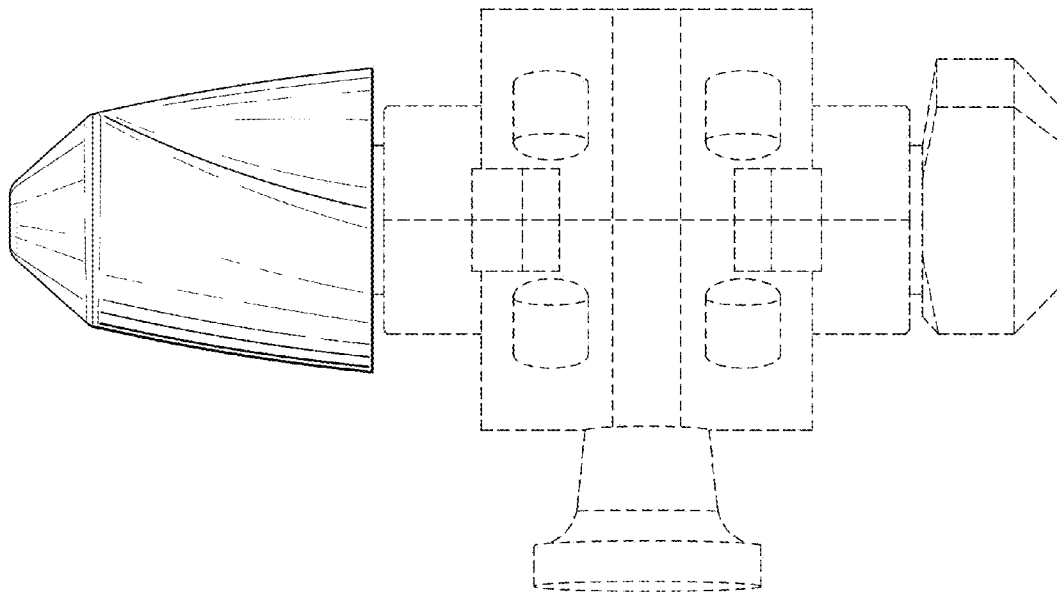


FIG. 7