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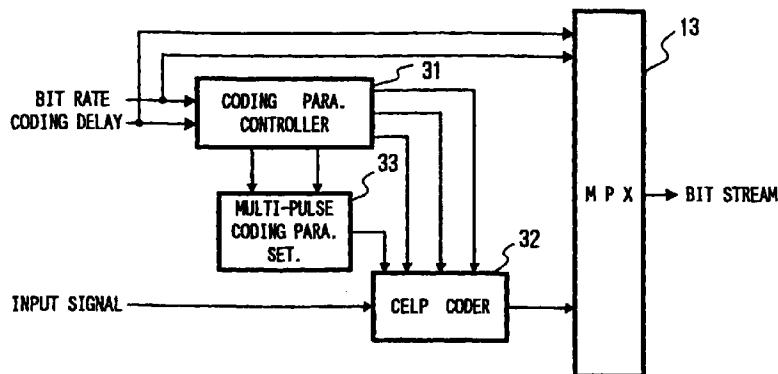
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(54) Multipulse-excited speech coder/decoder

(57) A coding parameter control circuit 31 computes frame length from bit rate and coding delay, and provides the computed frame length data to a CELP coding circuit 32. On the basis of the computed frame length, the coding parameter control circuit 32 selects control parameters from a table, in which a plurality of control parameters for controlling the operation of the CELP coding circuit are set, on the basis of the bit rate, and provides the selected control parameters to the CELP coding circuit. The coding parameter control circuit pro-

vides the sub-frame length, and bit number distributed to the multi-pulse signal to the multi-pulse signal generation parameter setting circuit 33. The multi-pulse signal coding parameter setting circuit 33 computes pulse number of multi-pulse excitation signal, pulse position candidates of each pulse and candidate positions thereof from the sub-frame length and bit number of multi-pulse signal.

FIG. 4





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EUROPEAN SEARCH REPORT

Application Number
EP 98 10 1335

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 2 February 1999	Examiner Ramos Sánchez, U
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X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document			

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ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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