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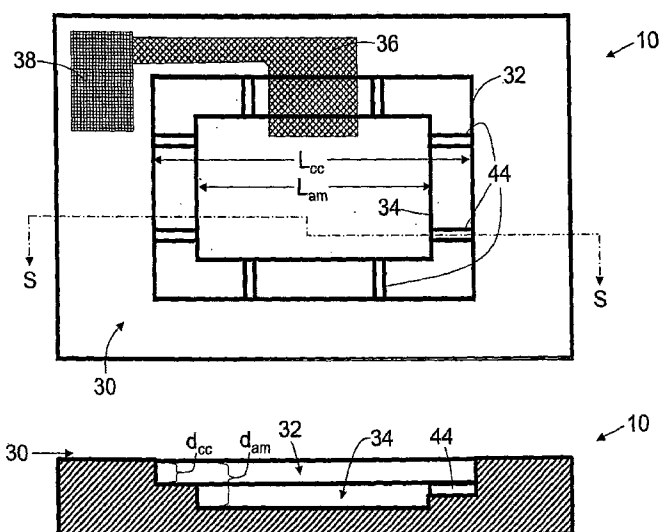
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(54) Title: SUPPORT FOR FLIP-CHIP BONDING A LIGHT EMITTING CHIP



(57) Abstract: In a light emitting device, a light emitting chip (12, 112) includes a stack of semiconductor layers (14) and an electrode (24, 141, 142) disposed on the stack of semiconductor layers. A support (10, 10', 110, 210) has a generally planar surface (30) supporting the light emitting chip in a flip-chip fashion. An electrically conductive chip attachment material (40, 41, 141, 142) is recessed into the generally planar surface of the support such that the attachment material does not protrude substantially above the generally planar surface of the support. The attachment material provides electrical communication between the electrode of the light emitting chip and an electrically conductive path (36, 36') of the support. Optionally, at least the stack of semiconductor layers and the electrode of the light emitting chip are also recessed into the generally planar surface of the support.

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2005/253159 A1 (CRESWICK STEVEN B [US] ET AL) 17 November 2005 (2005-11-17)	1-4, 8, 10-13, 16-22
Y	paragraphs [0067], [0068]; figure 8	5-7, 9, 14, 15
X	EP 0 854 520 A (OKI ELECTRIC IND CO LTD [JP]) 22 July 1998 (1998-07-22)	20, 23
Y	figure 36; example 8	5-7, 9, 14, 15
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☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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T later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

G document member of the same patent family

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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