

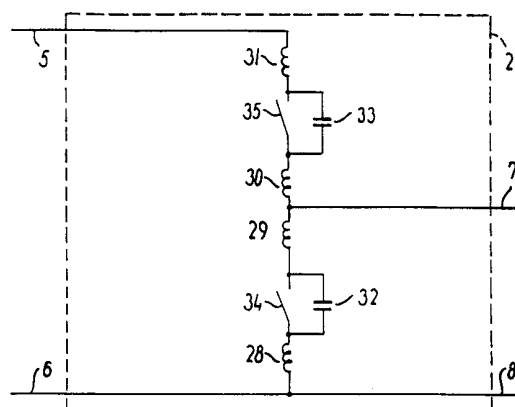
INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ :		(11) International Publication Number:	WO 99/59241
H03F 3/217, 1/56	A3	(43) International Publication Date:	18 November 1999 (18.11.99)
(21) International Application Number: PCT/DK99/00261 (22) International Filing Date: 11 May 1999 (11.05.99) (30) Priority Data: 0641/98 11 May 1998 (11.05.98) DK (71) Applicant (for all designated States except US): TOCCATA TECHNOLOGY [DK/DK]; Birkedommervej 27, 3, DK-2400 Copenhagen NV (DK). (72) Inventors; and (75) Inventors/Applicants (for US only): ANDERSKOUV, Niels [DK/DK]; Reventlovsgade 18, 4. tv, DK-1651 Copenhagen V (DK). RISBO, Lars [DK/DK]; Markmandsgade 14, 5. tv, DK-2300 Copenhagen S (DK). (74) Agent: LINGPAT; v/ Ole Jagtboe, Køjevænget 56, DK-2791 Dragør (DK).		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).	
		Published <i>With international search report.</i> (88) Date of publication of the international search report: 6 January 2000 (06.01.00)	

(54) Title: A METHOD OF REDUCING DISTORTION AND NOISE OF SQUARE-WAVE PULSES, A CIRCUIT FOR GENERATING MINIMALLY DISTORTED PULSES AND USE OF METHOD AND CIRCUIT

(57) Abstract

With a view to generating minimally distorted square-wave pulses with limited noise and limited peak current during and after the switching sequences in a switched circuit, e.g. a class D amplifier output stage or a power supply, the invention provides a circuit and a method. Seen from one port of the circuit, the short-circuit impedance has a low impedance at low frequencies, while it has a high impedance and is predominantly resistive at high impedances. In one embodiment, the circuit consists of an inductance which is connected between the one and the other ports of the two-port network, and of a series connection of a resistor and a capacitor which is connected between the terminals of the other port of the circuit. The circuit, in combination with a switched circuit, may be formed as an integrated unit on a substrate, where the inductance may advantageously be formed by a conductor path.



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INTERNATIONAL SEARCH REPORT

International application No.

PCT/DK 99/00261

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H03F 3/217, H03F 1/56

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5506532 A (CIRO MILAZZO), 9 April 1996 (09.04.96), column 5, line 1 - line 64; column 9, line 8 - column 10, line 26	1,4-5
A	--	2-3,6-15
A	US 5917369 A (HUEY NGUYEN), 29 June 1999 (29.06.99), column 2, line 36 - line 60	1-15
A	US 5163174 A (HORST BEEKEN), 10 November 1992 (10.11.92), column 8, line 61 - column 9, line 39	1-15
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Date of the actual completion of the international search

29 October 1999

Date of mailing of the international search report

03 -11- 1999

Name and mailing address of the ISA/

Swedish Patent Office

Box 5055, S-102 42 STOCKHOLM

Facsimile No. +46 8 666 02 86

Authorized officer

Lotta Gunnarsson/AE

Telephone No. +46 8 782 25 00

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/DK 99/00261

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