(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization

International Bureau

(43) International Publication Date

10 August 2017 (10.08.2017)





(10) International Publication Number WO 2017/133976 A1

(51) International Patent Classification:

H01L 31/0725 (2012.01) H01L 31/18 (2006.01) C30B 33/06 (2006.01) H01L 31/056 (2014.01) H01L 21/20 (2006.01) H01L 31/054 (2014.01) H01L 21/203 (2006.01) H01L 31/043 (2014.01)

(21) International Application Number:

PCT/EP2017/051751

(22) International Filing Date:

27 January 2017 (27.01.2017)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

3 February 2016 (03.02,2016) 1650859

- (71) Applicants: SOITEC [FR/FR]; Chemin des Franques, Parc Technologique des Fontaines, 38926 Bernin (FR). COMMISSARIAT À L'ÉNERGIE ATOMIQUE ET AUX ÉNERGIES ALTERNATIVES [FR/FR]; 25 rue Leblanc, bâtiment Le Ponant D, 75015 Paris (FR).
- (72) Inventors: GUIOT, Eric: Le Clos du Combre, 38570 Goncelin (FR). TAUZIN, Aurélie; 10 Place Salvador Allende, 38120 Saint-Egrève (FR). SIGNAMARCHEIX, Thomas; 8 Rue Marguerite Gonnet, 38000 Grenoble (FR). LAGOUTTE, Emmanuelle; 9 avenue Docteur Carrier, 38160 St Marcellin (FR).

- Agent: GRÜNECKER PATENT- UND RECHTSAN-WÄLTE PARTG MBB; Leopoldstrasse 4, 80802 München (DE).
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

with international search report (Art. 21(3))

(54) Title: ENGINEERED SUBSTRATE WITH EMBEDDED MIRROR

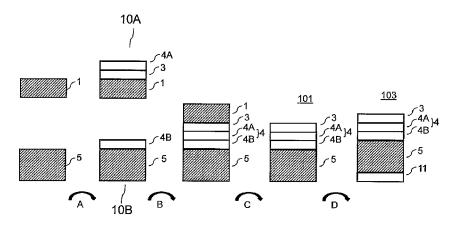


Fig. 1

(57) Abstract: Engineered substrate comprising: a seed layer made of a first semiconductor material for growth of a solar cell; a first bonding layer on the seed layer; a support substrate made of a second semiconductor material; a second bonding layer on a first side of the support substrate; a bonding interface between the first and second bonding layers; the first and second bonding layers each made of metallic material; wherein doping concentration and thickness of the engineered substrate, in particular of the seed layer, the support substrate, and both the first and second bonding layers, are selected such that the absorption of the seed layer is less than 20%, preferably less than 10%, as well as total area-normalized series resistance of the engineered substrate is less than 10 mOhm.cm², preferably less than 5 mOhm.cm².





Engineered substrate with embedded mirror

Field of Invention

5

The present invention relates to an engineered substrate having an embedded mirror as well as a method of manufacturing an engineered substrate having an embedded mirror.

10

15

20

25

30

Background of the invention

Photovoltaic or solar cells are designed for converting the solar radiation to electrical current. In concentrator solar photovoltaic applications the incoming sun light is optically concentrated before it is directed to solar cells. For example, the incoming sun light is received by a primary mirror that reflects the received radiation toward a secondary mirror that, in turn, reflects the radiation toward a solar cell, which converts the concentrated radiation to electrical current by the generation of electronhole pairs in III-V semiconductor or single crystal silicon, for example. Concentrator photovoltaics may, alternatively or additionally, comprise Fresnel lens optics for the concentration of the incoming solar radiation.

Recently, in the field of optoelectronics application, e.g. CPV (concentrator photovoltaic) cells, IR (infrared) detectors etc, interest has focused on recycling photons, reducing losses of photons and reducing resistivity.

One of the problems connected with these issues is the efficiency of the cell. For instance, commonly used substrates such as GaAs substrates need to be doped to ensure a low contact resistance. But this doping is inducing increased absorption of photons.

WO2013143851 discloses a process of manufacturing MJ solar cell devices, including a transfer of a seed layer on a final substrate.

5

10

15

20

25

30

For MJ (multijunction) solar cells, or more general « photodetectors », e.g. suitable for a fabrication method of the MJ solar cell as set forth in WO2013143851 that implies direct bonding of at least two subcells, i.e. each of which is grown on a substrate, however both substrates having different lattice constants, there is a need for proper substrates for improving the efficiency of the light detection or conversion devices based on such substrates.

In view of the above-mentioned problems it is an object of the present invention to provide a sufficiently transparent yet electrically conductive substrate with improved efficiency, for MJ solar cells.

Summary of the Invention

It is provided an engineered substrate comprising: a seed layer made of a first semiconductor material for growth of a solar cell; a first bonding layer on the seed layer; a support substrate made of a second semiconductor material; a second bonding layer on a first side of the support substrate; a bonding interface between the first and second bonding layers; the first and second bonding layers each made of metallic material; wherein doping concentrations of the semiconductor materials and thicknesses of the layers of the engineered substrate, in particular of the seed layer, the support substrate, and both the first and second bonding layers, are selected such that the absorption of the seed layer is less than 20%, preferably less than 10%, as well as total area-normalized series resistance of the engineered substrate is less than 10 mOhm·cm², preferably less than 5 mOhm·cm².

In the above provided engineered substrate the support substrate may also be termed a receiver substrate. The engineered substrate comprises two non lattice matched materials, namely the top thin layer comprising the seed layer and the typically thicker bottom material comprising the support / receiver substrate. e.g. InP for the top thin layer, i.e. the seed layer, and GaAs or Ge for the bottom receiver substrate, i.e. the support substrate. The thin layer of seed layer of a first material can be obtained by several approaches including bonding on the second material

al. The bonding interface between the first and second bonding layers thereby has the function of a mirror layer of metallic material in order to better reflect photons. The term mirror layer is used here to define a metallic layer exhibiting a high reflection coefficient (sup. 70%) in the 400nm – 2000nm wavelength range. The incoming photons that have not been absorbed by the active device grown on top of the seed layer during their first path can then be reflected at the mirror layer and be reinjected in the active device for absorption. The proximity of the mirror bonding layers with the active device ensures a high photon collection probability, leading to improved device characteristics (current, efficiency,...).

10

15

5

The electrical resistivity is an intrinsic property that quantifies how strongly a given material opposes the flow of electric current. A low resistivity indicates a material that readily allows the movement of electric charge. Resistivity is commonly represented by the Greek letter ρ (rho). The unit of electrical resistivity derives from the product of the resistance R with the surface area or cross sectional area A of the solar cell or the engineered substrate, respectively. As a formula this is $R \cdot A = \rho \cdot I$, where I represents the length or thickness which is substantially perpendicular to the area A. The unit of electrical resistivity ρ thus is Ohm-metre ($\Omega \cdot m$) although other units like Ohm-centimetre ($\Omega \cdot cm$) may derive from this.

20

25

The series resistance represents one of the major resistive effects in a solar cell. A high series resistance may reduce the fill factor and thereby ultimately the efficiency of the solar cell. In order to make values comparable, the values are areanormalized so as to multiply the resistance with an area, which is the same as using the current density instead of the current in Ohm's law, thereby obtaining a unit of Ohm-centimetre², i.e. Ω -cm².

The absorption being less than 20% should be understood as the seed layer should only absorb less than 20% of the incident light, when the light travels once through the layer, so that at least 80% of the incident light is transmitted through the layer and can be reflected at the mirror layer.

WO 2017/133976

For the engineered substrate the doping concentration of the seed layer may be less than $5x10^{17}$ at/cm³.In the engineered substrate the thickness of seed layer may be a range of 150 nm to 1 μ m. For instance, the doping concentration of the seed layer may be about $3x10^{18}$ at/cm³ with a thickness of about 300 nm.

5

In the engineered substrate the thickness of support substrate may be in a range from 100 μ m up to 500 μ m and the doping concentration of the support substrate may be in the range 10^{14} to $5x10^{17}$ at/cm³.

10

In the engineered substrate the metallic material of the first and second bonding layers may be one of W or Ti together with TiN.

In the engineered substrate according the first semiconductor material may have a lattice constant in the range of 5.8 to 6 Å (0.58 to 0.6 nm).

15

These values typically correspond to the lattice constant of InP. The support substrate typically is the final substrate. For the case of directly bonded MJ cells the subcell grown on this substrate should have the lowest energy gaps, which is only possible to achieve with materials having lattice constants around the lattice constant of InP.

20

25

30

In the engineered substrate the first semiconductor material may be InP or the first semiconductor material may be a ternary or quaternary or penternary III-V material, for example InGaAs or InGaAsP, and the second semiconductor material may be GaAs or Ge.

The engineered substrate may further comprise a metal contact provided on a se-

cond side of the support substrate opposite to the first side to collect the generated

electrons.

The back side metal contact may also serve as a back side mirror. Typically, it is placed right below the support substrate in order to prevent or at least minimize losses of photons by reflecting them back towards the support substrate and fur-

ther, at least partially, to the active layer of the solar cell. Photons may thus be recycled by the mirror and the loss of photons is reduced. The above mentioned area-normalized series resistance of the engineered substrate has the further effect of providing electrical connection of the cell to the back side metal contact.

5

10

15

It is further provided a light detection or conversion device, in particular a solar cell, comprising an engineered substrate as described above.

It is further provided a method of manufacturing an engineered substrate comprising: providing a first substrate; providing a seed layer on a first side of the first substrate; forming a first bonding layer of metallic material on the seed layer; providing a support substrate; forming a second bonding layer of metallic material on the support substrate; directly bonding the first and second bonding layers; and then removing the first substrate; the first and second bonding layers each made of metallic material; wherein doping concentrations of the semiconductor materials and thicknesses of the layers of the engineered substrate are selected such that both the absorption of the seed layer is less than 20%, preferably less than 10%, as well as total area-normalized series resistance of the engineered substrate is less than 10 mOhm·cm², preferably less than 5 mOhm·cm².

20

The seed layer is made of a first semiconductor material for growth of a solar cell and the support substrate is made of a second semiconductor material.

25

30

The method of manufacturing may further comprise that providing the seed layer is obtained by epitaxial growth.

The method of manufacturing may further comprise an ion implantation step for forming implantation weakened layer in a part of the first substrate or the seed layer before directly bonding the first and second bonding layers, and a detaching step to separate the remaining part of the first substrate or the seed layer provided on the first substrate) after directly bonding the first and second bonding layers.

The method of manufacturing may further comprise a step of providing a metal contact on a second side of the support substrate opposite to the first side.

The engineered substrate as detailed above may be used for the manufacturing of CPV cells, MJ cells or any light detection devices.

Brief Description of the Drawings

Fig. 1 Schematic drawing of one embodiment

10

20

25

30

Fig. 2 A modification of the embodiment shown in Fig. 1

Description of the Invention

In Fig. 1 a first embodiment is depicted of a method of manufacturing an engineered substrate according to the present invention.

In a first step, in the leftmost part of Fig. 1 it is indicated that a support substrate 5 is provided. Furthermore, a first substrate 1 is provided. The first substrate 1 serves as an auxiliary substrate which however may be removed or sacrificed before the final engineered substrate is ready.

In a next step, as indicated by the arrow A, a seed layer 3 is formed on the first substrate 1. The seed layer 3 may be of a first semiconductor material. The first semiconductor material may be, e.g. InP or it may be a ternary or quaternary or penternary III-V material, for example InGaAs or InGaAsP.

Furthermore, a first bonding layer 4A is formed on the seed layer 3. The first bonding layer 4A is a metallic layer, the material may be one of W or Ti together with TiN.

WO 2017/133976

7

PCT/EP2017/051751

In principle, as indicated in Fig. 1 the first substrate 1, seed layer 3 and first bonding layer 4A may also be viewed as one structure 10A.

Furthermore, a second bonding layer 4B is formed on the support substrate 5. The second bonding layer 4B is also a metallic layer, the material may be one of W or Ti together with TiN. The second bonding layer 4B is grown, e.g. deposited by chemical vapor deposition (CVD), on a first side of the support substrate 5. The support substrate 5 and the second bonding layer 4B are made of a second semiconductor material. Typically the second semiconductor material is GaAs or Ge. In principle, the support substrate 5 and the second bonding layer 4B together may be viewed as one structure 10B. The one structure 10 B corresponds to a support substrate comprising the second bonding layer 4B being formed in the uppermost / top part of the support substrate 5.

No correlation in time between forming the two parts or structures 10A and 10B, respectively, i.e. the part comprising the seed layer formed on the first substrate and the part comprising the second bonding layer 4B being provided on the support substrate 5, is required, other that both are available at the beginning of the next step which is indicated by an arrow B.

20

25

30

5

10

15

As illustrated in Fig. 1, in a next step indicated by B, the two structures are bonded together. That is, the structure 10A comprising the first substrate 1 with the seed layer 3 and the second bonding layer 4A is bonded together with the second structure 10B comprising the support substrate 5 and the second bonding layer 4B. In Fig. 1 this is depicted as if one of the structures 10A, 10B, here for purely exemplary reasons structure 10A, is flipped upside down so that the first bonding layer 4A and the second bonding layer 4B substantially contact each other. The first bonding layer 4A and the second bonding layer 4B then form a bonding interface 4 between both bonding layers. The bonding interface 4 may be a direct bonding interface. Typically, the bonding is performed by direct bonding of the two structures via the two bonding layers 4A, 4B. Direct bonding typically represents molecular adhesion between the two surfaces involved, without using any further bonding layers. Molecular adhesion is typically performed under partial vacuum. The bonding interface

4 comprising the first and second bonding layers 4A and 4B integrate the functionality of being both a bonding interface and due to the metallic nature of the bonding layers 4A, 4B also having a mirror function. Thus by integrating a mirror function, the efficiency of the engineered substrate for use in light detection or conversion devices is increased. Integration of a mirror function at the bonding interface is further advantageous with respect to a mirror formed at the backside of the substrate because it gives more flexibility in the design of the support substrate with respect to the maintain of low electrical resistance. Such support substrate may be highly doped in order to maintain low electrical resistance which, however, induces higher absorption of photons as already pointed out.

5

10

15

20

Subsequently, in step C, the first substrate 1 is removed / detached from the first substrate 1, resulting in an engineered substrate 101 comprising an exposed seed layer 3. Removal of the first substrate 1 may be performed in various ways. Notably, grinding and/or back etching may be used to remove the first substrate 1, thereby eventually exposing the seed layer 3. If this treatment is chosen, the bonding process performed in the step before may be conducted at higher temperatures, e.g. temperatures in a range of 200° C − 600° C, or more preferably between 300° C − 500° C. Another possibility may be performing the transfer of the first structure 10A onto the second structure 10 B prior to bonding by means of Smart Cut™, i.e. introducing an ion implantation step before bonding and then splitting / detaching, cf. Fig. 2. For this possibility, the bonding process should be carried out at a lower temperature, preferably less than 200° C.

Subsequently, in step D, an additional back side metal contact 11 may be provided on a second side of the support substrate 5 opposite to the first side, thereby resulting in an engineered substrate 103. The engineered substrate 103 may be substantially the same as the engineered substrate 101, except for having the additional back side metal contact. Step D and thus providing the additional back side metal contact 11 are optional, but may further improve the efficiency of the engineered substrate 103, and ultimately a solar cell including the engineered substrate. The back side metal contact 11 may serve as another mirror in addition to the mirror functionality of the bonding interface 4, i.e. its purpose is to reflect such

photons which have not yet been converted in the substrate back into the interior. The back side metal contact 11 may also serve for providing an electrical contact to the backside of a solar cell, e.g. contacting a conductive plate in order to avoid complex wiring.

5

10

15

20

Fig. 2 shows a slight modification of the embodiment of Fig. 1, however sharing the same major steps A, B, C and optional step D. The same elements are denoted by the same reference numbers and will not be explained, again. In Fig. 2, the first support substrate comprises a zipper layer 2 which is provided at the surface of the first substrate 1, representing the contact to the seed layer 3. For example, the zipper layer 2 may be provided in form of a weakened layer formed by the implantation of ionic species. The ionic species may be, for example, hydrogen or helium ions. In this case, the first substrate 1 can be provided as a bulk substrate, such as an InP substrate, then the implantation through the top surface of the bulk substrate forms the weakened layer 2. The weakened layer or zipper layer 2 thus separates the seed layer 3 and the first substrate 1 respectively in the top portion and the bottom portion of the bulk substrate. Subsequently, the first substrate 1 is detached from the seed layer 3 by means of the zipper layer 2, for example, the first engineered substrate is cleaved at the zipper layer 2 allowing the detachment of first substrate 1 from the seed layer 3. Then the further process of detachment of the first substrate 1 may be mediated by the application of mechanical forces to delaminate at the weakened layer formed by hydrogen or helium ions.

30

25

With respect to the above embodiment shown in Fig. 2. one understands that the seed layer 3 may be part of the first substrate 1 and is defined by the presence of the weakened layer 2. However, the present invention is not limited to such configuration. It is understood that the seed layer may be provided by epitaxial growth on the first substrate. An implantation step may thus define a weakened layer 2 either in the first substrate 1 or the seed layer. Such weakened layer 2 is formed in view of a subsequent detachment step as already explained above. Further surface preparation after such detachment (e.g. grinding, polishing, etching) of the transferred layer leaves behind a seed layer which is suitable for epitaxial growth (re-

duced roughness required), such seed layer 3 bonded via metallic bonding layers (4A, 4B) to the support substrate 5.

The resulting engineered substrate 101 and 103, the latter including a back side metal contact, are substantially the same as in Fig. 1 The thickness of the seed layer 3 typically is in a range of 150 nm up to 1 μ m. If the transfer is performed by Smart CutTM, said thickness may be up to 300 nm, but can be as low as 20 nm. Furthermore, the thickness of the support substrate 5 may range from 100 μ m up to 500 μ m. The doping concentration of the support substrate 5 typically ranges from 1x10¹⁴- 5x10¹⁷ at/cm³. The first semiconductor material typically has a lattice constant in the range of 5.8 - 6 Å (0.58 nm - 0.6 nm). The thickness of the bonding layers is in the range of 3 nm up to 2 μ m, preferentially in the range of 3 nm to 20 nm. In the latter case, due to low absorption within the bonding layers which depends on the thickness, a second mirror on the back side may be applicable in order to further increase the efficiency.

Each of the engineered substrate 101 and 103 may be used in forming an MJ solar cell. The advantage is that materials of the various junctions may be tuned in order to better match the solar spectrum.

5

10

15

Claims

5

15

- 1. Engineered substrate (101, 103) comprising:
- a seed layer (3) made of a first semiconductor material for growth of a solar cell;
 - a first bonding layer (4A) on the seed layer (3);
 - a support substrate (5) made of a second semiconductor material;
 - a second bonding layer (4B) on a first side of the support substrate (5);
- a bonding interface (4) between the first and second bonding layers (4A, 10 4B);

the first and second bonding layers (4A, 4B) each made of metallic material; wherein doping concentrations of the semiconductor materials and thicknesses of the layers of the engineered substrate (101, 103), in particular of the seed layer, the support substrate, and both the first and second bonding layers, are selected such that the absorption of the seed layer (3) is less than 20%, preferably less than 10%, as well as total area-normalized series resistance of the engineered substrate (101, 103) is less than 10 mOhm·cm², preferably less than 5 mOhm·cm².

- 2. The engineered substrate (101, 103) according to claim 1, wherein the doping concentration of the seed layer (3) is less than 5x10¹⁷ at/cm³.
 - 3. The engineered substrate (101, 103) according to one of claims 1 or 2, wherein the thickness of the seed layer (3) is in a range of 150 nm to 1 μ m.
- 4. The engineered substrate (101, 103) according to at least one of claims 1 3, wherein the thickness of the support substrate (5) is in a range from 100 μm up to 500 μm and the doping concentration of the support substrate (5) is in the range 10¹⁴ to 5x10¹⁷ at/cm³.
- The engineered substrate (101, 103) according to claim 1, wherein the metallic material of the first and second bonding layers (4A, 4B) is one of W or Ti together with TiN.

WO 2017/133976

30

- 6. The engineered substrate (101, 103) according to at least one of claims 1 5, wherein the first semiconductor material has a lattice constant in the range of 5.8 to 6 Å (0.58 to 0.6 nm).
- The engineered substrate (101, 103) according to at least one of claims 1 6, wherein the first semiconductor material is InP or the first semiconductor material being a ternary or quaternary or penternary III-V material, for example InGaAs or InGaAsP, wherein the second semiconductor material is chosen among GaAs or Ge.
- 8. The engineered substrate (101, 103) according to claim 1, further comprising a metal contact (11) provided on a second side of the support substrate (5) opposite to the first side.
- 15 9. Light detection or conversion device, in particular solar cell, comprising an engineered substrate (101, 103) according to at least one of claims 1 8.
 - 10. A method of manufacturing an engineered substrate (101, 103) comprising: providing a first substrate (1);
- providing a seed layer (3) on a first side of the first substrate; forming a first bonding layer (4A) of metallic material on the seed layer (3); providing a support substrate (5),

forming a second bonding layer (4B) of metallic material on the support substrate (5);

directly bonding the first and second bonding layers (4A, 4B); and then removing the first substrate (1);

the first and second bonding layers (4A, 4B) each made of metallic material; wherein doping concentrations of the semiconductor materials and thickness of the layers of the engineered substrate (101, 103) are selected such that absorption of the seed layer (3) is less than 20%, preferably less than 10%, as well as total area-normalized series resistance of the engineered substrate (101, 103) is less than 10 mOhm·cm², preferably less than 5 mOhm·cm².

13

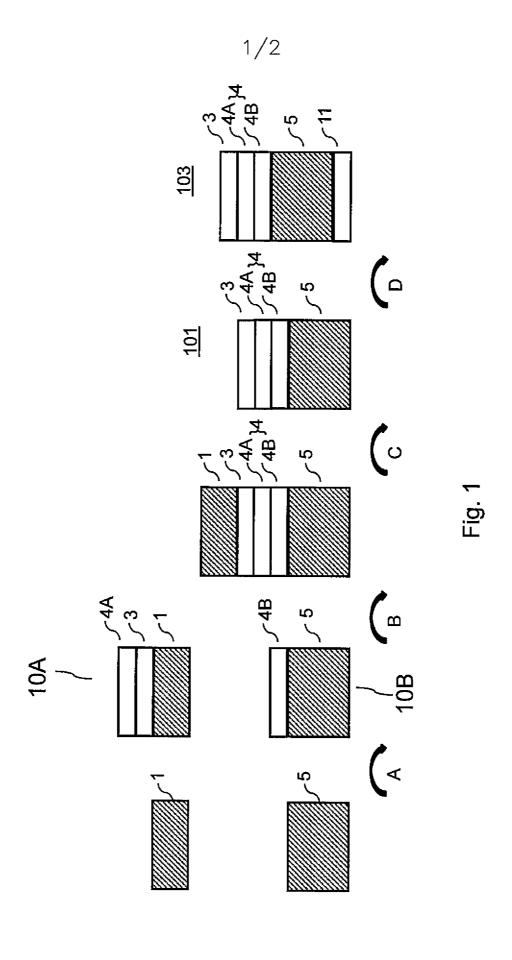
- 11. The method according to claim 10, wherein providing the seed layer (3) is obtained by epitaxial growth.
- 12. The method according to claim 10 or claim 11, further comprising:

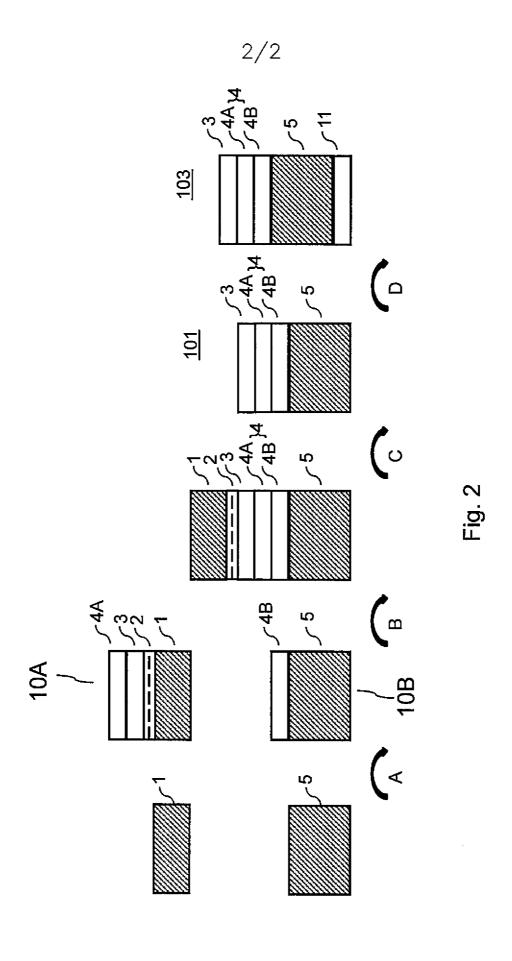
an ion implantation step for forming a weakened layer (2) in a part of the first substrate (1) or the seed layer (3) before directly bonding the first and second bonding layers (4A, 4B), and a detaching step to separate the remaining part of the first substrate (1) or the seed layer (3) provided on the first substrate (1) after directly bonding the first and second bonding layers (4A, 4B).

10

5

13. The method according to claims 10 to 12, further comprising the step of providing a metal contact (11) on a second side of the support substrate (5) opposite to the first side.





INTERNATIONAL SEARCH REPORT

International application No PCT/EP2017/051751

A. CLASSIFICATION OF SUBJECT MATTER INV. H01L31/0725 C30B33/06 H01L31/056

H01L31/054

H01L21/20 H01L31/043 H01L21/203

H01L31/18

ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L C30B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal

C. DOCUM	ENTS CONSIDERED TO BE RELEVANT	
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Υ	CN 102 270 693 B (SUZHOU INST NANO TECH & NANO B) 25 September 2013 (2013-09-25) abstract; claims 1-9; figures 1-4 example 1	1-13
Υ	WO 2014/154993 A1 (COMMISSARIAT ENERGIE ATOMIQUE [FR]) 2 October 2014 (2014-10-02) abstract; claims 1-15; figures 1-18 page 6, line 12 - page 13, line 20	1-13
A	CN 103 367 465 B (SHANDONG HUAGUANG OPTOELECTRONICS CO LTD) 6 January 2016 (2016-01-06) abstract; claims 1-9; figures 1-8	1-13

"A" document defining the general state of the art which is not considered to be of particular relevance
"E" earlier application or patent but published on or after the international

filing date

Further documents are listed in the continuation of Box C.

- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other
- document published prior to the international filing date but later than the priority date claimed
- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
- "&" document member of the same patent family

See patent family annex.

Date of the actual completion of the international search Date of mailing of the international search report 27 February 2017 06/03/2017 Name and mailing address of the ISA/ Authorized officer

Special categories of cited documents

European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016

Hamdani, Fayçal

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2017/051751

Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. A
W0 2013/143851 A1 (SOITEC SILICON ON INSULATOR [FR]) 3 October 2013 (2013-10-03) cited in the application abstract; claims 1-16; figures 1,2 US 2006/255341 A1 (PINNINGTON THOMAS H [US] ET AL) 16 November 2006 (2006-11-16) abstract; claims 1-32; figures 2a-2h paragraph [0025] - paragraph [0097] US 2015/380592 A1 (NEWMAN FRED [US] ET AL) 31 December 2015 (2015-12-31) abstract; claims 1-26; figures 1-4
INSULATOR [FR]) 3 October 2013 (2013-10-03) cited in the application abstract; claims 1-16; figures 1,2 US 2006/255341 A1 (PINNINGTON THOMAS H [US] ET AL) 16 November 2006 (2006-11-16) abstract; claims 1-32; figures 2a-2h paragraph [0025] - paragraph [0097] US 2015/380592 A1 (NEWMAN FRED [US] ET AL) 31 December 2015 (2015-12-31) abstract; claims 1-26; figures 1-4
[US] ET AL) 16 November 2006 (2006-11-16) abstract; claims 1-32; figures 2a-2h paragraph [0025] - paragraph [0097] US 2015/380592 A1 (NEWMAN FRED [US] ET AL) 31 December 2015 (2015-12-31) abstract; claims 1-26; figures 1-4
abstract; claims 1-26; figures 1-4

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No
PCT/EP2017/051751

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
CN 102270693 B	25-09-2013	NONE	
WO 2014154993 A1	02-10-2014	EP 2979306 A1 FR 3003692 A1 US 2016043269 A1 WO 2014154993 A1	03-02-2016 26-09-2014 11-02-2016 02-10-2014
CN 103367465 B	06-01-2016	NONE	
WO 2013143851 A1	03-10-2013	CN 104205362 A EP 2645431 A1 EP 2831927 A1 KR 20140138338 A US 2015059832 A1 WO 2013143851 A1	10-12-2014 02-10-2013 04-02-2015 03-12-2014 05-03-2015 03-10-2013
US 2006255341 A1	16-11-2006	JP 5364368 B2 JP 2008538658 A TW 200707799 A US 2006255341 A1 WO 2006116030 A2	11-12-2013 30-10-2008 16-02-2007 16-11-2006 02-11-2006
US 2015380592 A1	31-12-2015	US 2015380592 A1 WO 2015198117 A1	31-12-2015 30-12-2015