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(72) Inventors; and

(75) Inventors/Applicants (for US only): **WAKUTSU, Takashi** [JP/JP]. **SAKURAI, Shuichi** [JP/JP]. **ITO, Kuniaki** [JP/JP]. **TSUMAGARI, Yasufumi** [JP/JP].(74) Agents: **KURATA, Masatoshi** et al.; c/o **SUZUYE & SUZUYE**, 1-12-9, Toranomon, Minato-ku, Tokyo, 1050001 (JP).

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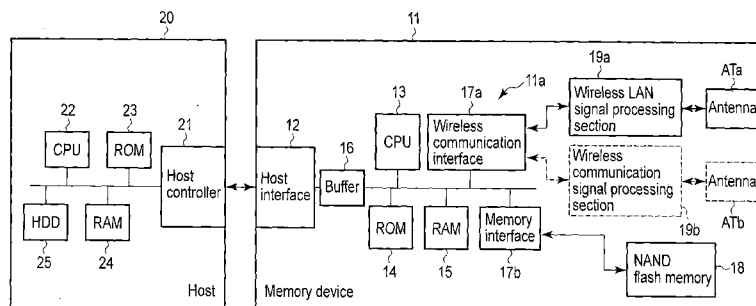


FIG. 1

(57) **Abstract:** According to one embodiment, a memory system includes a nonvolatile semiconductor memory device, controller, memory, wireless communication function section, and extension register. The extension register which can define the wireless communication function of the wireless communication function section. The controller processes a first command to read data from the extension register, and a second command to write data to the extension register, and the extension register records, first information specifying the type of the wireless communication function, and second information indicating a region of the extension register to which the wireless communication function is assigned, and third information including contents of the wireless communication function.



AMENDED CLAIMS**received by the International Bureau on 01 Octobre 2012 (01/10/2012)**

1. (Amended) A memory system comprising:
a nonvolatile semiconductor memory;
a controller configured to control the nonvolatile
5 semiconductor memory;
a memory serving as a work area of the controller;
a wireless communication module having a wireless
communication function; and
an extension register to be provided in the
10 memory,
wherein the controller processes a first command
to read data from the extension register, and a second
command to write data to the extension register, and
the extension register records,
15 an information specifying the type of the wireless
communication function in a specific page, and
an address information indicating a region on the
extension register to which the wireless communication
function is assigned,
20 wherein the extension register to which the
wireless communication function is assigned,
comprising:
a first register which indicates the wireless
communication function to support;
25 a second register for writing a third command to
perform a wireless communication;
a third register for reading a processing state of

the third command; and

a fourth register for reading an execution result of the third command.

2. (Amended) The system according to claim 1,
5 wherein

the third command is constituted by one or more command, and

the controller writes a processing state to the third register while a command written to the second
10 register is sequentially processed, and writes one or more responses corresponding to a command which execution has completed, to the fourth register.

3. (Amended) The system according to claim 2,
wherein

15 the third command controls the wireless communication function, and the controller transfers a file stored in the nonvolatile semiconductor memory or data stored in the second register, to a server, and writes a response supplied from the server to a file
20 stored in the nonvolatile semiconductor memory or to the fourth register.

4. The system according to claim 1, wherein
the extension register includes a data port for writing a command for the wireless communication
25 function and an argument.

5. The system according to claim 1, wherein
the extension register records a status

information for the wireless communication function.

6. The system according to claim 1, wherein
the extension register records information
indicating an execution situation of a command for the
5 wireless communication function.

7. The system according to claim 1, wherein
the extension register includes a data port for
acquiring a response as an execution result of a
command for the wireless communication function.

10 8. The system according to claim 1, wherein the
system is conformed to at least one of the SD card
specifications.

9. The system according to claim 8, wherein the
system is capable of being inserted to a digital steel
15 camera which has no wireless communication function.

10. The system according to claim 9, wherein the
wireless communication module is conformed to at least
one of the IEEE802.11b, 11g and 11n standards.

11. A memory system comprising:
20 a memory device connected to a host, comprising:
a nonvolatile semiconductor memory;
a controller configured to control the
nonvolatile semiconductor memory;
a memory serving as a work area of the
25 controller;
a wireless communication module having a
wireless communication function ; and

an extension register to be provided in the memory capable of defining the wireless communication function,

wherein the extension register records,

5 first information specifying the type of the wireless communication function,

second information indicating a region of the extension register to which the wireless communication function is assigned, and

10 third information including contents of the wireless communication function.

12. The system according to claim 11, wherein at the time of start-up, the host

15 issues a first command to read at least one of the first and second information recorded in the extension register,

enables a function of the host corresponding to the wireless communication function when the host supports the wireless communication function.

20 13. The system according to claim 11, wherein when the host issues a command for the wireless communication function to the memory device, the host issues a second command to write information about the command for the wireless communication function to a
25 first region of the extension register.

14. The system according to claim 13, wherein the controller records information indicating

whether or not the command for the wireless communication function could have been received in a second region of the extension register.

15 15. The system according to claim 14, wherein the host issues the first command to read the information recorded in the second region, and confirm whether or not the memory device could have received the command for the wireless communication function.

10 16. The system according to claim 15, wherein the controller records response status information of the command for the wireless communication function in the second region of the extension register.

15 17. The system according to claim 16, wherein the host issues the first command to read the response status information from the second region, and confirm whether or not the command for the wireless communication function could have been processed.

20 18. The system according to claim 17, wherein the host successively deletes information items for which processing has already been completed among information items in the second region.

19. The system according to claim 11, wherein the system is conformed to at least one of the SD card specifications.

25 20. The system according to claim 19, wherein the wireless communication module is conformed to at least one of the IEEE802.11b, 11g and 11n standards.