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Menetelmä ja järjestelmä uusiutuvan energian laitoksen tehorajoituksen kontrolloimiseksi

Förfarande och system för att kontrollera effektbegränsning på anläggning för förnybar energi

Method and system of power limit control for a renewable energy site

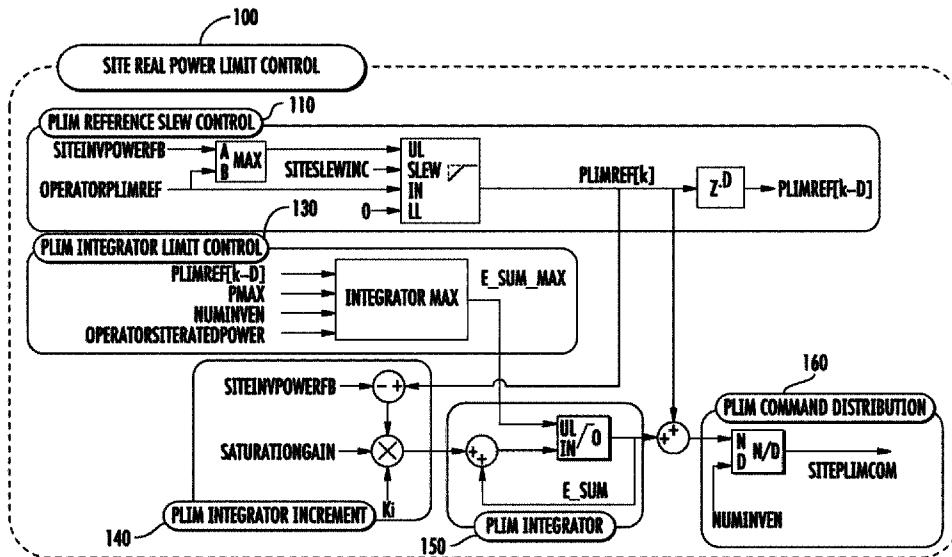
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The present invention provides methods, systems, computer program products, and devices for renewable energy site power limit control that address conditions of plant saturation and loop delay. In an exemplary embodiment, power limit control of the present invention is broken into two levels of control: a site-wide real power limit control that produces a single site-wide command, SitePlimCom, for all inverters, and inverter-level power limit control that outputs a command, Inv[x].PlimCom[k], for individual inverters. In some embodiments, the invention has one or more of the following features: an integrator with nonlinear gains, limit based anti-windup, a power limit control term based on the sum of both integrator and feed-forward terms, and an inverter-level controller designed to meet slew rate and site power overshoot constraints. The invention can be used at any renewable energy site employing inverters wherein improved dynamic control performance is needed, including solar, wind, and tidal energy sites.

Esillä oleva keksintö tarjoaa menetelmät, systeemit, tietokoneohjelmatuotteet ja laitteet uusiutuvan energian laitoksen tehorajoituksen kontrolloimiseksi osoittaa laitossaturaation ja silmukkaviiveen tilat. Esimerkinomaisessa suoritusmuodossa esillä olevan keksinnön tehorajoituksen kontrolli on jaettu kahteen kontrollitasoon: paikkalaajuisen todellisen tehon rajoituksen kontrolliin (24, 100), joka tuottaa yksittäisen paikkalaajuisen komennon, SitePlimCom (163, 329), kaikille invertereille, ja inverteri-tason tehon rajoituksen kontrolliin (330), joka tuottaa komennon, Inv[x].PlimCom[k] (313), yksittäisille invertereille. Joissain suoritusmuodoissa keksinnöllä on yksi tai useampi seuraavista piireistä: integraattori epälineaarisilla vahvistuksilla, rajoituspohjainen anti-windup (210), tehonrajoituskontrollitermi perustuen sekä integraattorin että myötäkytkettyjen termien summaan, ja inverteritason kontrolleri, joka on suunniteltu vastaamaan muutosnopeuden ja sähköverkon ylityksen rajoitteita.



METHOD AND SYSTEM OF POWER LIMIT CONTROL FOR A RENEWABLE ENERGY SITE

BACKGROUND OF THE INVENTION

5 Field of the Invention

[00001] The present invention is related to the control of power limits at renewable energy sites such as solar, wind, and tidal energy sites. More particularly, the present invention relates to renewable energy site power limit control that addresses conditions of plant saturation and loop delay.

10 Description of Related Art

[00002] Renewable energy sites are typically composed of multiple power conversion devices connected in parallel generating fixed frequency AC power to a grid. The devices are typically AC-AC or DC-AC inverters. Inverters are designed to extract maximum power from the renewable power supply, subject to a real power limit reference and often, a reactive power command.

[00003] A simple and common scenario is to configure inverters individually with the same fixed power limit and reactive power reference and allow them to operate independently to deliver power to the grid. The system is connected in terms of power output, but not control. However, this scenario tends to underutilize the total available power when it varies from inverter to inverter, and cannot regulate total site output power. Furthermore, such a system cannot regulate power at a point on the grid separated from the collective inverter outputs by grid impedance, especially when there are disturbances in grid voltage and load.

[00004] More recently, site controllers have been employed to reduce these problems by measuring site total power feedback to actively control site total power. As shown in

FIG. 1, the site control loop 10 consists of commands 23 from the controller 12 to the inverters 18 and feedbacks 21 from the inverters 18 and/or a utility meter 14 back to the site controller 12. The inverters 18 are connected to a power source 16 such as a photovoltaic (PV) module, and a step-up transformer 20 may intervene between the 5 inverters 18 and the power meter 14.

[00005] The site controller typically generates the site-wide inverter command by feeding the site power error between the feedback and reference through a PI or I controller to generate a site-wide power command and then divides that equally among inverters to generate a command to send to individual inverters. The inverter power 10 controllers are typically much faster than the site control loop. Therefore it is important to implement as much control functionality by the inverter itself, if possible.

[00006] However, traditional controllers can be improved by an effective means of handling non-ideal plant saturation and control loop delay, which cause many problems. The methods, systems, computer program products, and devices of the present invention 15 are designed to overcome the following challenges of non-ideal operation encountered when implementing site power limit control: 1) Poor utilization of available power near limits of inverter saturation when available power is unequally distributed among inverters. The plant can appear quite nonlinear in this situation and performance suffers if appropriate anti-windup and saturation-dependent integration techniques are not used; and 20 2) Degraded controller dynamic response due to significant delay in the communications channels used to implement the control loop. Loop delay can result in unnecessary integrator windup during reference slewing and general instability. It can also result in large site power overshoot in the case of a power surge if power limit commands are not distributed to inverters appropriately.

[00007] The first problem is that of quickly maximizing utilization of available power when it is unequally distributed among inverters. This might occur, for example, at a PV site on a cloudy day when the PV array is partially shaded. In this case, the total site output drops below the site power reference. To compensate, the present invention increases the 5 site-wide inverter power limit commands by incrementing an integrator which changes according to the error between the site power limit reference and site power feedback. The change in the integrator is chosen to take into account the number of inverters which are capable of producing more power. Furthermore, the present invention provides anti-windup techniques to prevent the integrator from increasing while no inverter is capable of 10 increasing power output.

[00008] The second problem is that of handling significant site control loop delay. This delay is due to site controller task time and communication latency. This delay greatly restricts controller bandwidth. To reduce dependence on loop delay, exemplary embodiments of the present invention employ a feed-forward term which provides a fast 15 transient response independent of feedback loop delay. The feed-forward term sums with an error term which maintains zero steady state error. Loop delay causes undesired integrator windup when the power limit reference changes. In the present invention the integrator error term is modified to compensate for this effect of loop delay. Another side effect of loop delay is that instantaneous site power surges cannot be corrected in less time 20 than the loop delay. Often, the utility imposes restrictions such as acceptable site power overshoot or site power positive slew rate. At a PV site, for example, available solar irradiance may change very quickly as clouds pass overhead. Although there is nothing that can be done to prevent a sudden drop in available power, a sudden increase in available power can be prevented, even if the site control loop is not quick enough to react

to it. The present invention addresses this issue by constraining each inverter power limit to be no larger than a certain margin higher than the inverter output power. This margin is the effective maximum real power overshoot that could be expected to an instantaneous surge in available inverter power. Incidentally, the margin also has the effect of slew 5 limiting inverter power up, which is typically desired.

[00009] Various power controllers exist, such as those described in U.S. Patent Nos. 8,260,469; 7,923,862; 7,890,217; and 6,512,966; as well as in U.S. Patent Application No. 2003/0006613 (which patents and publication are hereby incorporated by reference herein in their entireties), suffer from one or more of the limitations described herein. 10 There is a need for improved renewable energy plant control that addresses issues of site power saturation and loop delay so that dynamic performance can be significantly improved.

SUMMARY OF THE INVENTION

[00010] An objective of the present invention is to improve existing renewable energy 15 site power limit control systems subject to significant loop delay and site saturation.

[00011] The present invention achieves this objective by providing improved methods, systems, computer program products, and devices for providing power limit control at a renewable energy site. The invention addresses problems associated with site power saturation and loop delay by elements including an integrator with nonlinear gains, anti- 20 windup, a power limit control term based on the sum of an integrator and feed-forward term, and/or an inverter-level controller designed to meet slew rate and site power overshoot constraints.

[00012] In one embodiment, the present invention provides methods and systems of power limit control for a renewable energy site comprising one or more inverters, which

methods and systems comprise generating a site power limit command and an inverter power limit command.

[00013] In one embodiment, the site power limit command is generated by performing one or more of the following processes and operations: 1. calculating a feed-forward power limit reference $PlimRef[k]$ based on a power limit reference slew control process; 5 2. calculating a power limit integrator limit; 3. calculating a power limit integrator increment; 4. integrating an error integral term; 5. adding said power limit reference to said error integral term to yield said site power limit command; and 6. distributing said site power limit command among one or more inverters that are enabled at the site.

10 [00014] In another embodiment, the feed-forward power limit reference is compensated for loop delay.

[00015] In another embodiment, the inverter power limit command is generated by performing one or more of the following processes and operations: 1. calculating an inverter power limit command upper limit; 2. calculating an inverter power limit command 15 based on an inverter power limit command slew control process; and 3. detecting whether an inverter is open loop.

[00016] In another embodiment, the present invention provides a system for renewable energy power limit control, comprising a renewable energy power limit controller operable to perform one or more of the processes and operations according to the invention that are 20 described and/or depicted in this disclosure. In another embodiment, the system of the invention may further comprise one or more inverters in a two-way communication with said controller through a network. In another embodiment, the controller of the system of the invention may send a site power limit command or an individual inverter power limit command to the inverter or inverters. In another embodiment, the system may further

comprise a site meter in communication with the controller through the network. In another embodiment, the controller of the system of the invention receives site inverter power feedback from the site meter. In another embodiment of the system of the invention, the controller receives inverter power feedback from each inverter. In another embodiment, the 5 controller of the system of the invention comprises at least one processing element, a form of computer-readable memory, and a set of computer-executable instructions configured for performing one or more of the processes and operations according to the invention that are described and/or depicted in this disclosure. In another embodiment, the controller of the system of the invention is in further communication with a remote computer through 10 said network, wherein said remote computer is configured with an operator interface.

[00017] In another embodiment, the present invention provides a computer program product comprising a set of computer-executable instructions configured for performing one or more of the processes and operations according to the invention that are described and/or depicted in this disclosure that is storable in a form of computer-readable memory 15 or on a computer-readable medium.

[00018] In another embodiment, the present invention provides a device comprising at least one processing element, a form of computer-readable memory, and a set of computer-executable instructions configured for performing one or more of the processes and operations according to the invention that are described and/or depicted in this disclosure.

20 [00019] Specific embodiments of the invention provide a method of power limit control for a renewable energy site comprising generating a site power limit command by: (i) calculating a feed-forward power limit reference $PlimRef[k]$; (ii) integrating an error integral term; and (iii) adding said feed-forward power limit reference to said error integral term to yield a site-wide power limit command for a renewable energy site.

[00020] Such methods can also comprise dividing said site-wide power limit command by a number of enabled inverters at the site to obtain an individual inverter command.

[00021] Such methods can also comprise generating an individual inverter power limit command from either: i) a maximum based on said inverter power limit command and 5 feedback; or ii) a slew rate limit to said site-wide power limit command.

[00022] Any of these methods, and other methods disclosed herein, can also comprise steps of calculating a power limit integrator limit and calculating a power limit integrator increment.

[00023] Such methods can include steps wherein said calculating of said feed-forward 10 power limit reference is performed by a power limit reference slew control process comprising: (a) providing site inverter power feedback and an operator power limit reference; and (b) calculating said power limit reference based on: i) an upper limit based on said site inverter power feedback and said operator power limit reference; ii) a slew increment; and iii) said operator power limit reference.

[00024] Methods of the invention can comprise loop delay compensation of said feed-forward power limit reference. For example, said loop delay compensation in 15 embodiments can comprise offsetting said feed-forward power limit reference by the delay term D to yield $PlimRef[k-D]$. Further, for example, said loop delay compensation in embodiments can be implemented by setting the delay term D to a nonzero number 20 corresponding to the loop delay.

[00025] In embodiments, said power limit integrator limit can be calculated based on one or more or all of: i) said $PlimRef[k-D]$; ii) maximum inverter power feedback; iii) number of enabled inverters; and iv) site rated power limit reference.

[00026] Said power limit integrator increment can be calculated by multiplying a site power error and a gain which depends on a number of saturated inverters. For example, said power limit integrator increment in embodiments can be calculated by: i) subtracting site inverter power feedback from said PlimRef[k] or PlimRef[k-D] to generate an error term; and/or ii) scaling said error term by a saturation gain term and a constant Ki to generate a scaled error term.

[00027] In embodiments, said scaled error term can be integrated by: i) providing said scaled error term; and ii) integrating said scaled error term until it reaches said integrator limit to provide said error integral term.

10 [00028] In such methods of the invention, a windup of said scaled error term integration can be minimized by said integrator limit. Alternatively or in addition, windup of said scaled error term integration can be minimized by halting the integration when one or more or all applies: i) a number of inverters free to produce more power (NumInvFree) is zero; ii) said scaled error term reduces the absolute value of said integral term. Further, in 15 embodiments, said error integral term can be added to said PlimRef[k] to provide a numerator for said power limit command distribution.

[00029] According to aspects of the invention, methods can be configured such that said power limit command distribution comprises dividing said numerator by said number of enabled inverters to generate a distributed site power limit command.

20 [00030] Any of the methods described herein can involve calculating an inverter power limit command upper limit. For example, said inverter power limit command upper limit calculation can be a function of said inverter power limit command and inverter power feedback. Alternatively or in addition, said inverter power limit command can be calculated based on an inverter power limit command slew control process.

[00031] In embodiments, said slew control process can be a function of: i) said inverter power command upper limit; and/or ii) a slew rate limit, which is applied when the inverter is detected as open loop and is a function of a slew rate increment; and/or iii) said site power limit command. Said slew rate increment can be equal to an inverter power 5 rating when the inverter is not open loop.

[00032] An inverter according to methods and systems of the invention can be detected as open loop when said inverter power limit command is not equal to said site power limit command. Likewise, an inverter is not detected as open loop when said inverter power limit command is equal to said site power limit command.

10 [00033] Systems are also included within the scope of the invention, such as a system configured to perform one or more method or method steps described in this specification. For example, provided by embodiments of the invention is a system of power limit control for a renewable energy site comprising: one or more inverters; and a power limit controller in operable communication with at least one of the one or more inverters and operably 15 configured to generate a site power limit command by: (i) calculating a feed-forward power limit reference $P_{limRef}[k]$; and/or (ii) integrating an error integral term; and/or adding said feed-forward power limit reference to said error integral term to yield a site power limit command for a renewable energy site.

20 [00034] Such systems can comprise distributing said site power limit command among some or all of said one or more inverters which are enabled at the site.

[00035] Alternatively or in addition in such systems, the power limit controller can be operably configured to generate an inverter power limit command. Alternatively or in addition, the power limit controller can be operably configured to calculate a power limit integrator limit and to calculate a power limit integrator increment.

[00036] In systems of the invention, the at least one of said one or more inverters can be in two-way communication with said controller through a network.

[00037] Alternatively or in addition, said controller of such systems can be operably configured to send a site power limit command or an individual inverter power limit 5 command to at least one of or all of said one or more inverters.

[00038] Optionally, in system embodiments of the invention, the systems can comprise a site meter in communication with said controller through said network. In any of the systems described herein, said controller can be operably configured to receive site inverter power feedback from said site meter. In system embodiments, said controller can be 10 operably configured to receive inverter power feedback from each of said one or more or all inverters. Additionally, systems of the invention can comprise any number of inverters.

[00039] In system embodiments of the invention, said controller can comprise: i) at least one processing element; ii) a form of computer-readable memory; and iii) a set of computer-executable instructions configured for performing one or more method or one or 15 more method step described in this specification. Additionally or alternatively, the controller can be in communication with a remote computer through said network, wherein said remote computer is configured with an operator interface.

[00040] Also included within the scope of the invention is a computer program comprising a set of computer-executable instructions operably configured for performing 20 one or more method described in this specification, which program is storable in a form of computer-readable memory or on a computer-readable medium.

[00041] Included within the scope of the invention is a device comprising: i) at least one processing element; ii) a form of computer-readable memory; and iii) a set of

computer-executable instructions configured for performing one or more method or one or more method steps described in this specification.

[00042] Methods of the invention can alternatively or in addition comprise a method of power limit control for a renewable energy site, said method comprising Loop Delay compensation wherein said Loop Delay compensation is implemented by setting delay term D to a nonzero number corresponding to loop delay in either of two cases: (i) subtracting present inverter feedback signal from a corresponding reference generated LoopDelay seconds prior in order to compute an integration error increment; or (ii) basing inverter level power limit maximum on an inverter command generated LoopDelay 10 seconds prior.

[00043] Additional embodiments of the invention and aspects of these embodiments will become apparent in the foregoing detailed description.

BRIEF DESCRIPTION OF THE DRAWINGS

[00044] FIG. 1 is a schematic diagram of a renewable energy site control loop.

15 [00045] FIG. 2 is a block diagram of a renewable energy site real power limit control.

[00046] FIG. 3A is a block diagram showing an overview of an exemplary embodiment of renewable energy site real power limit control according to the invention which depicts the following processes: power limit reference slew control, power limit integrator limit control, power limit integrator increment calculation, power limit integration, and power 20 limit command distribution.

[00047] FIG. 3B is a block diagram showing an expanded view of an exemplary embodiment of the power limit reference slew control process shown in FIG. 3A.

[00048] FIG. 3C is a block diagram showing an expanded view of an exemplary embodiment of the power limit integrator limit control process shown in FIG. 3A.

[00049] FIG. 3D is a block diagram showing an expanded view of an exemplary embodiment of the power limit integrator increment calculation process shown in FIG. 3A.

5 [00050] FIG. 3E is a block diagram showing an expanded view of an exemplary embodiment of the power limit integration process shown in FIG. 3A.

[00051] FIG. 3F is a block diagram showing an expanded view of an exemplary embodiment of the power limit command distribution process shown in FIG. 3A.

10 [00052] FIG. 4 is a block diagram showing an exemplary embodiment of an integrator anti-windup process according to the invention.

[00053] FIG. 5A is a block diagram showing an overview of an exemplary embodiment of renewable energy site inverter real power control according to the invention which depicts the following processes: inverter power limit command upper limit computation, inverter power limit command slew control, and open loop detection.

15 [00054] FIG. 5B is a block diagram showing an expanded view of an exemplary embodiment of an inverter power limit command upper limit computation process shown in FIG. 5A.

[00055] FIG. 5C is a block diagram showing an expanded view of an exemplary embodiment of an inverter power limit command slew control process shown in FIG. 5A.

20 [00056] FIG. 5D is a block diagram showing an expanded view of an exemplary embodiment of an open loop detection process shown in FIG. 5A.

[00057] FIG. 6 is a schematic diagram showing an embodiment of a renewable energy site system according to the invention wherein a main site controller is configured for controlling a plurality of inverters through a network.

DETAILED DESCRIPTION OF
5 VARIOUS EMBODIMENTS OF THE INVENTION

[00058] Reference will now be made in detail to various exemplary embodiments of the invention. However, the embodiments described in the description and shown in the figures are illustrative only and are not intended to limit the scope of the invention, and
10 changes may be made in the specific embodiments described in this specification and accompanying drawings that a person of ordinary skill in the art will recognize are within the scope and spirit of the invention.

[00059] Conventional power limit controllers produce a single site power limit command, PLimCom, that is distributed equally across inverter controllers. A typical
15 control process is shown in block diagram form in FIG. 2. As shown in FIG. 2, the typical control process 24 calculates 26 a feed-forward power limit reference (PlimRef) 35 based on inputs such as site maximum power (SiteMaxPower) 29, site slew increment (SiteSlewInc) 31, and operator power limit reference (OperatorPlimREF) 33. A power limit (Plim) integrator increment process 36 first subtracts 37 site inverter power feedback 20 (SiteInvPowerFB) 39 from the feed-forward term 35. Then, the difference, total site power error 41 is multiplied 45 by the integrator time constant, Ki 43. The product 47 of this multiplication (the power limit integrator increment) is then added to e_sum 49 by the Plim integrator 48 to generate new summed error term (e_sum) 51. PlimRef 35, which may bypass 53 the integrator 48 or be used as input to the integrator increment 36, is added 55
25 to e_sum 51, the sum 57 of which is the power limit command, which is then processed by

the Plim command distribution. This occurs by first dividing 61 the power limit command 57 by inverted rated power 59. The quotient 63 from this division 61 and a maximum inverter power upper limit 65 are used to calculate 67 the distributed power limit command (PLimCom) 69.

5 [00060] In the case of conventional controllers, the power error is processed by an integral (I) controller or other integral-type controllers such as proportional-integral (PI) or proportional-integral-derivative (PID). FIG. 2 shows an integrator implementation, but could be modified for PI or PID as well. The reference is slewed to provide a feed-forward power limit reference 35, which may be used in place of the integrator output 51 but not at 10 the same time.

[00061] The present invention builds upon the traditional power limit controller to provide a method of power limit control to a renewable energy plant that overcomes difficulties commonly found in conventional controllers. One major issue present in conventional controllers is poor utilization of available power near limits of inverter 15 saturation when available power is unequally distributed among inverters. The plant can appear quite nonlinear in this situation and performance suffers if appropriate anti-windup and saturation-dependent integration techniques are not used. Another issue is degraded controller dynamic response due to significant delay in the communications channels used to implement the control loop. Loop delay can result in unnecessary integrator windup 20 during reference slewing and general instability. It can also result in large site power overshoot in the case of a power surge if power limit commands are not distributed to inverters appropriately.

[00062] The present invention overcomes these limitations by providing methods, systems, computer program products, and devices, which in some embodiments have one

or more of the following: an integrator with nonlinear gains, limit based anti-windup, a power limit control term based on the sum of both integrator and feed-forward terms, and an inverter-level controller designed to meet slew rate and site power overshoot constraints. In an exemplary embodiment, power limit control of the present invention is 5 broken into two levels of control: a site-wide real power limit control that produces a single site-wide command, SitePlimCom, for all inverters, and an inverter-level power limit control that outputs a command, Inv[x].PlimCom[k], for individual inverters.

[00063] Reference will now be made to block diagrams that illustrate the processes and operations of methods, systems, devices, and/or computer program products according to 10 exemplary embodiments of the invention. However, there may be variations in the order of these operations, elimination of one or more operations, or substitution or addition of one or more new operations, that fall within the scope of the invention as appreciated by a skilled artisan.

[00064] FIGS. 3A-3F show these processes and operations. FIG. 3A shows an overview 15 of an embodiment of site real power limit control 100 according to the invention. The site real power limit control 100 includes the following processes: power limit (Plim) reference slew control 110, Plim integrator limit control 130, Plim integrator increment calculation 140, Plim integration 150, and Plim command distribution 160. As will be described in further detail, the Plim reference slew control 110 calculates a feed-forward 20 power limit reference that is used as input for the Plim integrator increment process 140 to calculate an integrator increment which is integrated by the Plim integration process 150 to calculate an error integral (or summed error term) that is limited by a value that is set by the Plim integrator limit control 130. The feed-forward power limit reference and error integral are summed and used to generate the distributed Plim command 160 by dividing

by the number of enabled inverters on the site. As described in this specification, “enabled inverters” refer to those inverters which are both online and running. This command equally distributes the site power limit command among operating inverters. Certain exemplary embodiments of these processes and their operations and their interconnection 5 will be further described below.

[00065] Reference will now be made to FIG. 3B which shows an embodiment of the feed-forward reference slew control process 110 according to the invention. The following merely describes one example of a feed-forward power limit reference generating process. For example, in one embodiment, the feed-forward reference PlimRef[k] 123 may be 10 calculated 122 by first slewing the operator power limit reference, as shown in FIG. 3B wherein 111 represents the operator power limit reference (OperatorPlimRef). The slew rate may be set by a slew increment 117, which is the maximum amount that the power limit may change in one control period. Second, an upper limit 115 may be imposed on the site power limit reference 123. This upper limit 115 is the maximum 114 of site power 15 feedback (SiteInvPowerFB) 113 and OperatorPlimRef 111. The lower limit 121 may be set at zero. This upper limit 115 eliminates unnecessary slew time and associated windup in some cases by allowing the feed-forward reference 123 to immediately drop to near the point at which the site power will begin to reduce with respect to the power limit reference 123. The feed-forward term 123 is designed to improve the power limit command response 20 to a change in site power reference under ideal conditions when the integrator is a small fraction of the total command. In such cases, the effects of the feedback loop, and hence loop delay, are minimized. The feed-forward reference 123 may be time delayed 124 by the delay term D to generate a delayed feed-forward reference PlimRef[k-D] 125.

[00066] Since the power limit controller often operates at available site power limits, saturation compensating techniques such as integrator anti-windup are essential for quick transitions in and out of site power limits. In one embodiment, shown in FIG 3C, anti-windup may be implemented by imposing an upper limit 139 e_{sum_max} on the integrator 150 by the Plim integrator limit control process 130. For example, the integrator upper limit 139 may be calculated 138 as a function of four terms: 1) PlimRef[k-D] 125, which may represent the delayed feed-forward reference if D is not 0; 2) Maximum inverter power feedback (PMAX) 133; 3) Number of enabled inverters (NumInvEn) 135; and 4) Site rated power limit reference (OperatorSiteRatedPower) 137.

[00067] The integrator limit 139 of the present invention prevents the integrator from increasing more than a certain fraction of the site rated power beyond the command for which no inverters are capable of producing more real power. By minimizing integrator windup in this way, the controller dynamic performance is dramatically improved for operation near the site available power limit.

[00068] Turning to FIG. 3D, an embodiment of a process in which a Plim integrator increment is calculated 140 is shown. In this embodiment, the increment may be calculated as follows. First, the site power error 143 is calculated by subtracting 142 site total power feedback 113 from the power limit reference 123. The error 143 may be multiplied 148 by a gain composed of a constant, K_i 147, and a varying term called the SaturationGain 145.

“ K_i ” as referred to in this specification is the classical integrator gain term typically used in PI or I controllers, and sets the time constant of the integrator. The SaturationGain term 145 may be computed based on the number of inverters currently not saturated, that is, capable of generating more real power. This time-varying SaturationGain term 145 is key

to quickly maximizing site available power when available power is limited and unequally distributed among inverters.

[00069] As shown in FIGS. 3E and 3F which depict the power limit integration process 150 and the power limit command distribution process 160 the scaled error 149 is 5 then summed 152 with the error sum (e_sum) wherein the upper limit 139 is set 154 to this sum 153 and the resulting error integral (e_sum) 155 is summed 156 with the power limit reference 123 to generate a sum 157, the site-wide power limit command, which is divided 162 by NumInvEn 135 to yield the distributed site power limit command (SitePLimCom) 163.

10 [00070] In another embodiment, anti-windup is implemented by simply halting the integrator when either all inverters are either deemed saturated, such that the number of inverters free to produce more power (NumInvFree) is zero, or the error is reducing the absolute value of the integral. In this case, the anti-windup would appear as FIG. 4 which shows an error integrator 200 with integral anti-windup 210. As shown in FIG. 4, the 15 error 249 is summed 202 in either two situations (as indicated by OR operator, 224): situation 221 when the number of free inverters 219 is greater than 220 zero 217 or situation 223 when upon conversion 212 and 214 of the summed errors 203 and 205 to absolute values, A 213 and B 215, A is less than B 216. When either of these conditions is present 225, error_sum is generated 205, if not, the integrator is halted 204.

20 [00071] FIG. 5A shows an overview of an embodiment of inverter-level real power control 300 for generating a command for individual inverters. The inverter-level real power control 300 may comprise one or more of the following processes: inverter PlimCom upper limit computation 310, inverter PlimCom slew/limit control 320, and open

loop detection 340. These processes and their operations will be shown in the following figures.

[00072] As shown in FIGS. 5B and 5C, the site-wide inverter command of the present invention may then be further constrained at the inverter level. For example, to reduce site overshoot and rapid site power increase in the case of dramatic disturbances in available power, the site-wide inverter command 329 may be further constrained at the inverter level by either of two limits: 1) As shown in FIG. 5B, an upper limit PlimMax 315 which may be a function 314 of the inverter power feedback Inv[x].Power 311 and the current power limit command Inv[x].PlimCom[k-D] 313. The upper limit 315 is preferably designed to 5 limit the worst case site-wide overshoot that might result from a steep disturbance in site available power. The lower limit as shown in FIG. 5C is set to zero 331. 2) As shown in 10 FIG. 5C, a slew rate limit 327 which may be calculated 326 only when the inverter is considered open loop Inv[x].OL 321. As used in this specification and shown in the open 15 loop detection process 340 of FIG. 5D, an inverter is deemed “open-loop” when its command 333 is not equal to the site-wide inverter command (SitePLimCom) 329. While open loop 321, the inverter command 333 will preferably slew toward the site-wide inverter command 329 until it reaches it, at which point 342 the inverter is deemed not 20 open loop (or closed loop). When not open loop, the inverter power limit command slew rate limit 327 is preferably released by applying a slew increment InvSlewInc 325 equal to the inverter power rating InvRatedPower 323.

[00073] Another advantage of the present invention, only some of which are described herein, is it provides a simple method for correcting problems introduced by loop delay. Delay presents a major challenge to any control loop. In practice, there is a delay of a few seconds from site controller power limit command output to inverter power feedback. One

technique for handling delay is computing a feed-forward control term based on known information about the site. The site power limit feed-forward term, for example, assumes that, under ideal conditions, the inverters will be able to track their power limit commands within one control period. This way, under ideal conditions the integrator will not have to 5 wind up at all and thus dependence on the feedback loop and any associated delay has been eliminated.

[00074] Ideally, delay compensation would involve predicting future inverter states by extrapolating current ones and using this information to optimally distribute power commands to the site. The present invention, in one embodiment, provides a simpler 10 technique to ensure that comparisons between commands and feedbacks use the command generated at the time $t - \text{LoopDelay}$ rather than the present command. This technique is effective at improving dynamic performance when the command signal is varying with time. As shown in FIG. 5D, this feature is preferably implemented by setting the D offset to the loop delay to implement a time delay block 324 and corresponding delayed signal 15 (Inv[x].PlimCom[k-D]) 313. To turn the feature off, D would be set to 0. This may be used in two places in the controller as shown by the following examples. First, the power limit reference, PlimRef[k-D], supplying the error term in the site power limit controller is replaced by PlimRef[k-LoopDelay/Ts], or just PlimRef[k-LoopDelay], since the control period, Ts, is 1 second in this application. This prevents unnecessary windup which can 20 occur while slewing the reference while the site is not saturated. Such a case should not require integrator windup, since the inverters track their commands with high precision. Second, the power limit command, Inv[x].PlimCom[k-D] 313, entering the power limit command maximum block 314 with inverter feedback power, Inv[x].Power 311 as shown

in FIG. 5B, is replaced by Inv[x].PlimCom[k-LoopDelay]. This change leads to more stable and reliable site power limit control.

[00075] It will be understood that the various processes and operations of the Site Real Power Limit Control 100 and Inverter Real Power Control 300 described and/or depicted herein may be carried out by a group of computer-executable instructions that may be organized into routines, subroutines, procedures, objects, methods, functions, or any other organization of computer-executable instructions that is known or becomes known to a skilled artisan in light of this disclosure, where the computer-executable instructions are configured to direct a computer or other data processing device to perform one or more of the specified processes and operations. Embodiments of the invention include one or more computers or devices, or systems with such computers or devices, loaded with a set of the computer-executable instructions described herein wherein the one or more computers or devices are instructed and configured to carry out the processes and operations of the invention. The computer or device performing the specified processes and operations may comprise at least one processing element such as a central processing unit and a form of computer-readable memory which may include random-access memory (RAM) or read-only memory (ROM). The computer-executable instructions can be embedded in computer hardware or stored in the computer-readable memory such that the computer or device may be directed to perform one or more of the processes and operations depicted in the block diagrams and/or described herein. Embodiments of the invention also include a computer program product comprising one or more computer files comprising a set of computer-executable instructions for performing one or more of the processes and operations described and/or depicted herein. In exemplary embodiments, the files may be stored contiguously or non-contiguously on a computer-readable medium, in computer-readable

memory on a single computer, or distributed across multiple computers. A skilled artisan will further appreciate, in light of this disclosure, how the invention can be implemented, in addition to software, using hardware or firmware. As such, as used herein, the operations of the invention can be implemented in a system comprising any combination of 5 software, hardware, or firmware.

[00076] In an exemplary embodiment, a single computer or device may be configured at a renewable energy site to serve as a single Main Site Controller which receives feedbacks from the inverters and the site power meter and sends the site real power limit command and the inverter real power limit command through a network such as shown in FIG. 1 to 10 one or more inverters of the renewable energy site. For example, FIG. 6 shows an embodiment of a renewable energy site system 400 according to the invention comprising a plurality of stations 410. Here the stations 410 are referred to as Solar Ware® stations, each station comprising at least one inverter 420. As illustrated, the stations 410 each comprise at least two inverters 420. In the context of the invention, there is no limit to the 15 number of Solar Ware® stations that can be included in site system 400. The Solar Ware® stations 410 of system 400 may be interconnected using Ethernet connectivity wherein data is transmitted between stations through a Modbus TCP protocol 430. Commands and feedbacks may be sent to and from the inverters through a network interface such as an Ethernet switch 440. However, any suitable network protocol, including IP, UDP, or 20 ICMP, as well any suitable wired or wireless network including any local area network, Internet network, telecommunications network, Wi-Fi enabled network, or Bluetooth enabled network may be used. The Main Site Controller 450 may be configured at one Solar Ware® station 410 to control the inverters 420 as well as receive inputs from the inverters 420 and from the site meter. The Main Site Controller 450 may allow an operator

to control the power at the renewable energy site through an operator interface which may be a graphical user interface (GUI) which may be present at the Main Site Controller itself or be presented as an HTTP webpage 460 that may be accessed by the operator at a remote general purpose computer with a processor, computer-readable memory, and standard I/O 5 interfaces such as a universal serial bus (USB) port and a serial port, a disk drive, a CD-ROM drive, as well as one or more user interface devices including a display, keyboard, keypad, mouse, control panel, touch screen display, microphone, etc. for interacting with the Main Site Controller through the GUI. The Main Site Controller 450 may be used to control the power limits of any renewable energy site employing one or more inverters that 10 is connected to the public power grid, including but not limited to solar (photovoltaic), wind, and tidal energy sites.

[00077] The present invention has been described with reference to particular embodiments having various processes and operations. It will be apparent to those skilled in the art that various modifications and variations can be made in the practice of the 15 present invention without departing from the scope or spirit of the invention. For example, in other embodiments, the power error may be processed by other types of integration processes such as PI or PID. Further, one skilled in the art will recognize that these processes and operations may be used singularly or in any combination, or one or more may be eliminated or substituted with an equivalent operation or process based on the 20 requirements and specifications of a given application or design. Other embodiments of the invention will be apparent to those skilled in the art from consideration of the specification and practice of the invention. The description of the invention provided is merely exemplary in nature and, thus, variations that do not depart from the essence of the invention are intended to be within the scope of the invention.

CLAIMS

1. A method of power limit control for a renewable energy site characterized by generating a distributed site power limit command (163) by:

calculating (122) a feed-forward power limit reference PlimRef[k] (123);

5 calculating (138) a power limit integrator limit (139);

integrating (150) an error integral term (155) that is limited by the power limit integrator limit (139);

adding (156) said feed-forward power limit reference PlimRef[k] (123) to said error integral term (155) to yield a site-wide power limit command (157) for the renewable

10 energy site; and

dividing (162) said site-wide power limit command (157) by a number of enabled inverters (135) at the site to obtain the distributed site power limit command (163).

2. The method of claim 1 comprising generating an individual inverter power limit command (333) from either: i) a inverter power limit command upper limit (315) based on 15 said inverter power limit command and feedback; or ii) a slew rate limit (327) to said site-wide power limit command (157).

3. The method of claim 1 comprising calculating a power limit integrator increment (149).

4. The method of claim 1, wherein said calculating (122) of said feed-forward power 20 limit reference PlimRef[k] (123) is performed by a power limit reference slew control process (110) comprising:

providing site inverter power feedback (113) and an operator power limit reference (111); and

calculating (122) said feed-forward power limit reference $\text{PlimRef}[k]$ (123) based on:

- i) an upper limit (115) based on said site inverter power feedback (113) and said operator power limit reference (111);
- 5 ii) a slew increment (117); and
- iii) said operator power limit reference (111).

5. The method of claim 1 comprising loop delay compensation of said feed-forward power limit reference $\text{PlimRef}[k]$ (123), wherein said loop delay compensation comprises offsetting (124) said feed-forward power limit reference by a delay term D to yield

10 $\text{PlimRef}[k-D]$ (125).

6. The method of claim 5, wherein said loop delay compensation is implemented by setting the delay term D to a nonzero number corresponding to the loop delay.

7. The method of claim 1, wherein said power limit integrator limit (139) is calculated (138) based on one or more of:

- 15 i) a $\text{PlimRef}[k-D]$ (125);
- ii) maximum inverter power feedback (133);
- iii) number of enabled inverters (135); and
- iv) site rated power limit reference (137).

8. The method of claim 3 wherein said power limit integrator increment (149) is calculated (140) by performing one or more of: i) multiplying a site power error and a gain which depends on a number of saturated inverters; or ii) subtracting (142) site inverter power feedback (113) from said $\text{PlimRef}[k]$ (123) or $\text{PlimRef}[k-D]$ (125) to generate an error term (143) and scaling (148) said error term (143) by a saturation gain term (145) and

a constant K_i (147) to generate a scaled error term which is the power limit integrator increment (149).

9. The method of claim 8, comprising integrating (150) said power limit integrator increment (149) until it reaches an integrator limit (139) to provide said error integral term 5 (155).

10. The method of claim 9, wherein windup of said power limit integrator increment (149) integration is minimized: i) by said integrator limit (139); or ii) by halting the integrating (150) when one or more applies: (a) a number of inverters free to produce more power (NumInvFree) (219) is zero; or (b) said power limit integrator increment (149) 10 reduces the absolute value of said error integral term (155).

11. The method of claim 1, wherein an inverter power limit command upper limit (315) is calculated (314) as a function of a current inverter power limit command (313) and an inverter power feedback (311).

12. The method of claim 1, wherein an inverter power limit command is calculated

15 based on an inverter power limit command slew control process as a function of:

- i) said inverter power limit command upper limit (315); or
- ii) a slew rate limit (327), which is applied when the inverter is detected as open loop and is a function of a slew rate increment (325); and
- iii) said site-wide power limit command (329).

20 13. The method of claim 12, wherein said slew rate increment (325) is equal to an inverter power rating (323) when the inverter is not open loop.

14. A system of power limit control for a renewable energy site characterized by: one or more inverters (18); and

a power limit controller (12) in operable communication with at least one of the one or more inverters (18) and operably configured to generate a distributed site power limit command (163) by:

- calculating (122) a feed-forward power limit reference $PlimRef[k]$ (123);
- 5 calculating (138) a power limit integrator limit (139);
- integrating (150) an error integral term (155), wherein the error integral term (155) is limited by the power limit integrator limit (139);
- 10 adding (156) said feed-forward power limit reference $PlimRef[k]$ (123) to said error integral term (155) to yield a site-wide power limit command (157) for the renewable energy site; and
- dividing (162) said site-wide power limit command (157) by a number of enabled inverters (135) at the site to obtain the distributed site power limit command (163).

15. The system of claim 14 comprising distributing said site power limit command 15 among some or all of said one or more inverters (18) which are enabled at the site.

16. The system of claim 14 wherein the power limit controller (12) is operably configured to generate an individual inverter power limit command (333).

PATENTTIVAATIMUKSET

1. Menetelmä uusiutuvan energian voimalan tehonrajoituksen ohjaamiseksi, **tunnettua** siitä, että muodostetaan hajautettu voimalan tehon rajoituskäsky (163):

5 laskemalla (122) myötäkytkentäinen tehonrajoituksen asetusarvo PlimRef[k] (123);
laskemalla (138) tehonrajoitusintegraattorin raja-arvo (139);
integroimalla (150) tehonrajoitusintegraattorin raja-arvon (139) rajoittama virhe-integraalitermi (155);
lisäämällä (156) mainittu myötäkytkentäinen tehonrajoituksen asetusarvo PlimRef[k] (123) mainittuun virheintegraalitermiin (155), jolloin uusiutuvan energian 10 voimalalle saadaan voimalanlaajuinen tehonrajoituskäsky (157); ja
jakamalla (162) mainittu voimalanlaajuinen tehonrajoituskäsky (157) voimalassa olevien aktivoitujen inverttereiden (135) lukumäärällä voimalan tehon hajautetun rajoituskäskyn (163) saamiseksi.

2. Patenttivaatimuksen 1 mukainen menetelmä, jossa muodostetaan yksittäinen

15 invertterin tehonrajoituskäsky (333) joko:

i) invertterin tehonrajoituskäskyn yläraja-arvosta (315) mainitun invertterin tehonrajoituskäskyn ja takaisinkytkenän perusteella; tai
ii) muutosnopeuden raja-arvosta (327), mainituksi voimalanlaajiseksi tehonrajoituskäskyksi (157).

20 3. Patenttivaatimuksen 1 mukainen menetelmä, jossa lasketaan

tehonrajoitusintegraattorin lisäys (149).

4. Patenttivaatimuksen 1 mukainen menetelmä, jossa mainittu myötäkytkentäisen tehonrajoituksen asetusarvon PlimRef[k] (123) laskenta (122) suoritetaan tehonrajoituksen asetusarvon muutoksen ohjausprosessin (110) avulla, jossa:

järjestetään saataville voimalan invertteritehon takaisinkytkentä (113) ja käyttäjän syöttämä tehonrajoituksen asetusarvo (111); ja lasketaan (122) mainittu myötäkytkentäinen tehonrajoituksen asetusarvo PlimRef[k] (123) seuraavien perusteella:

5 i) yläraja (115), joka perustuu mainittuun voimalan invertteritehon takaisinkytentäään (113) ja mainittuun käyttäjän syöttämään tehonrajoituksen asetusarvoon (111);

ii) muutoslisäys (117); ja

iii) mainittu käyttäjän syöttämä tehonrajoituksen asetusarvo (111).

10 5. Patenttivaatimuksen 1 mukainen menetelmä, joka käsittää mainitun myötäkytkentäisen tehonrajoituksen asetusarvon $\text{PlimRef}[k]$ (123) silmukkaviiveen kompensoinnin, joka silmukkaviiveen kompensointi käsittää mainitun myötäkytkentäisen tehonrajoituksen asetusarvon muuttamisen (124) viivetermin D verran, arvon $\text{PlimRef}[k-D]$ (125) saamiseksi.

15 6. Patenttivaatimuksen 5 mukainen menetelmä, jossa mainittu silmukkaviiveen kompensointi tehdään asettamalla viivetermi D luvuksi, joka on erisuuri kuin nolla ja joka vastaa silmukkaviivettä.

7. Patenttivaatimuksen 1 mukainen menetelmä, jossa mainittu tehonrajoitusintegraattorin raja-arvo (139) lasketaan (138) yhden tai useamman seuraavista 20 perusteella:

i) $\text{PlimRef}[k-D]$ (125);

ii) invertteritehon maksimaalinen takaisinkytentäarvo (133);

iii) aktivoitujen inverttereiden (135) lukumäärä; ja

iv) voimalakohtainen tehonrajoituksen asetusarvo (137).

8. Patenttivaatimuksen 3 mukainen menetelmä, jossa mainittu tehonrajoitusintegraattorin lisäys (149) lasketaan (140) suorittamalla yksi tai useampi seuraavista laskutoimituksista: i) kertomalla keskenään voimalan tehon virhe ja vahvistus, joka riippuu saturoituneiden inverttereiden lukumäärästä; tai ii) vähentämällä (142) voimalan 5 invertteritehon takaisinkytkevä (113) mainitusta arvosta $PlimRef[k]$ (123) tai $PlimRef[k-D]$ (125) virhetermin (143) muodostamiseksi ja skaalaamalla (148) mainittu virhetermin (143) saturaation vahvistustermillä (145) ja vakiolla Ki (147) skaalatun virhetermin muodostamiseksi, joka on tehonrajoitusintegraattorin lisäys (149).

9. Patenttivaatimuksen 8 mukainen menetelmä, jossa integroidaan (150) mainittua 10 tehonrajoitusintegraattorin lisäystä (149), kunnes se saavuttaa integraattorin raja-arvon (139), mainitun virheintegraalitermin (155) saamiseksi.

10. Patenttivaatimuksen 9 mukainen menetelmä, jossa mainitun tehonrajoitusintegraattorin lisäyksen (149) windupia minimoidaan: i) mainitun integraattorin raja-arvon (139) avulla; tai ii) pysäytämällä integrointi (150), kun yksi tai 15 useampi seuraavista pätee: (a) niiden inverttereiden (219) lukumäärä ($NumInvFree$), jotka ovat vapaana tuottamaan lisää tehoa, on nolla; tai (b) mainittu tehonrajoitusintegraattorin lisäys (149) pienentää mainitun virheintegraalitermin (155) absoluuttista arvoa.

11. Patenttivaatimuksen 1 mukainen menetelmä, jossa lasketaan (314) invertterin tehon rajoituskäskyn yläraja-arvo (315) kulloisenkin invertteritehon rajoituskäskyn (313) ja 20 invertteritehon takaisinkytkenän (311) funktiona.

12. Patenttivaatimuksen 1 mukainen menetelmä, jossa invertterin tehon rajoituskäsky lasketaan invertterin tehon rajoituskäskyn muutoksen ohjausprosessin perusteella:

i) mainitun invertterin tehon rajoituskäskyn yläraja-arvon (315) funktiona; tai

ii) muutosnopeuden raja-arvon (327) funktiona, jota sovelletaan, kun invertteri havaitaan avoimeksi silmukaksi, ja joka on muutosnopeuden lisäyksen (325) funktio; ja

iii) mainitun voimalanlaajuisen tehonrajoituksen asetusarvon (329) funktiona.

13. Patenttivaatimuksen 12 mukainen menetelmä, jossa mainittu muutosnopeuden lisäys (325) on yhtä kuin invertterin nimellisteho (323), kun invertteri ei ole avoin silmukka.

14. Järjestelmä uusiutuvan energian voimalan tehonrajoituksen ohjaamiseksi, joka järjestelmä on **tunnettu**:

yhdestä tai useammasta invertteristä (18); ja

10 tehonrajoituksen säätimestä (12), joka on toiminnallisessa yhteydessä ainakin yhden mainitun yhden tai useaman invertterin (18) kanssa ja on järjestetty toiminnallisesti muodostamaan hajautettu voimalan tehonrajoituskäsky (163):

laskemalla (122) myötäkytkentäinen tehonrajoituksen asetusarvo

PlimRef[k] (123);

15 laskemalla (138) tehonrajoitusintegraattorin raja-arvo (139);

integroimalla (150) virheintegraalitermi (155), jota virheintegraalitermiä (155) tehonrajoitusintegraattorin raja-arvo (139) rajoittaa;

lisäämällä (156) mainittu myötäkytkentäinen tehonrajoituksen asetusarvo

PlimRef[k] (123) mainittuun virheintegraalitermiin (155), voimalanlaajuisen

20 tehonrajoituskäskyn (157) saamiseksi uusiutuvan energian voimalalle; ja

jakamalla (162) mainittu voimalanlaajuisen tehonrajoituskäsky (157) voimalassa olevien aktivoitujen inverttereiden (135) lukumäärällä voimalan tehon hajautetun rajoituskäskyn (163) saamiseksi.

15. Patenttivaatimuksen 14 mukainen järjestelmä, jossa hajautetaan mainittu voimalan tehon rajoituskäsky joidenkin tai kaikkien mainittujen yhden tai useamman invertterin (18) kesken, jotka on aktivoitu voimalassa.
16. Patenttivaatimuksen 14 mukainen järjestelmä, jossa tehonrajoitussäädin (12) on 5 järjestetty toiminnallisesti muodostamaan yksittäinen invertterin tehonrajoituskäsky (333).

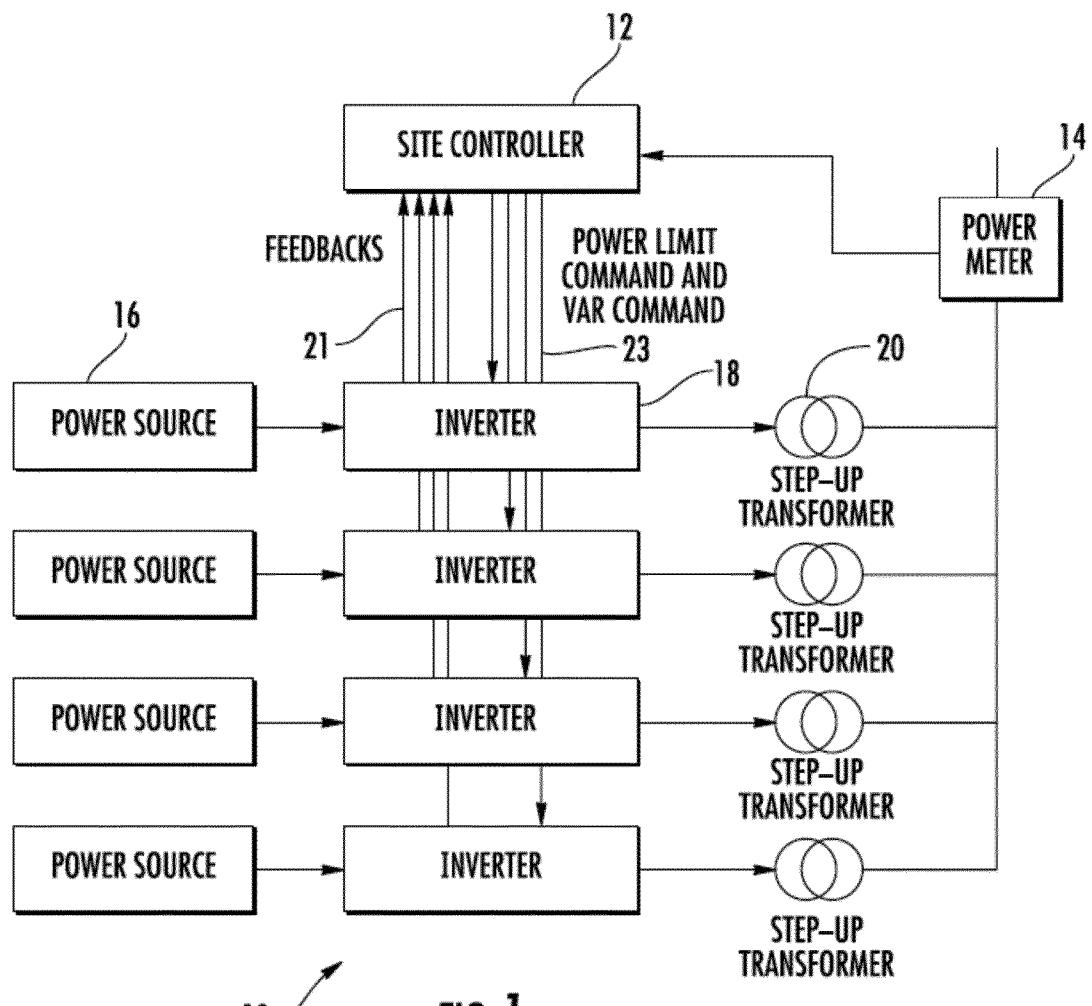


FIG. 1

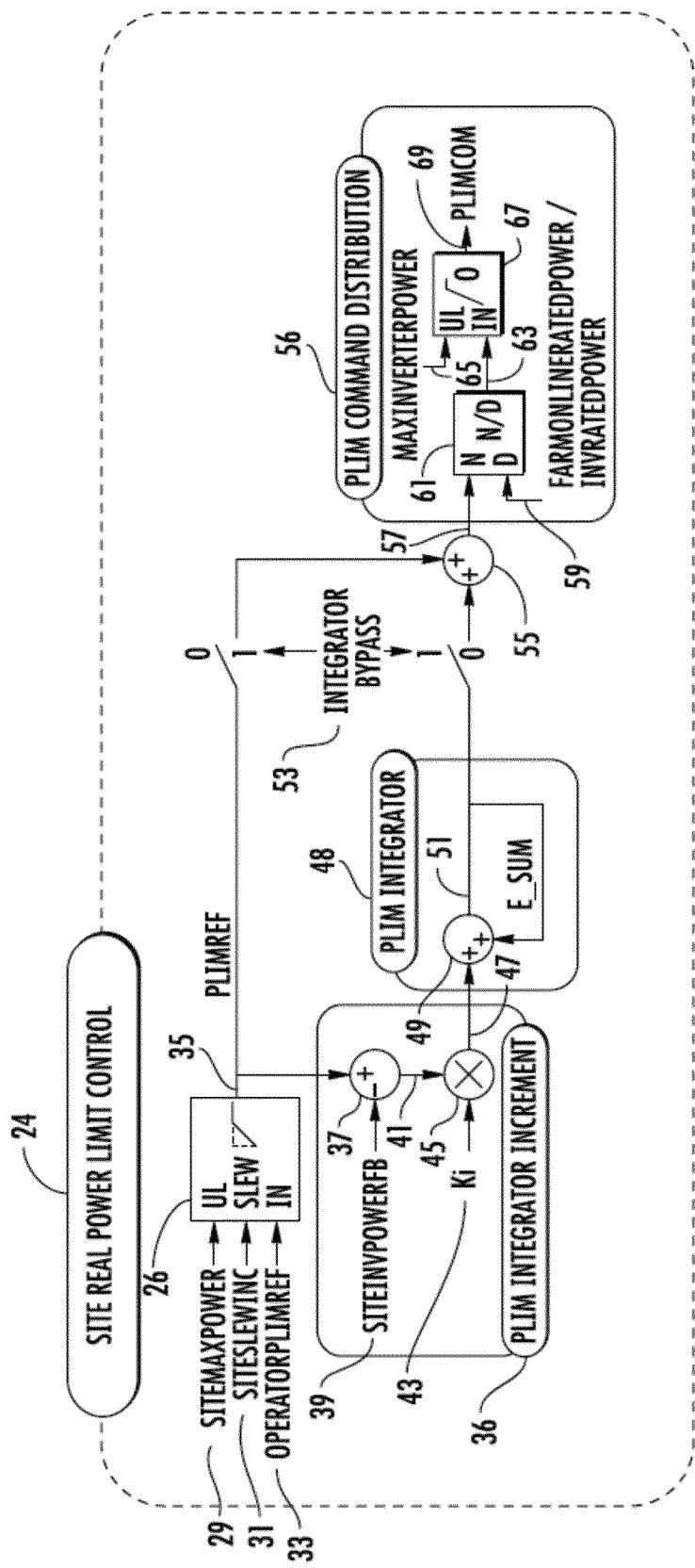


FIG. 2

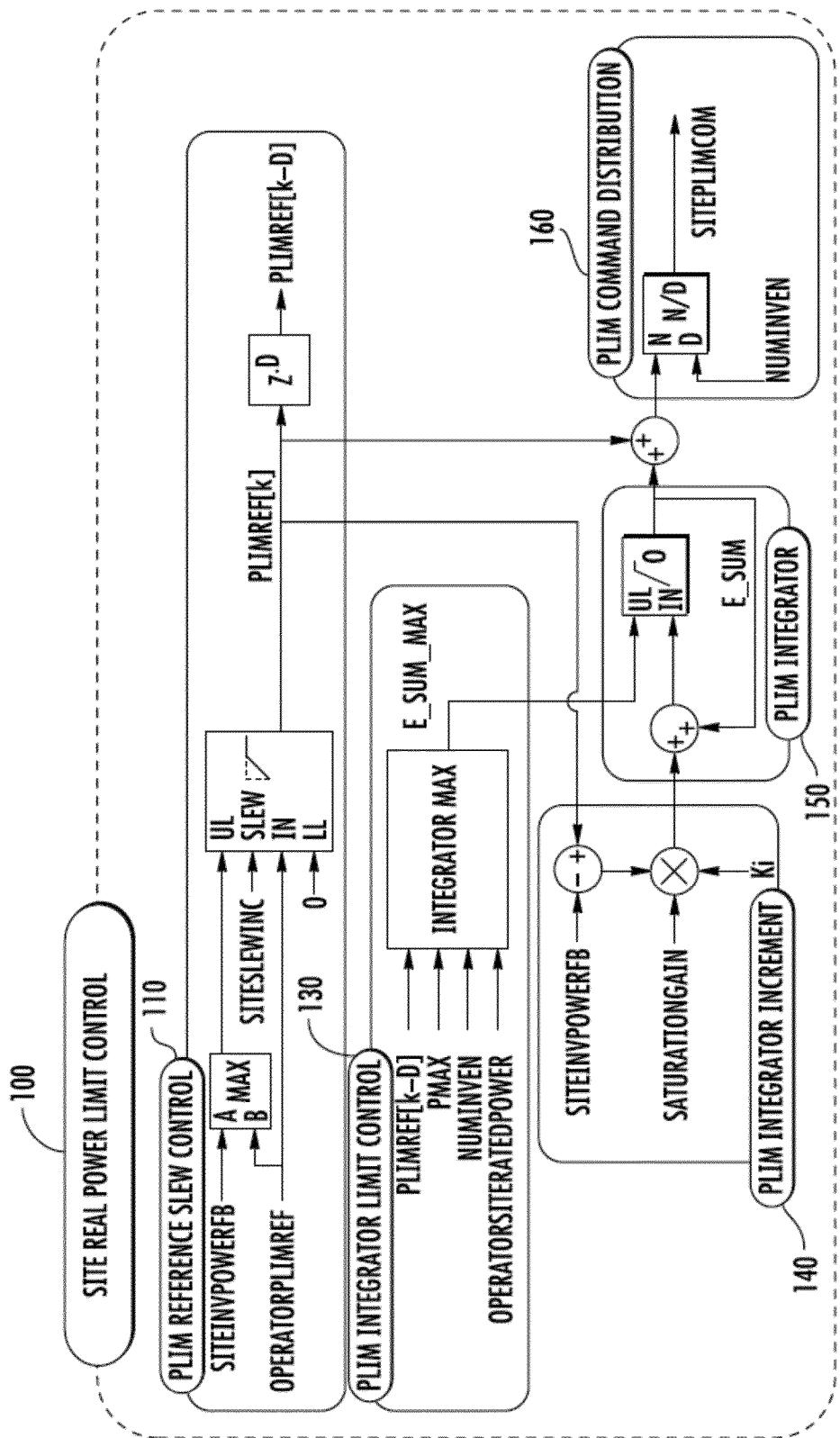


FIG. 3A

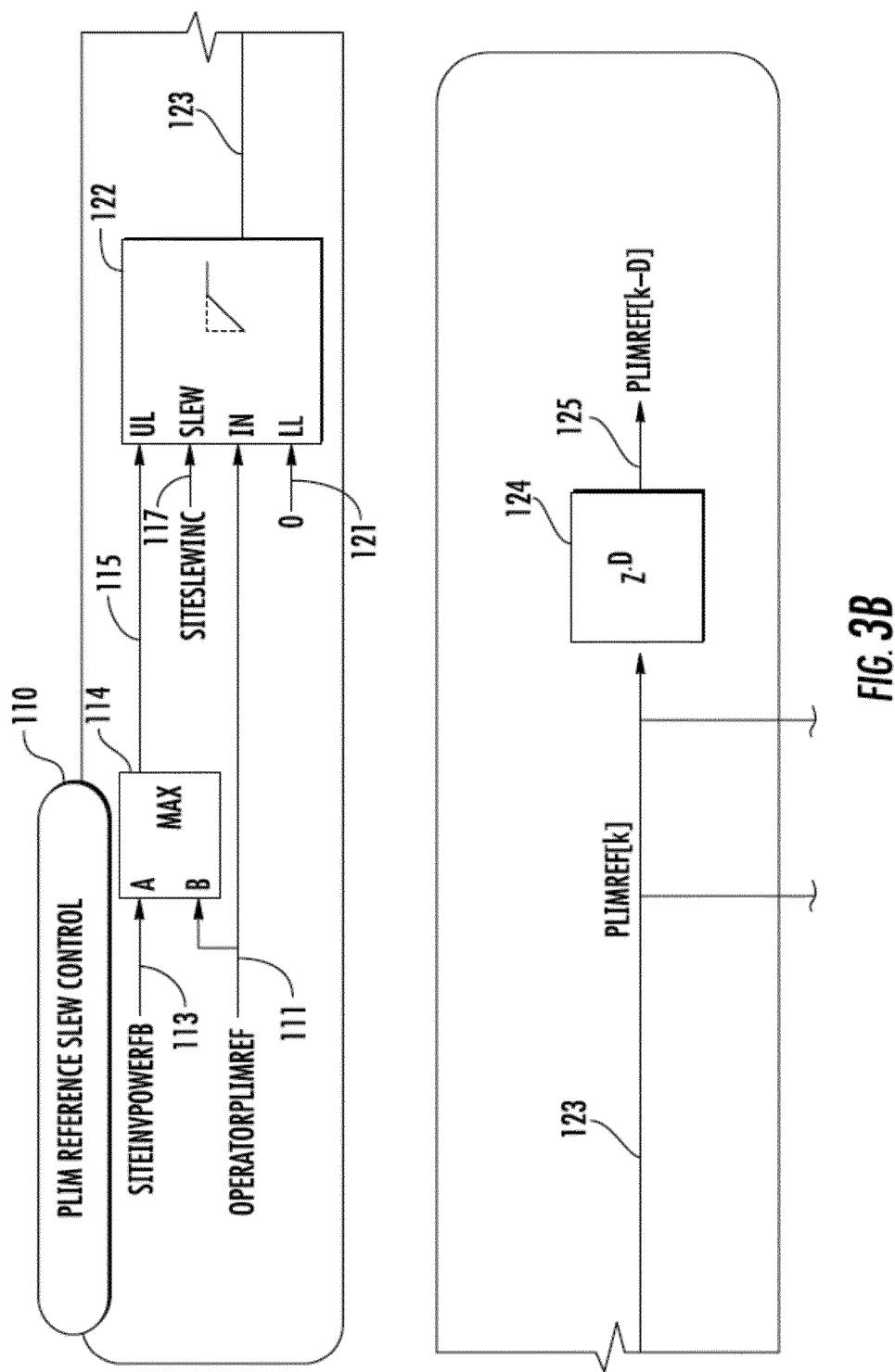


FIG. 3B

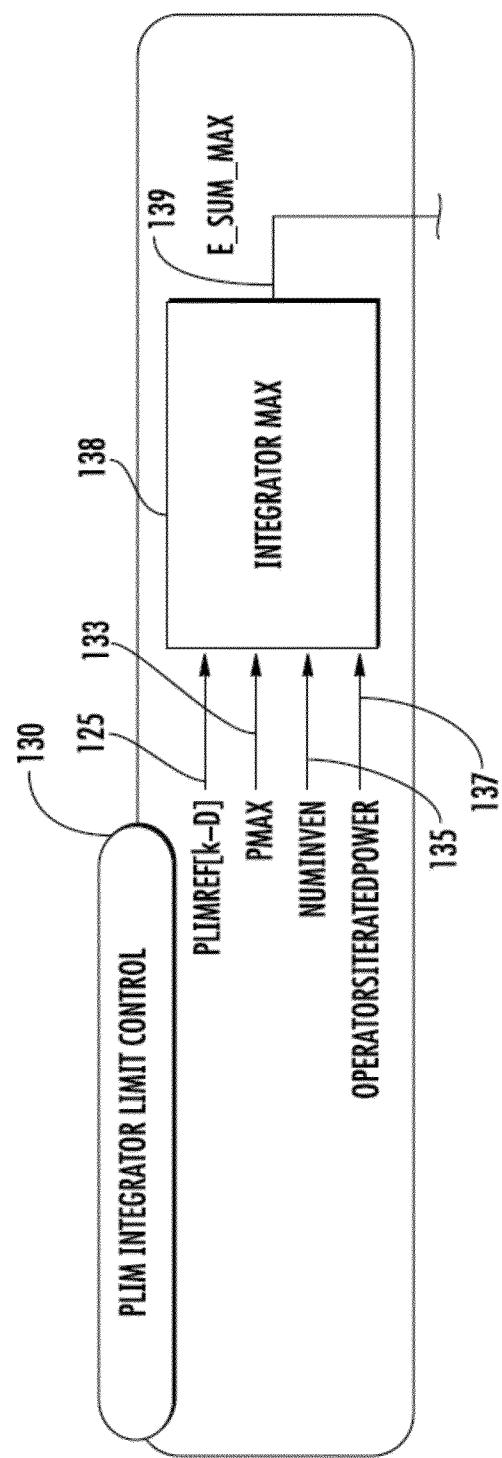


FIG. 3C

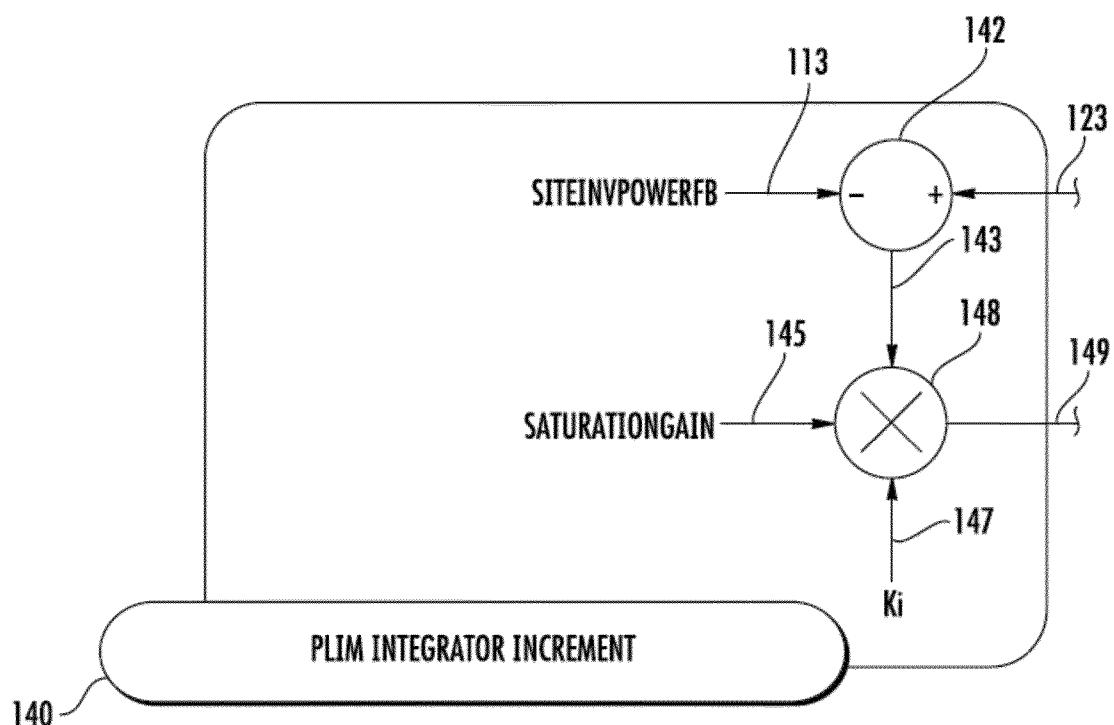


FIG. 3D

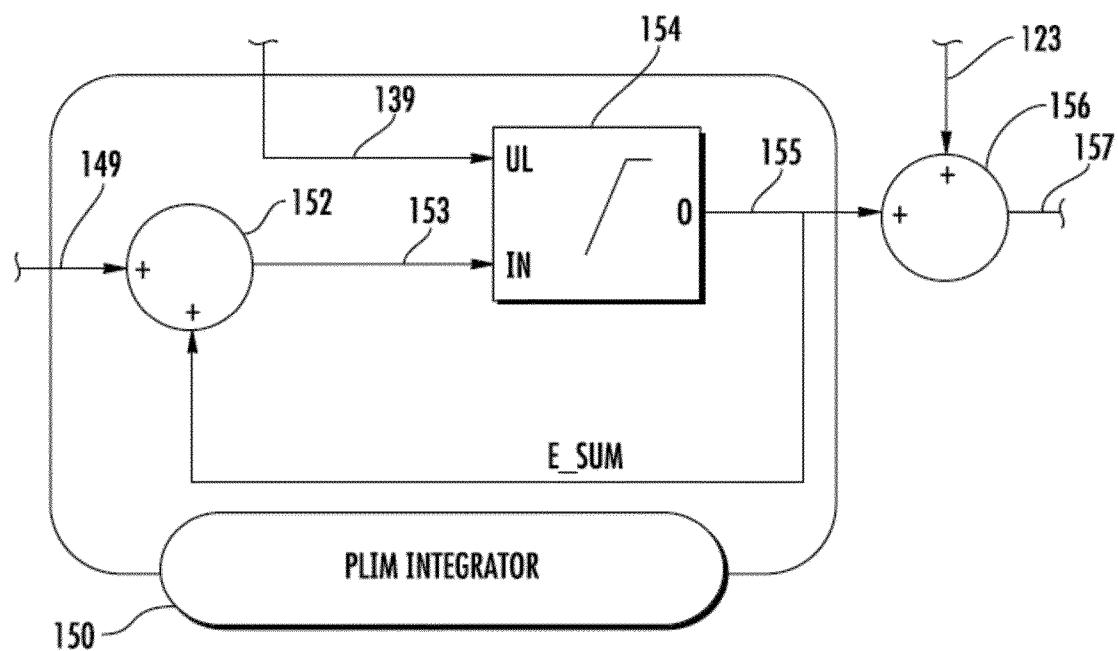


FIG. 3E

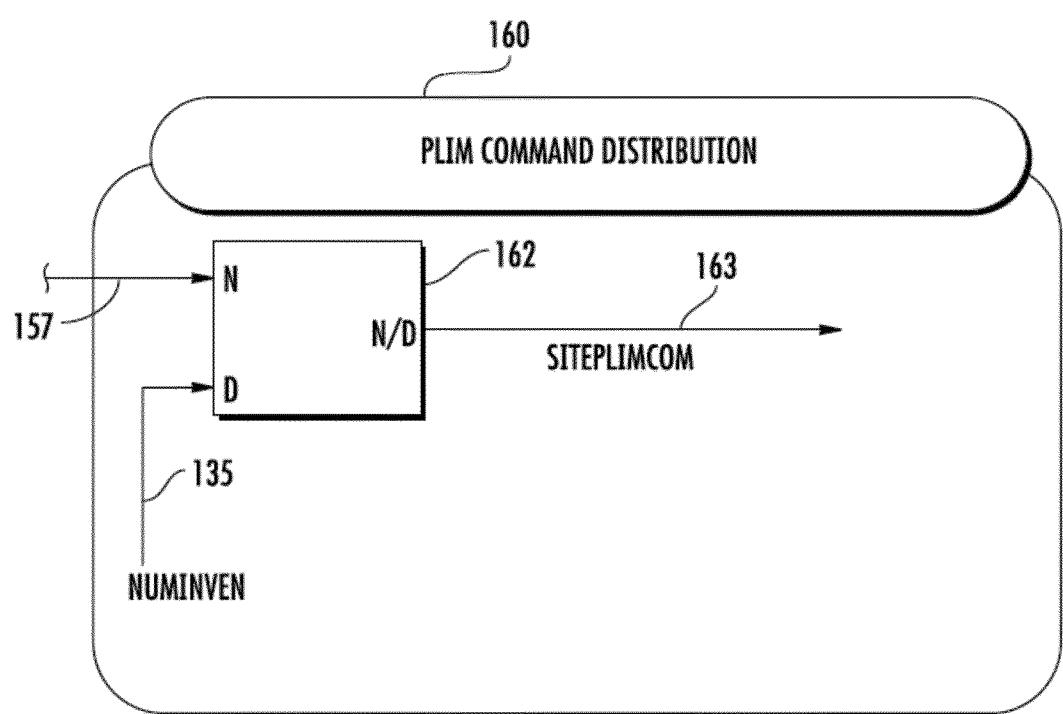


FIG. 3F

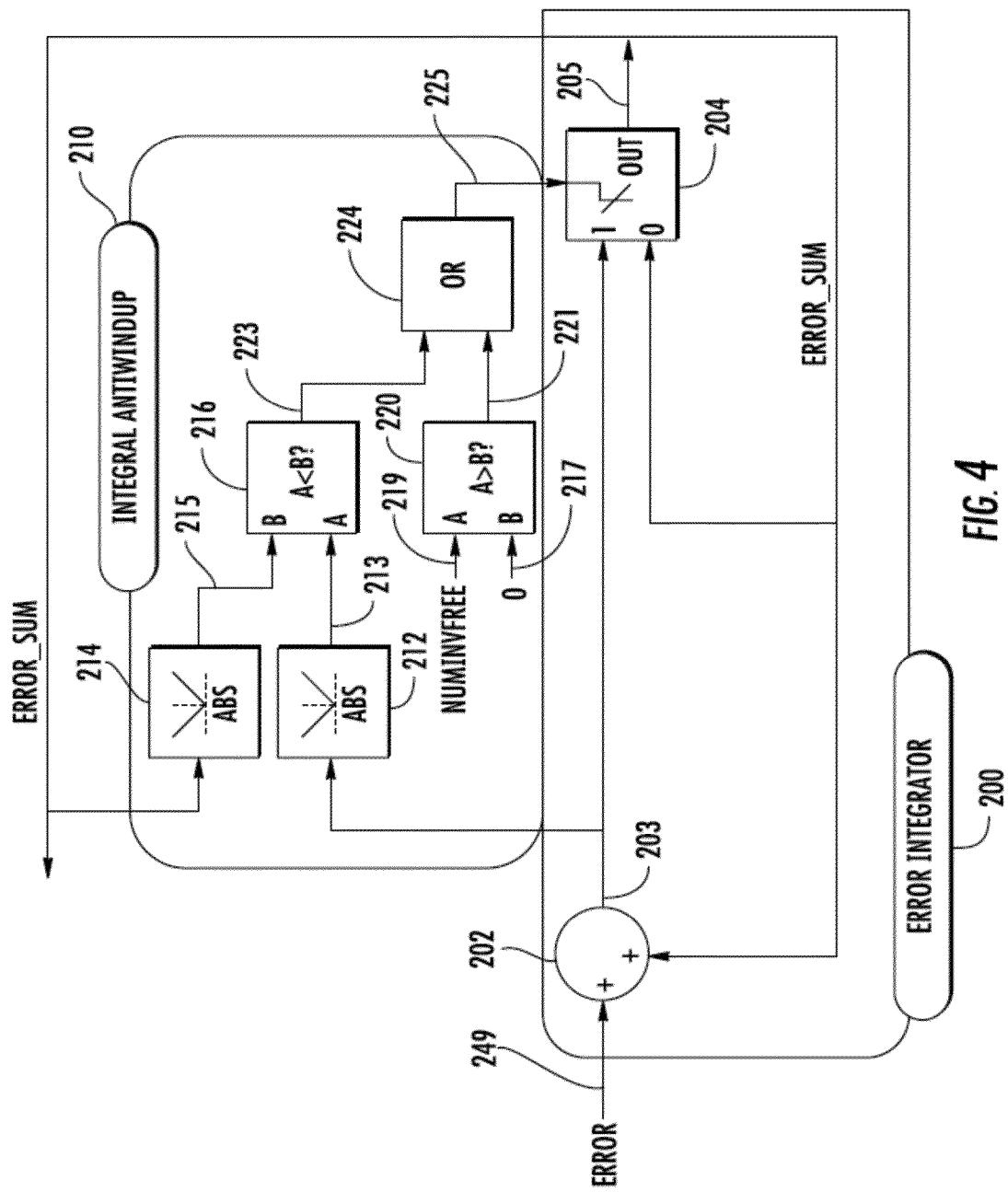


FIG. 4

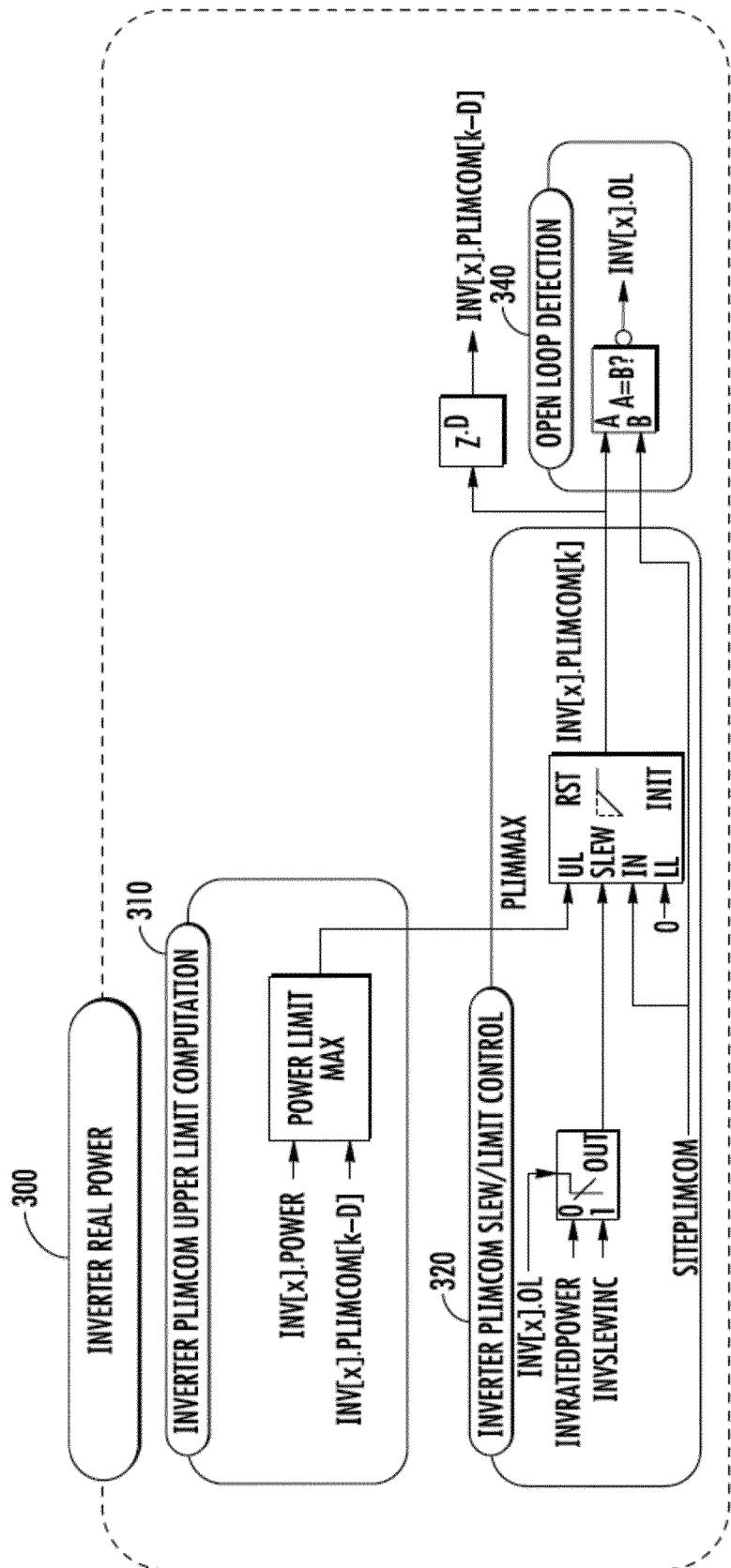


FIG. 5A

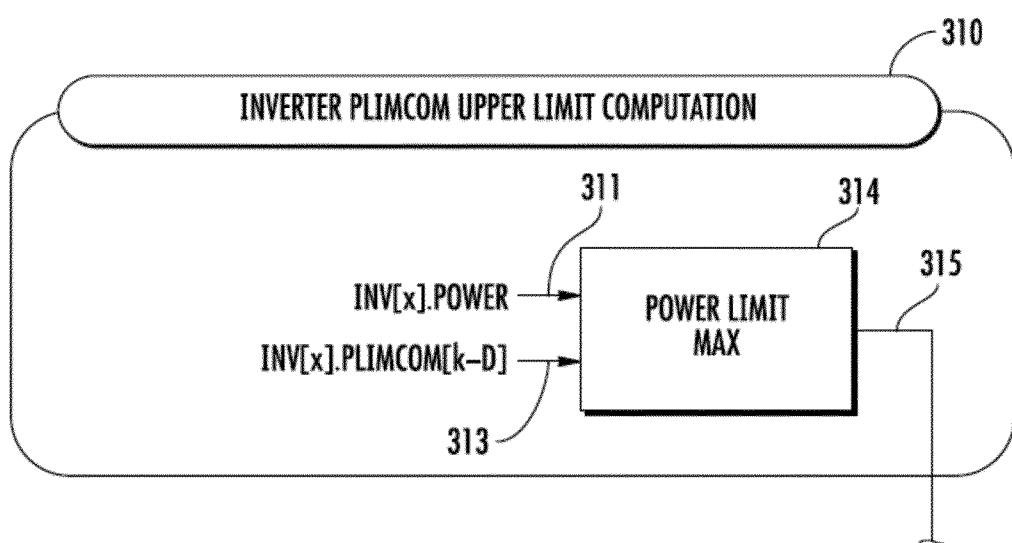


FIG. 5B

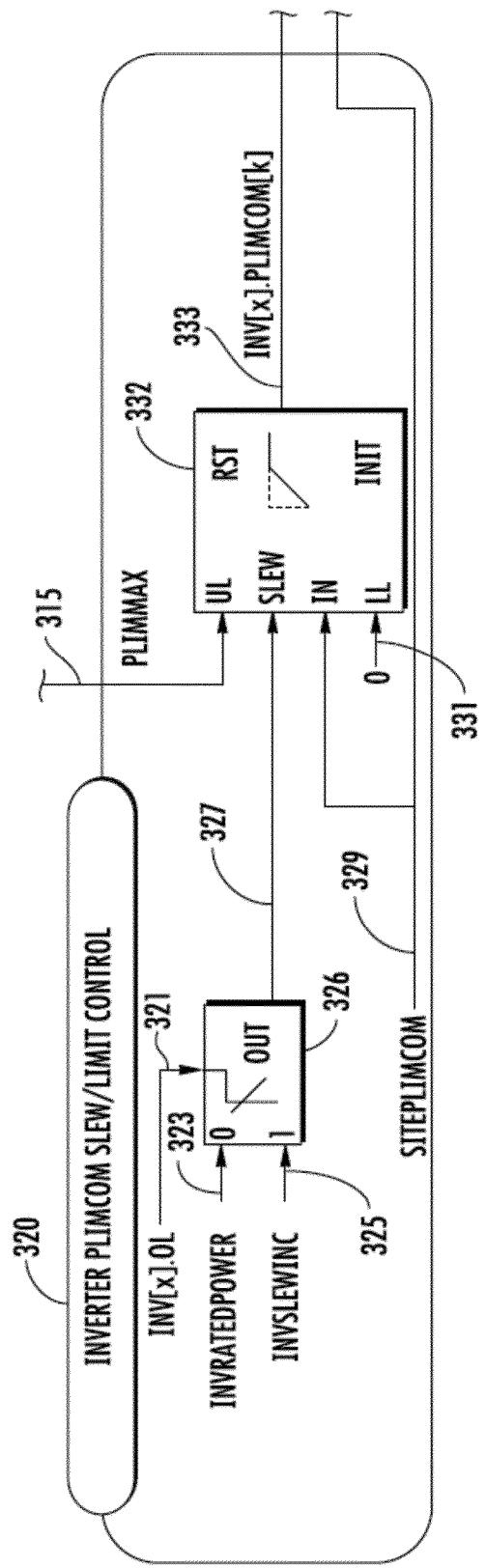


FIG. 5C

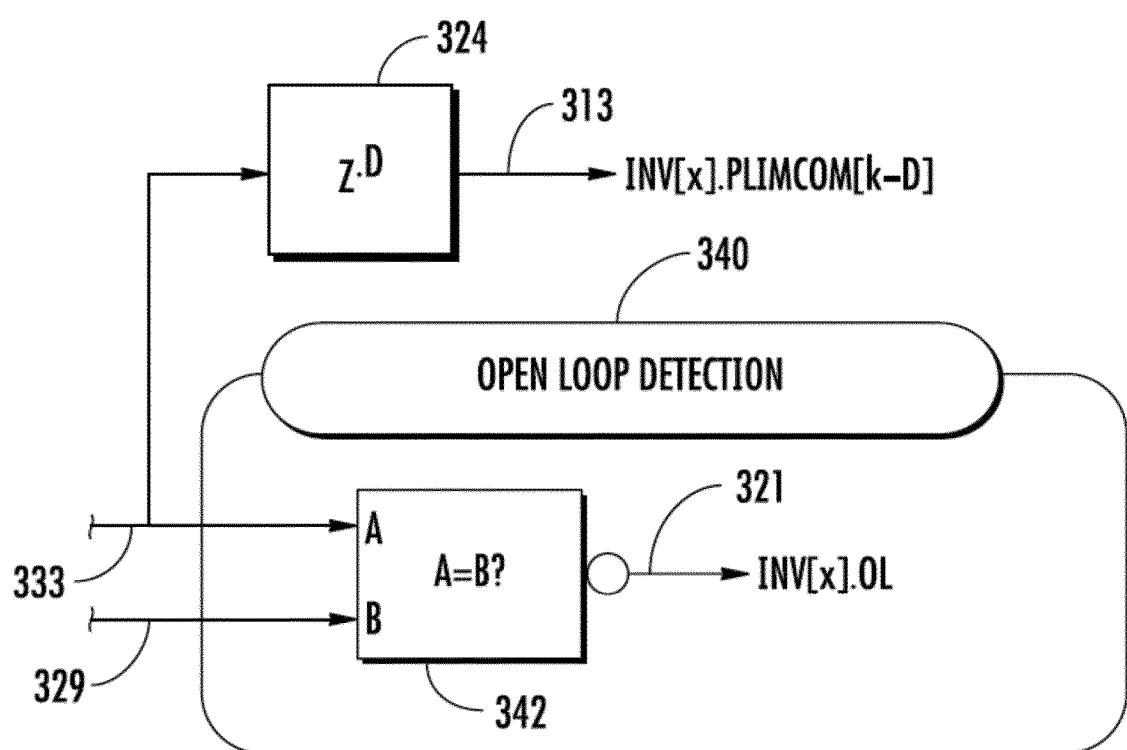


FIG. 5D

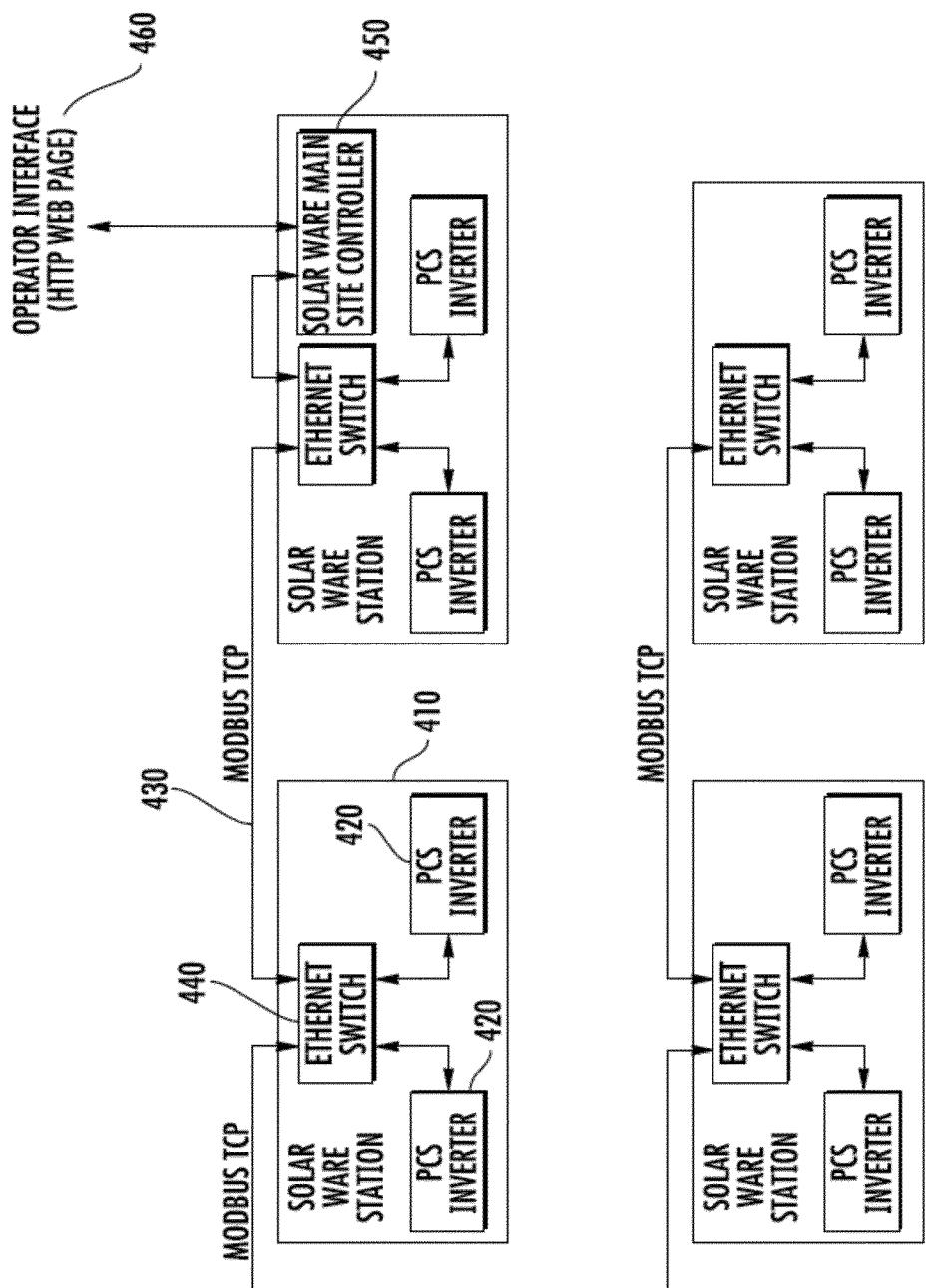


FIG. 6