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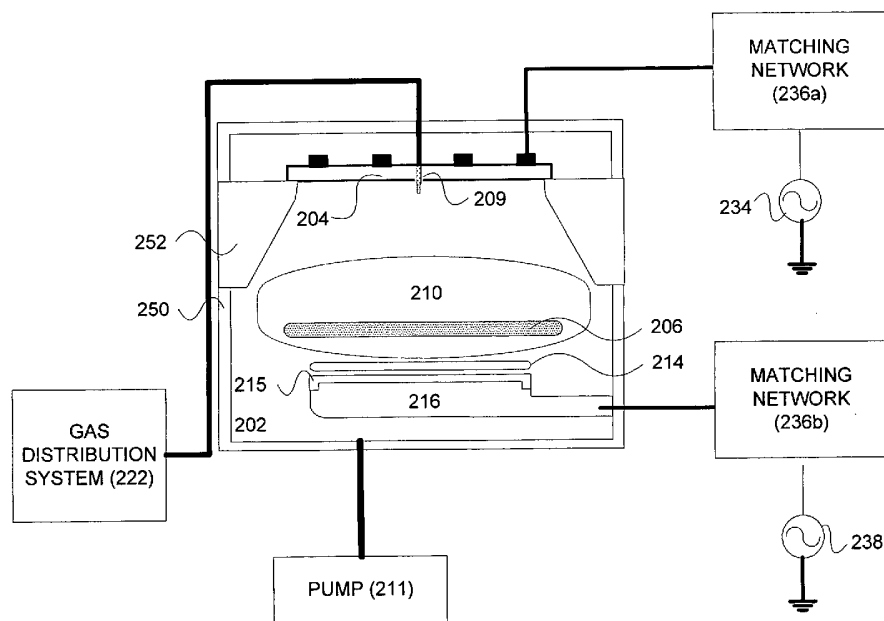
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(54) Title: METHODS AND ARRANGEMENT FOR THE REDUCTION OF BYPRODUCT DEPOSITION IN A PLASMA PROCESSING SYSTEM



(57) Abstract: In a plasma processing system, a method of reducing byproduct deposits on a set of plasma chamber surfaces of a plasma processing chamber is disclosed. The method includes providing a deposition barrier in the plasma processing chamber, the deposition barrier is configured to be disposed in a plasma generating region of the plasma processing chamber, thereby permitting at least some process byproducts produced when a plasma is struck within the plasma processing chamber to adhere to the deposition barrier and reducing the byproduct deposits on the set of plasma processing chamber surfaces.

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METHODS AND ARRANGEMENT FOR THE REDUCTION OF BYPRODUCT DEPOSITION IN A PLASMA PROCESSING SYSTEM

BACKGROUND OF THE INVENTION

[0001] The present invention relates in general to substrate manufacturing technologies and in particular to methods and arrangement for the reduction of byproduct deposition in a plasma processing system.

[0002] In the processing of a substrate, e.g., a semiconductor wafer or a glass panel such as one used in flat panel display manufacturing, plasma is often employed. As part of the processing of a substrate (chemical vapor deposition, plasma enhanced chemical vapor deposition, physical vapor deposition, etc.) for example, the substrate is divided into a plurality of dies, or rectangular areas, each of which will become an integrated circuit. The substrate is then processed in a series of steps in which materials are selectively removed (etching) and deposited (deposition) in order to form electrical components thereon.

[0003] Many plasma processes include some type of plasma bombardment. For example, pure ion etching, often called sputtering, is used to dislodge material from the substrate (e.g., oxide, etc.). Commonly an inert gas, such as Argon, is ionized in a plasma and subsequently accelerate toward a negatively charged substrate. Likewise, reactive ion etch (RIE), also called ion-enhanced etching, combines both chemical and ion processes in order to remove material from the substrate (e.g., photoresist, BARC, TiN, Oxide, etc.). Generally ions in the plasma enhance a chemical process by striking the surface of the substrate, and subsequently breaking the chemical bonds of the atoms on the surface in order to make them more susceptible to reacting with the molecules of the chemical process.

[0004] However, a plasma processing system may also produce pollutants. Generally comprised of organic and inorganic byproducts, pollutants are generated by the plasma process from materials in the etchant gases (e.g., carbon, fluorine, hydrogen, nitrogen, oxygen, argon, xenon, silicon, boron, chlorine, etc.), from materials in the substrate (e.g. photoresist, silicon, oxygen, nitrogen, aluminum, titanium, etc.), or from structural materials within the plasma processing chamber itself (e.g., aluminum, quartz, etc.).

[0005] Some pollutants are volatile, and may be substantially pumped away by a

vacuum system, while other pollutants form non-volatile or low-volatile sputtered species that tend to be deposited on interior surfaces and plasma chamber walls that tend to be difficult to efficiently evacuate from the plasma chamber. The resulting pollutant deposits may eventually flake and hence increase susceptibility of substrate defects, reduce the mean time between cleaning (MTBC), reduce yield, etc. For example, depending on the plasma process, conductive film deposits may form on plasma chamber interior surfaces which may impact the FW coupling of the plasma source and bias. In addition, byproduct deposits may contribute to plasma density drift.

[0006] Non-volatile and low-volatile byproducts include direct line-of-sight deposition of sputtered material, direct ion enhance etch byproduct deposition, volatile byproduct condensation, high sticking coefficient plasma dissociated byproducts, ion assisted deposition of plasma species, etc. Examples include high-k dielectrics (HfO_x, HfSixO_y, etc) byproducts, metal electrode (Pt, Ir, IrO_x, etc.) byproducts, memory material byproducts (PtMn, NiFe, CoFe, FeW, etc), interconnect byproducts (Cu, Ru, CoWP, Ta, etc.).

[0007] In general, the emission profile for the sputtered atoms is generally characterized by a cosine distribution. This means that the emission rate at some angle other than normal (perpendicular) is equal to the normal incidence emission rate times the cosine of the angle from the normal. This is usually drawn as a circle touching the impact point, in which the circle is the envelope of the magnitudes of the emission at other angles. Generally, since sputtered atoms tend to be neutral, it is not possible to redirect their trajectories in flight, and hence the sputtered atoms tend to travel in straight lines.

[0008] The degree of deposit adhesion to surfaces within the chamber, and hence the subsequent degree of potential contamination, is usually dependent on the specific plasma processing recipe (e.g., chemistry, power, and temperature) and the initial surface condition of chamber process kits. Since substantially removing deposits may be time consuming, a plasma processing system chamber is generally substantially cleaned only when particle contamination levels reach unacceptable levels, when the plasma processing system must be opened to replace a consumable structure (e.g., edge ring, etc.), or as part of scheduled preventive maintenance (PM).

[0009] Referring now to FIG. 1, a simplified diagram of an inductively coupled

plasma processing system, such as a Lam Research Transformer Coupled Plasma Processing System™, is shown. In a common configuration, the plasma chamber is comprised of a bottom piece 150 located in the lower chamber, and a detachable top piece 152 located in the upper chamber. Generally, an appropriate set of gases is flowed into chamber 102 from gas distribution system 122 and through dielectric coupling window 104. These plasma processing gases may be subsequently ionized at injector 108 to form a plasma 110 in a plasma generating region, in order to process (e.g., etch or deposition) exposed areas of substrate 114, such as a semiconductor substrate or a glass pane, positioned with edge ring 115 on an electrostatic chuck 116.

[0010] A first RF generator 134 generates the plasma as well as controls the plasma density, while a second RF generator 138 generates bias RF, commonly used to control the DC bias and the ion bombardment energy. Further coupled to source RF generator 134 is matching network 136a, and to bias RF generator 138 is matching network 136b, that attempt to match the impedances of the RF power sources to that of plasma 110. Furthermore, pump 111 is commonly used to evacuate the ambient atmosphere from plasma chamber 102 in order to achieve the required pressure to sustain plasma 110.

[0011] While these are severe issues to tackle requiring complicated high temperature chamber designs, special materials etc, there is no commonality to the behavior of these different materials. For example, if plasma process conditions allow it, a clean or a self-cleaning plasma recipe can be developed, or the chamber surfaces can be designed with materials that have a reduced sticking coefficient to the problem byproduct, or if the byproducts are sufficiently adhered or “stuck” to the chamber surfaces, the plasma process can be run until flaking becomes problematic. However, since these solutions are very process sensitive, the possibility of a single robust reactor design and process approach which can handle most of these materials and potential chemistries is problematic.

[0012] In view of the foregoing, there are desired methods and arrangement for the reduction of byproduct deposition in a plasma processing system.

SUMMARY OF THE INVENTION

[0013] The invention relates, in one embodiment, in a plasma processing system, a method of reducing byproduct deposits on a set of plasma chamber surfaces of a plasma processing chamber. The method includes providing a deposition barrier in the plasma processing chamber, the deposition barrier is configured to be disposed in a plasma generating region of the plasma processing chamber, thereby permitting at least some process byproducts produced when a plasma is struck within the plasma processing chamber to adhere to the deposition barrier and reducing the byproduct deposits on the set of plasma processing chamber surfaces.

[0014] The invention relates, in another embodiment, to a method of reducing a set of byproduct deposits on a set of plasma chamber surfaces in a plasma reactor. The method includes positioning a substrate in a plasma processing chamber. The method further includes positioning a deposition barrier in the plasma processing chamber, wherein a first plasma is configured to surround the deposition barrier when struck, and whereby the deposition barrier is configured to make contact with a first subset of the set of byproduct deposits from the substrate. The method also includes re-positioning the deposition barrier in the plasma processing chamber, wherein a second plasma is configured to surround the deposition barrier when struck, and whereby the deposition barrier is configured to make contact with a second subset of the set of byproduct deposits from the substrate.

[0015] The invention relates, in another embodiment, in a plasma processing system, to an arrangement for reducing byproduct deposits on a set of plasma chamber surfaces of a plasma processing chamber. The arrangement includes barrier means disposed in the plasma processing chamber; the barrier means being configured to be disposed in a plasma generating region of the plasma processing chamber, thereby permitting at least some process byproducts produced when a plasma is struck within the plasma processing chamber to adhere to the deposition barrier and reducing the byproduct deposits on the set of plasma processing chamber surfaces. The arrangement also includes attachment means for attaching the barrier means to one of a top, bottom, and side of an interior of the plasma processing chamber.

[0016] The invention relates, in another embodiment, to a deposition barrier arrangement configured to reduce byproduct deposits on a set of plasma chamber surfaces

of a plasma processing chamber. The arrangement includes a deposition barrier configured to be disposed in a plasma generating region of the plasma processing chamber, the deposition barrier being configured to enable at least some process byproducts produced when a plasma is struck within the plasma processing chamber to adhere to the deposition barrier and thereby reducing the byproduct deposits on the set of plasma chamber surfaces.

[0017] These and other features of the present invention will be described in more detail below in the detailed description of the invention and in conjunction with the following figures.

BRIEF DESCRIPTION OF THE DRAWINGS

[0018] The present invention is illustrated by way of example, and not by way of limitation, in the figures of the accompanying drawings and in which like reference numerals refer to similar elements and in which:

[0019] FIG. 1 shows a simplified diagram of a inductively coupled plasma processing system, such as a Lam Transformer Coupled Plasma Processing System;

[0020] FIG. 2 shows a simplified diagram of a inductively coupled plasma processing system with a deposition barrier, according to one embodiment of the invention;

[0021] FIG. 3 shows a simplified diagram of a inductively coupled plasma processing system in which a deposition barrier is supported with a structure that is attached to the bottom of the plasma chamber, according to one embodiment of the invention;

[0022] FIG. 4 shows a simplified diagram of a inductively coupled plasma processing system in which a deposition barrier is supported with a structure that is attached to the top of the plasma chamber, according to one embodiment of the invention;

[0023] FIG. 5 shows a simplified diagram of a inductively coupled plasma processing system in which a deposition barrier is supported with a structure that is attached to a side of the plasma chamber, according to one embodiment of the invention;

[0024] FIG. 6 shows a simplified diagram of a inductively coupled plasma processing system in which a deposition barrier is supported with a structure that is attached to a chuck, according to one embodiment of the invention; and,

[0025] FIG. 7 shows a simplified diagram of a method for the reduction of low

volatility line-of-sight byproducts in a plasma processing system, according to one embodiment of the invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0026] The present invention will now be described in detail with reference to a few preferred embodiments thereof as illustrated in the accompanying drawings. In the following description, numerous specific details are set forth in order to provide a thorough understanding of the present invention. It will be apparent, however, to one skilled in the art, that the present invention may be practiced without some or all of these specific details. In other instances, well known process steps and/or structures have not been described in detail in order to not unnecessarily obscure the present invention.

[0027] While not wishing to be bound by theory, it is believed by the inventor herein that sputtering deposition can be reduced on a set of plasma chamber surfaces through the use of a deposition barrier that is substantially surrounded by a plasma. That is, a deposition barrier may be positioned such that if a particle is sputtered from the substrate toward a plasma chamber surface, the particle will first strike the deposition barrier.

[0028] In a non-obvious fashion, sputtered atoms that would normally collide with other surfaces in the chamber, as well as the pumping manifold including the turbo pump, can be intercepted with a deposition barrier that is substantially easy to remove. For example, the deposition barrier may be automatically transferred in and out of the plasma chamber to an ex-situ cleaning process. In one embodiment, the deposition barrier can also protect the electrostatic chuck during wafer-less auto clean WACTM (or wafer less chamber conditioning), in which plasma chamber components are exposed to the plasma as part of the cleaning or conditioning/seasoning process.

[0029] Referring now to FIG.2, a simplified diagram of an inductive coupled plasma processing system is shown, according to one embodiment of the invention. In a common configuration, the plasma chamber is comprised of a bottom piece 250 located in the lower chamber, and a detachable top piece 252 located in the upper chamber. Generally, an appropriate set of gases is flowed into chamber 202 from gas distribution system 222 and through dielectric coupling window 204. These plasma processing gases may be subsequently ionized at injector 209 to form a plasma 210, in order to process (e.g., etch or

deposition) exposed areas of substrate 214, such as a semiconductor substrate or a glass pane, positioned with edge ring 215 on an electrostatic chuck 216.

[0030] A first RF generator 234 generates the plasma as well as controls the plasma density, while a second RF generator 238 generates bias RF, commonly used to control the DC bias and the ion bombardment energy. Further coupled to source RF generator 234 is matching network 236a, and to bias RF generator 238 is matching network 236b, that attempt to match the impedances of the RF power sources to that of plasma 210. Furthermore, pump 211 is commonly used to evacuate the ambient atmosphere from plasma chamber 202 in order to achieve the required pressure to sustain plasma 220.

[0031] In addition, a deposition barrier 206 is positioned at a height above the bottom surface of said plasma reactor, such that if a particle is sputtered from the substrate toward a plasma chamber wall, the particle will first strike the deposition barrier.

[0032] Referring now to FIG.3, the simplified diagram of a inductive coupled plasma processing system of FIG. 2 is shown, in which a deposition barrier is supported with a structure 308 that is attached to the bottom of the plasma chamber (lower interior surface), according to one embodiment of the invention.

[0033] Referring now to FIG.4, the simplified diagram of a inductive coupled plasma processing system of FIG. 2 is shown, in which a deposition barrier is supported with a structure 408 that is attached to the top of the plasma chamber (upper interior surface), according to one embodiment of the invention.

[0034] Referring now to FIG.5 the simplified diagram of a inductive coupled plasma processing system of FIG. 2 is shown, in which a deposition barrier is supported with a structure 508 that is attached to a side of the plasma chamber (side interior surface), according to one embodiment of the invention.

[0035] Referring now to FIG.6 the simplified diagram of an inductive coupled plasma processing system of FIG. 2 is shown, in which a deposition barrier is supported with a structure 608 that is attached to chuck 216, according to one embodiment of the invention.

[0036] In one embodiment, the height of the bottom surface of the deposition barrier can be repositioned relative to the bottom surface of said plasma reactor, in order to better

optimize plasma processing conditions. In another embodiment, the deposition barrier is substantially transparent to a generated RF. In yet another embodiment, the deposition barrier comprises a material that is substantially resistant to plasma attack (i.e., quartz, Y₂O₃, yttrium, CeO₂, cerium, ZrO₂, zirconium, Teflon, Vespel, substantially pure plastic, ceramic, SiC, BN, BC, SiN, SiO, etc.). In yet another embodiment, the deposition barrier comprises a material that generates a set volatile etch products when exposed to said plasma.

[0037] In another embodiment, the deposition barrier is heated independently of the plasma. In yet another embodiment, a RF bias is applied to the deposition barrier. In yet another embodiment, deposition barrier can be removed in-situ, for example by a robotic arm. In yet another embodiment, deposition barrier can be replaced in-situ, for example by a robotic arm. In another embodiment, the deposition barrier may be removed from the plasma processing system by a vacuum robot under automatic control.

[0038] In yet another embodiment, deposition barrier includes a substantially continuous surface. In yet another embodiment, deposition barrier includes a set of holes. In yet another embodiment, deposition barrier can be removed prior to, simultaneously with, or after the removal of said substrate from said plasma chamber. In yet another embodiment, deposition barrier is a Faraday barrier.

[0039] In another embodiment, the deposition barrier can be cleaned and reused. In yet another embodiment, plasma processing system includes a source RF that is coupled from the top, side, or bottom of the plasma chamber. In yet another embodiment, a set of plasma chamber walls can be heated and/or cooled independently of the plasma.

[0040] In another embodiment, the deposition barrier may be heated to encourage adhesion of thicker films and to prevent incorporation of volatile species which may lead to premature flaking. In yet another embodiment, the deposition barrier may be cooled to increase the sticking probability of substantially volatile deposition by products and to enable thicker films before flaking. In yet another embodiment, the deposition barrier may be cleaned in-situ by a plasma cleaning process when the deposition barrier has cooled down from being hot. In yet another embodiment, the deposition barrier is cleaned in-situ by a plasma cleaning process when the deposition barrier has heated up from being cool.

[0041] In another embodiment, the deposition barrier is comprised of a metal that

does not substantially generate a set of volatile etch products when exposed to the plasma (e.g. Ni, Pt, Ir, anodized Al, Cu, etc.).

[0042] In another embodiment, the deposition barrier may be cleaned in-situ by a plasma cleaning process. In yet another embodiment, the deposition barrier may be cleaned in-situ by a wet chemical flush process. In yet another embodiment, the deposition barrier comprises a material that is substantially resistant to a wet clean process. In yet another embodiment, the deposition barrier is coated by a material that is substantially resistant to a wet clean process. In another embodiment, the deposition barrier may be lowered onto the chuck, in order to protect the chuck, while running an in-situ plasma chamber clean. In another embodiment, the deposition barrier may be lowered onto the chuck, in order to protect the chuck, while the chamber is idle between substrate processing cycles.

[0043] In another embodiment the deposition barrier may protect a source RF. In yet another embodiment, the source RF comprises an inductive source. In yet another embodiment, the source RF comprises a capacitive source. In yet another embodiment the source RF comprises an ECR (electron-cyclotron resonance) source. In yet another embodiment, the source RF comprises a microwave source. In yet another embodiment, the source RF may be coupled from the top of the plasma chamber. In yet another embodiment, the source RF may be coupled from the side of the plasma chamber. In yet another embodiment, the source RF may be coupled from the bottom of the plasma chamber.

[0044] In another embodiment, the deposition barrier may protect a set of plasma gas injectors from erosion and deposition clogging. In another embodiment, the deposition barrier may protect a set of in-situ metrology sensors (such as optical emission, interferometry, etc.) or the transparent windows covering them from erosion and deposition clogging. In yet another embodiment, the deposition barrier surface has a pre-determined roughness which facilitates control of adhesion of the depositing material. In another embodiment, the deposition barrier surface has a pre-determined surface composition which facilitates control of adhesion of the depositing material.

[0045] Referring now to FIG. 7, a simplified method for the reduction of low volatility products in a plasma processing system is shown, according to one embodiment of the invention. Initially, a substrate is positioned in a plasma processing chamber at step 702. Next a deposition barrier is positioned in the plasma processing chamber, wherein a

plasma can be struck that substantially surrounds the deposition barrier, at step 704. A plasma is then struck within the plasma processing chamber, at step 706. If a particle is sputtered from the substrate toward a plasma chamber surface from the set of plasma chamber surfaces, the particle will strike said deposition barrier, as step 708.

[0046] The invention has been described in terms of several preferred embodiments, there are alterations, permutations, and equivalents which fall within the scope of this invention. For example, although the present invention has been described in connection with a Lam Research Transformer Coupled Plasma Processing System™, other plasma processing systems may be used (e.g., etching, deposition, ion sputter, electron beam, cluster ion beam. etc.) It should also be noted that there are many alternative ways of implementing the methods of the present invention.

[0047] Advantages of the invention include methods and arrangement for the reduction of byproduct deposition in a plasma processing system. Additional advantages include substantially improving productivity and device yield, the use of a common plasma chamber design across multiple plasma processing applications (i.e, FeRAM, MRAM, Cu, MEMS, metal gate high-k gate, etc.), process repeatability, low CoC, low COO, high MTBC, low MTTCR, and the extended lifetime of plasma chamber parts.

[0048] Having disclosed exemplary embodiments and the best mode, modifications and variations may be made to the disclosed embodiments while remaining within the subject and spirit of the invention as defined by the following claims.

CLAIMS

What is claimed is:

1. In a plasma processing system, a method of reducing byproduct deposits on a set of plasma chamber surfaces of a plasma processing chamber, comprising:

providing a deposition barrier in said plasma processing chamber, said deposition barrier is configured to be disposed in a plasma generating region of said plasma processing chamber, thereby permitting at least some process byproducts produced when a plasma is struck within said plasma processing chamber to adhere to said deposition barrier and reducing said byproduct deposits on said set of plasma processing chamber surfaces.

2. The method of claim 1, wherein said deposition barrier is substantially transparent to a generated RF.

3. The method of claim 2 wherein said deposition barrier is a Faraday shield.

4. The method of claim 2, wherein said deposition barrier comprises a material that is substantially resistant to plasma attack.

5. The method of claim 2, wherein said deposition barrier is coated by a material that is substantially resistant to plasma attack.

6. The method of claim 1, wherein said deposition barrier comprises at least one of quartz, Y_2O_3 , yttrium, CeO_2 , cerium, ZrO_2 , zirconium, Teflon, Vespel, BN, BC, SiN, SiO, SiC, substantially pure plastic, and ceramic.

7. The method of claim 1, wherein said deposition barrier comprises a material that generates volatile etch byproducts when exposed to said plasma.

8. The method of claim 1, further comprising independently heating said deposition barrier.
9. The method of claim 1, further comprising applying a RF bias to said deposition barrier.
10. The method of claim 1 wherein said deposition barrier comprises a metal that does not generate a substantial quantity of volatile etch byproducts when exposed to said plasma.
11. The method of claim 10 wherein said metal includes at least one of Ni, Pt, Ir, anodized Al, and Cu.
12. The method of claim 1 further comprising performing in-situ cleaning of said deposition using a plasma cleaning process.
13. The method of claim 1 further comprising performing ex-situ cleaning of said deposition barrier using a wet chemical flush process.
14. The method of claim 13, wherein said deposition barrier comprises a material that is substantially resistant to a wet clean process.
15. The method of claim 13, wherein said deposition barrier is coated by a material that is substantially resistant to a wet clean process.
16. The method of claim 1, wherein said deposition barrier can be removed while substantially maintaining a vacuum in the plasma chamber.

17. The method of claim 16, wherein said removal can be accomplished by a vacuum robot under automatic control.
18. The method of claim 1, wherein said deposition barrier can be replaced in-situ.
19. The method of claim 1, wherein said deposition barrier includes a substantially continuous surface.
20. The method of claim 1, wherein said deposition barrier includes a set of holes.
21. The method of claim 1, wherein said deposition barrier is configured to be removed prior to removing said substrate from said plasma processing chamber.
22. The method of claim 1, wherein said deposition barrier is configured to be removed after removing said substrate from said plasma processing chamber.
23. The method of claim 1, wherein said deposition barrier is configured to be removed simultaneously with removing said substrate from said plasma processing chamber.
24. The method of claim 1, wherein said deposition barrier is reusable after cleaning.
25. The method of claim 1, wherein said deposition barrier protects a source RF.
26. The method of claim 25 wherein said source RF comprises an inductive source.

27. The method of claim 26 wherein said source RF is configured to protect a dielectric coupling window for said inductive source.
28. The method of claim 25 wherein said source RF comprises a capacitive source.
29. The method of claim 25 wherein said source RF comprises an inductive source and a capacitive source.
30. The method of claim 25 wherein said source RF comprises ECR source.
31. The method of claim 25 wherein said source RF comprises a microwave source.
32. The method of claim 1, wherein said plasma processing system includes a source RF that is coupled from a top of said plasma processing chamber
33. The method of claim 1, wherein said plasma processing system includes a source RF that is coupled from a side of said plasma processing chamber.
34. The method of claim 1, wherein said plasma processing system includes a source RF that is coupled from a bottom of said plasma processing chamber.
35. The method of claim 1, wherein said set of plasma chamber surfaces is configured to be independently heated.
36. The method of claim 1, wherein said set of plasma chamber surfaces is configured to be independently cooled.

37. The method of claim 1 further comprising independently heating said deposition barrier.
38. The method of claim 1 further comprising independently cooling said deposition barrier.
39. The method of claim 1 further comprising:
halting processing of substrates in said plasma processing chamber at least until said deposition barrier is cooled down to a predefined temperature threshold; and
cleaning said deposition barrier in-situ after said deposition barrier is cooled at least down to said predefined temperature threshold.
40. The method of claim 1 wherein said deposition barrier is cleaned in-situ by a plasma cleaning process when said deposition barrier has heated up.
41. The method of 1 wherein said deposition barrier is configured to protect a set of gas injectors from said byproduct deposits
42. The method of claim 1, wherein said deposition barrier surface has a pre-determined roughness which facilitates control of adhesion of the depositing material.
43. The method of claim 1, wherein said deposition barrier surface has a pre-determined surface composition which facilitates control of adhesion of the depositing material.
44. A method of reducing a set of byproduct deposits on a set of plasma chamber surfaces in a plasma reactor, comprising:
positioning a substrate in a plasma processing chamber;

positioning a deposition barrier in said plasma processing chamber, wherein a first plasma is configured to surround said deposition barrier when struck, and whereby said deposition barrier is configured to make contact with a first subset of said set of byproduct deposits from said substrate;

re-positioning said deposition barrier in said plasma processing chamber, wherein a second plasma is configured to surround said deposition barrier when struck, and whereby said deposition barrier is configured to make contact with a second subset of said set of byproduct deposits from said substrate.

45. A deposition barrier arrangement configured to reduce byproduct deposits on a set of plasma chamber surfaces of a plasma processing chamber, comprising:

a deposition barrier configured to be disposed in a plasma generating region of said plasma processing chamber, said deposition barrier being configured to enable at least some process byproducts produced when a plasma is struck within said plasma processing chamber to adhere to said deposition barrier and thereby reducing said byproduct deposits on said set of plasma chamber surfaces.

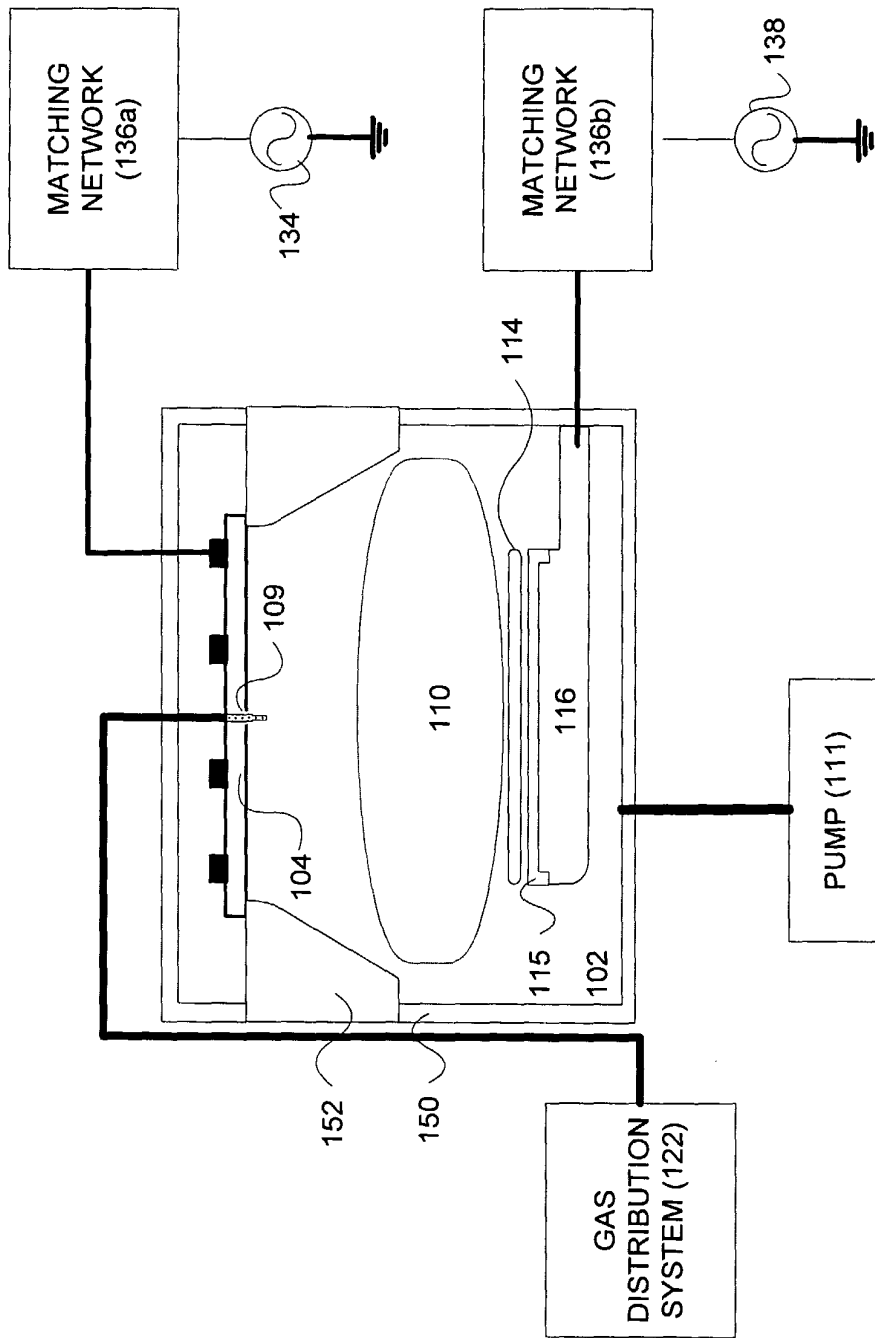


FIG. 1

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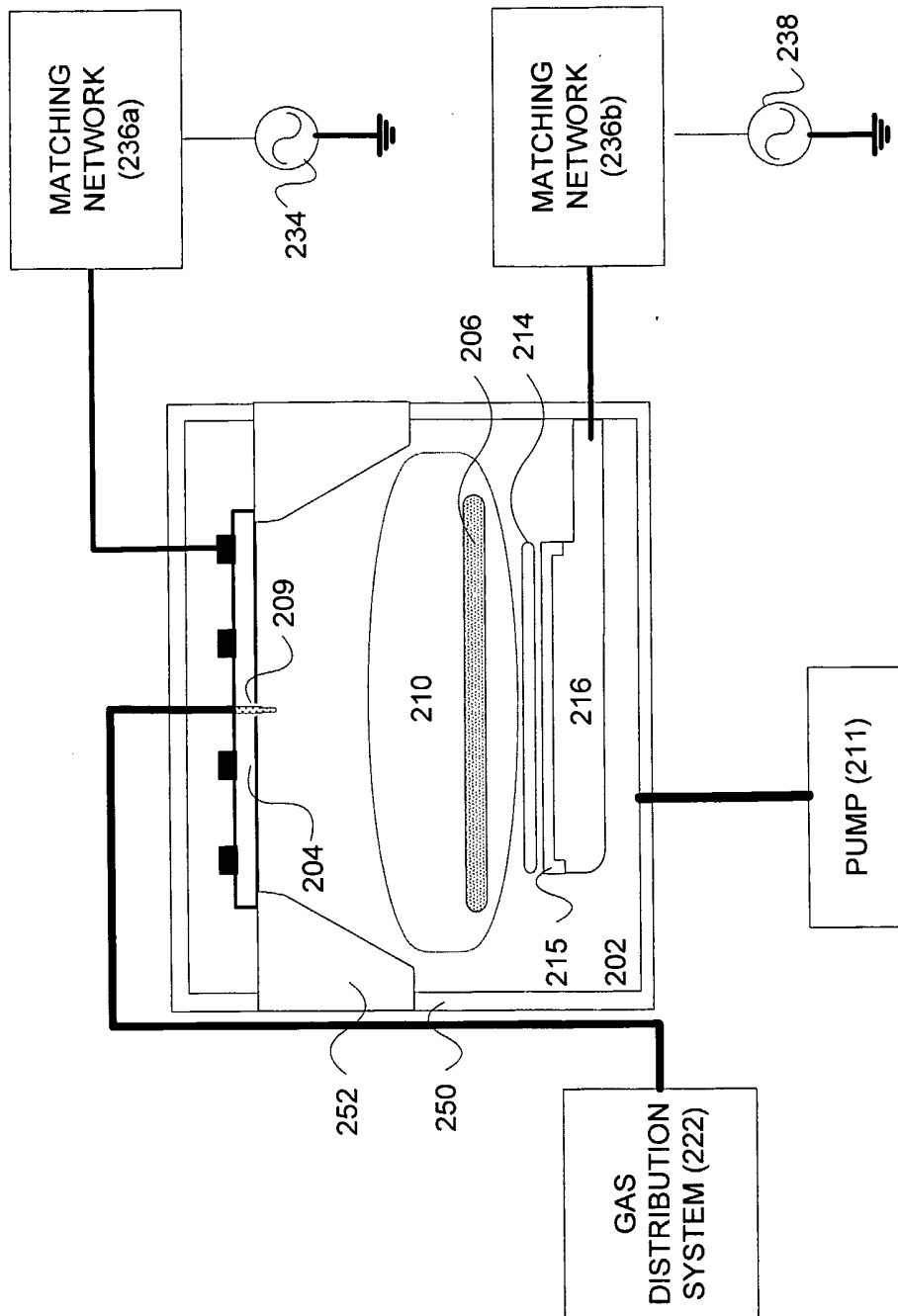


FIG. 2

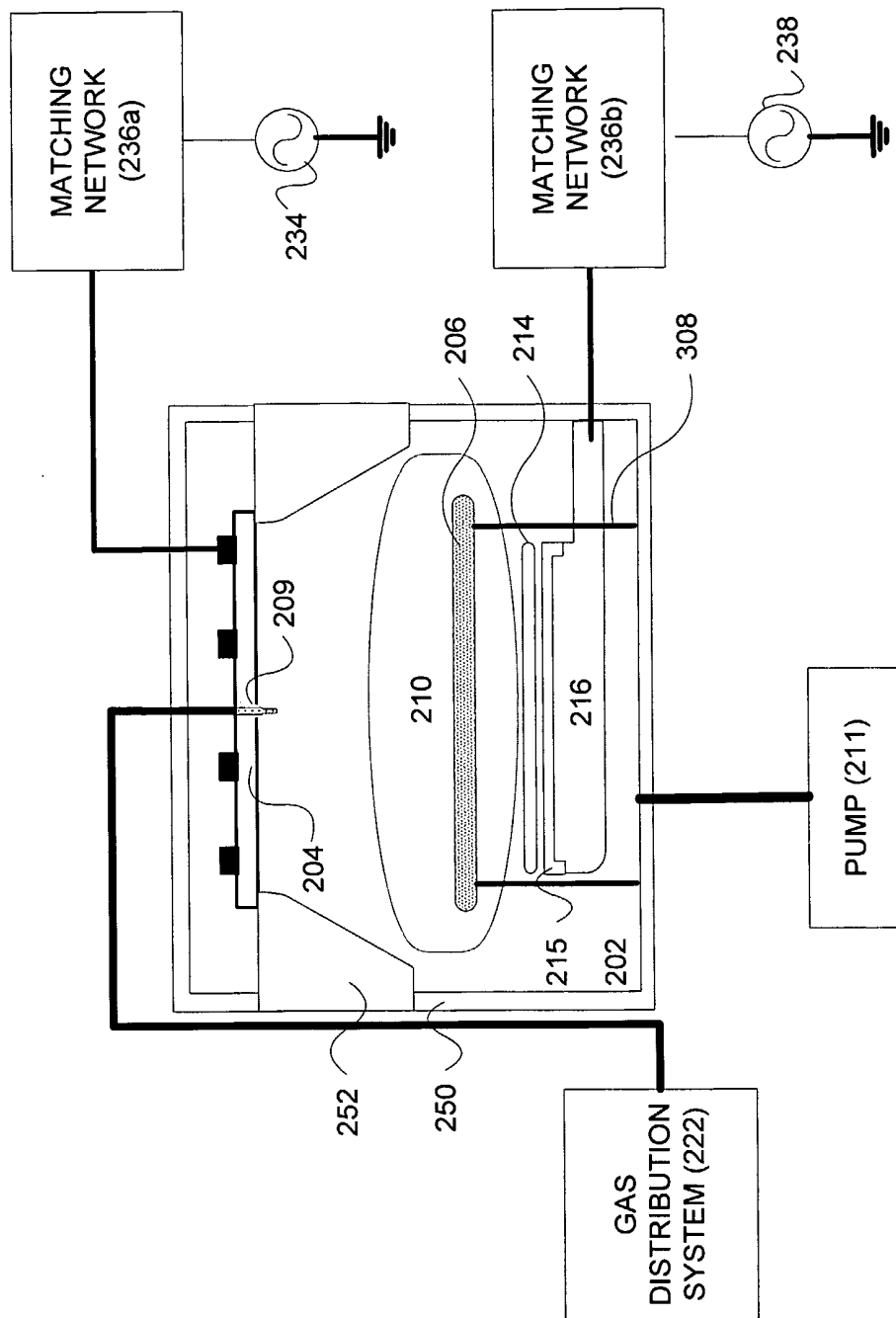


FIG. 3

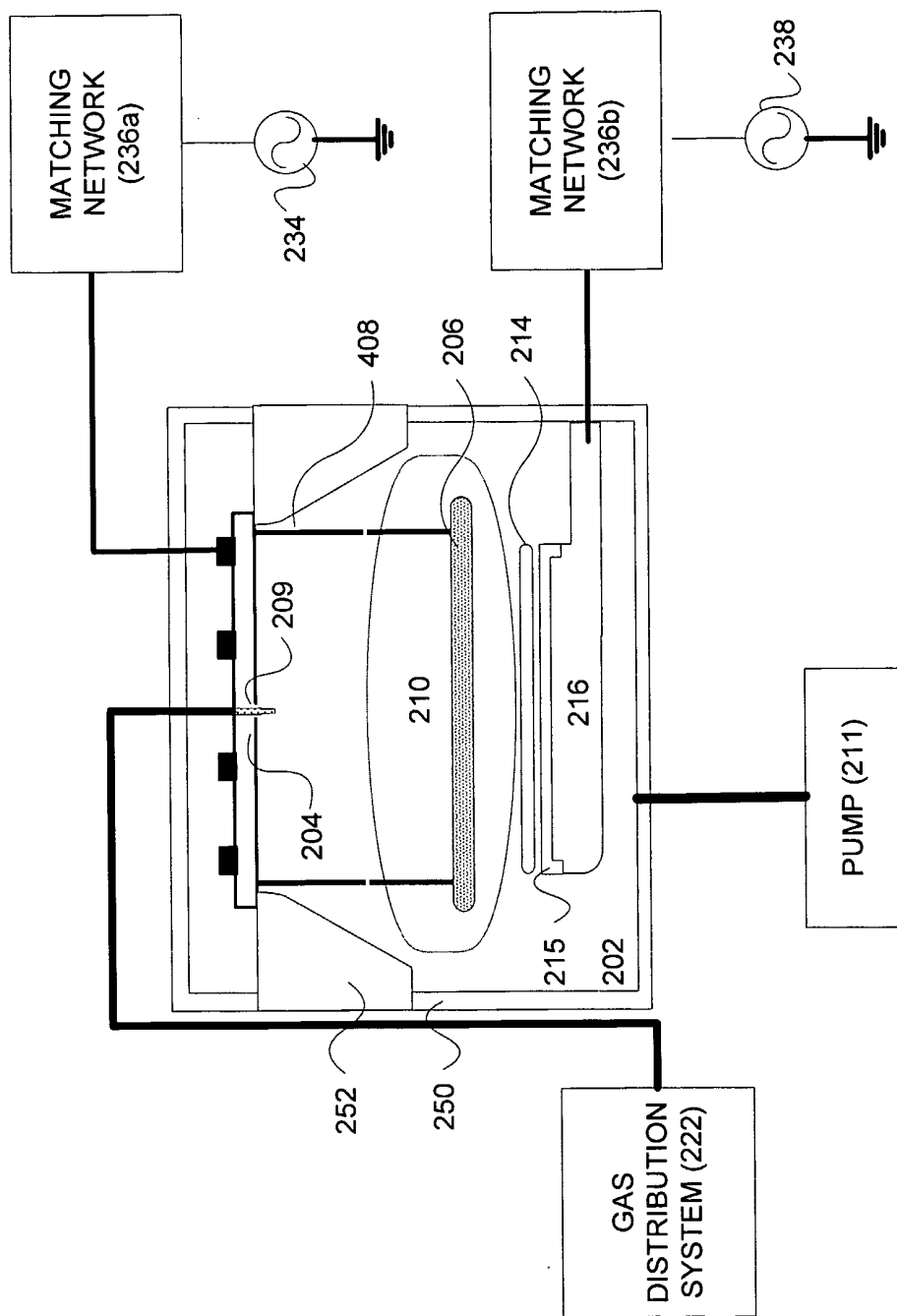


FIG. 4

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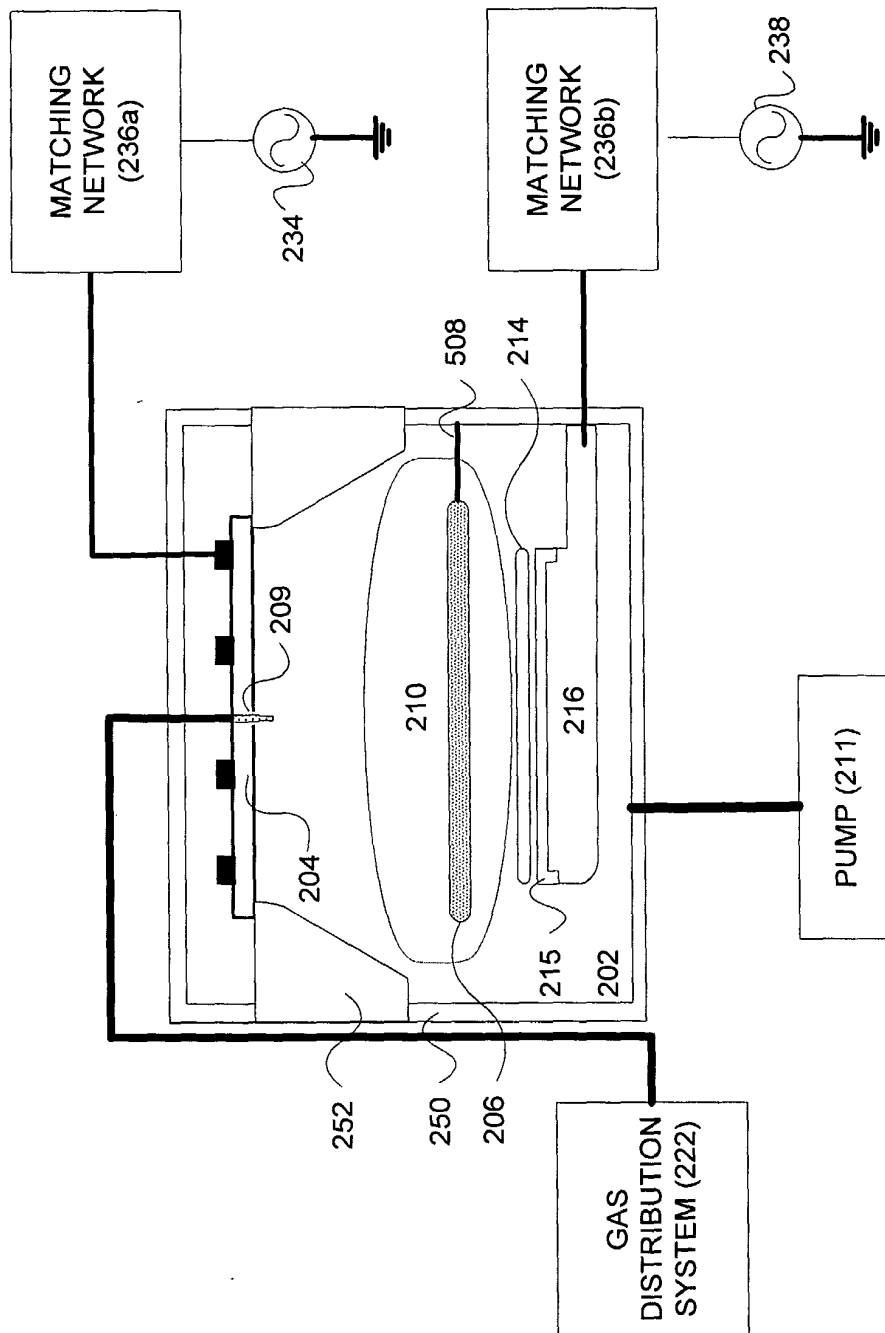


FIG. 5

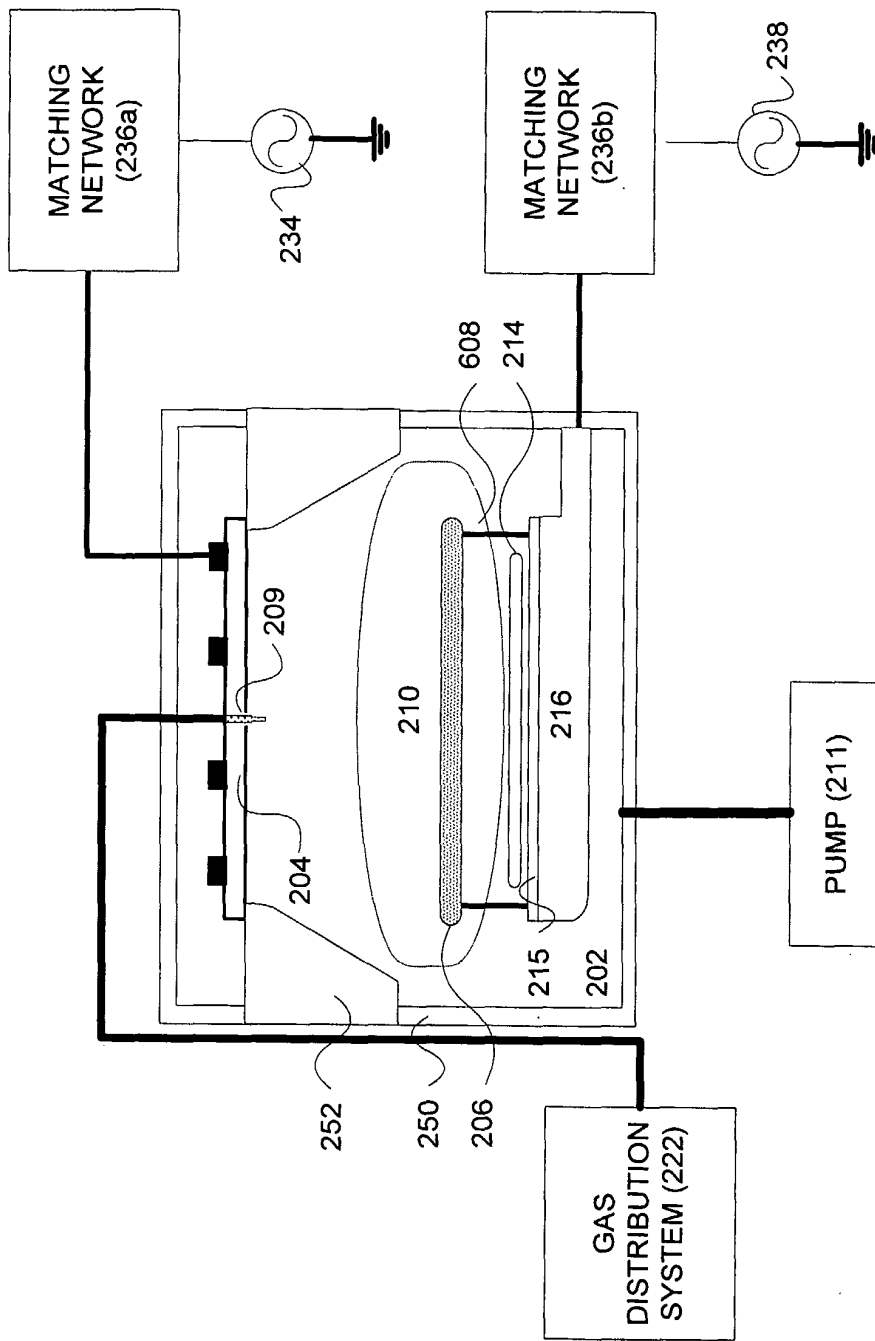


FIG. 6

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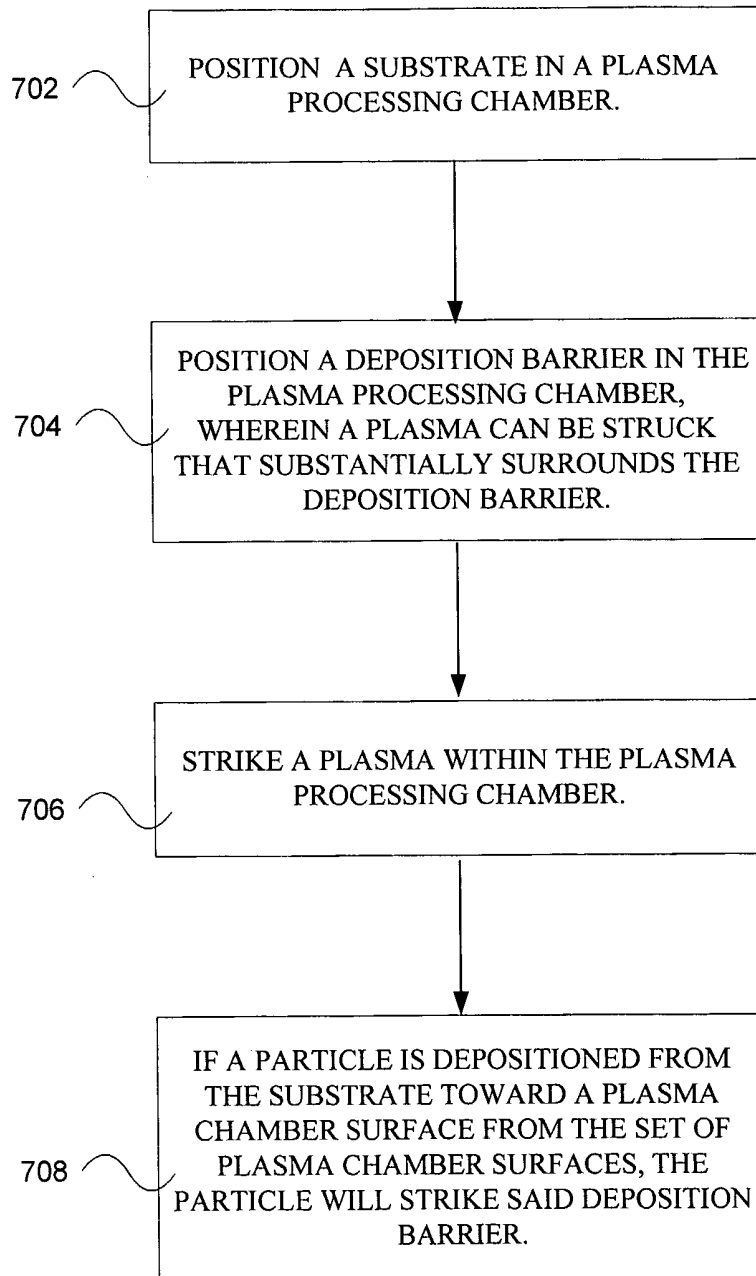


FIG. 7