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(12) **United States Design Patent**
Masuda

(10) **Patent No.:** **US D692,843 S**

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(54) **SEMICONDUCTOR DEVICE**

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Osaka-shi (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/431,909**

(22) Filed: **Sep. 12, 2012**

(30) **Foreign Application Priority Data**

May 2, 2012 (JP) 2012-010399

(51) **LOC (9) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; 174/250; 257/77, 79, 83;
438/44

See application file for complete search history.

(56) **References Cited**

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(57) **CLAIM**

The ornamental design for a semiconductor device, as shown.

DESCRIPTION

FIG. 1 is a front view of a semiconductor device showing my new design;

FIG. 2 is a rear view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a bottom plan view thereof;

FIG. 5 is a right side view thereof;

FIG. 6 is a left side view thereof; and

FIG. 7 is an enlarged front view showing the area defined by indicia 7A and 7B of FIG. 1 thereof;

FIG. 8 is an enlarged front view showing the area defined by indicia 8C and 8D of FIG. 7 thereof; and,

FIG. 9 is an enlarged perspective view thereof.

1 Claim, 4 Drawing Sheets

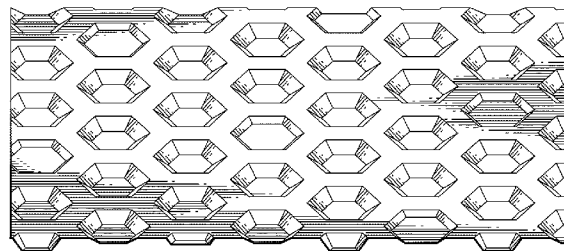
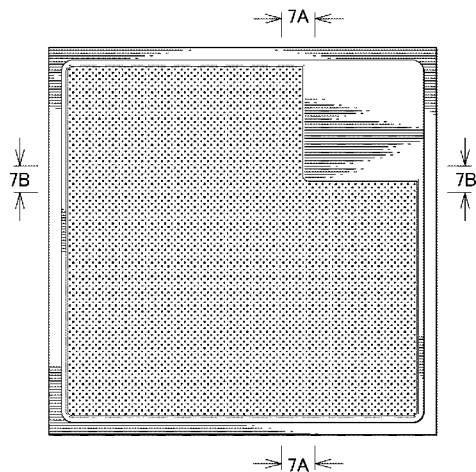


FIG.1

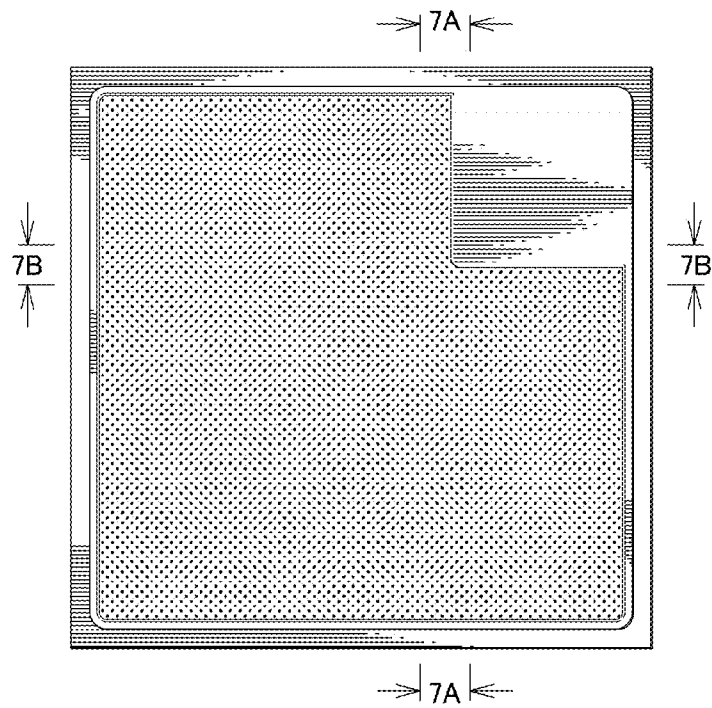


FIG.2

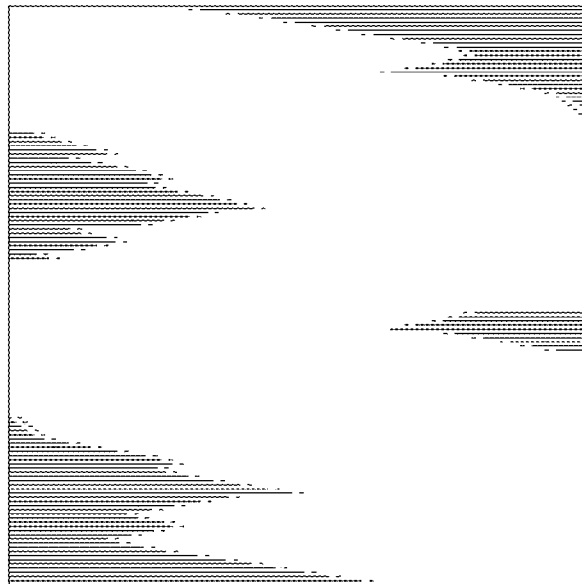


FIG.3

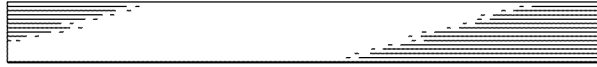


FIG.4

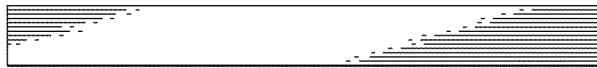


FIG.5

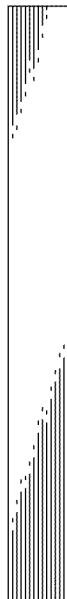


FIG.6



FIG.7

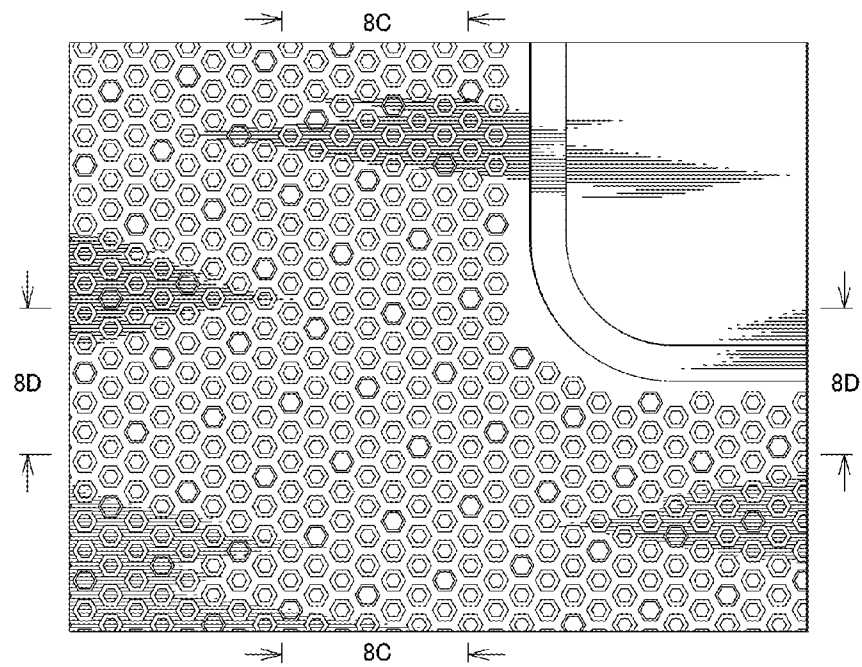


FIG.8

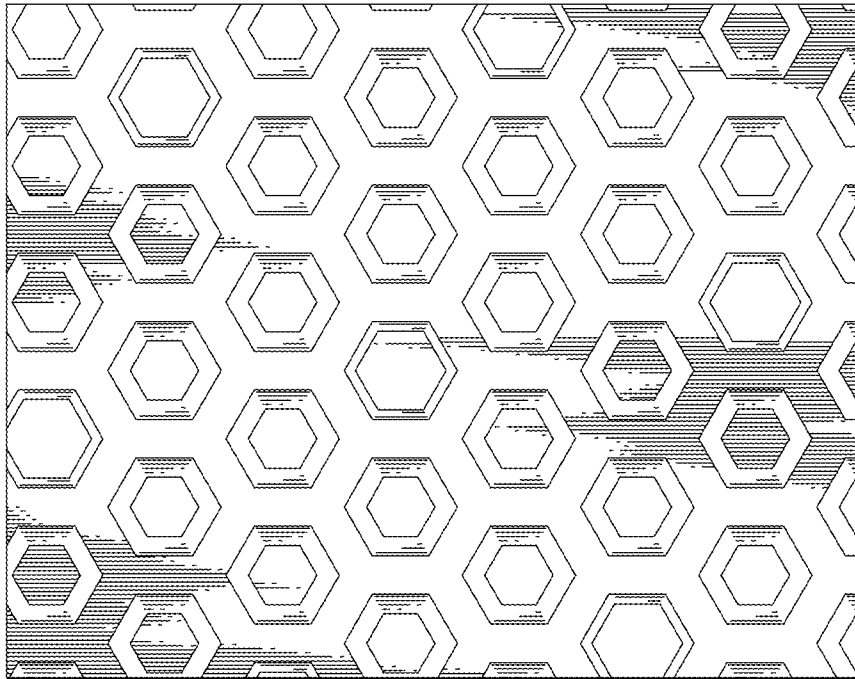


FIG.9

