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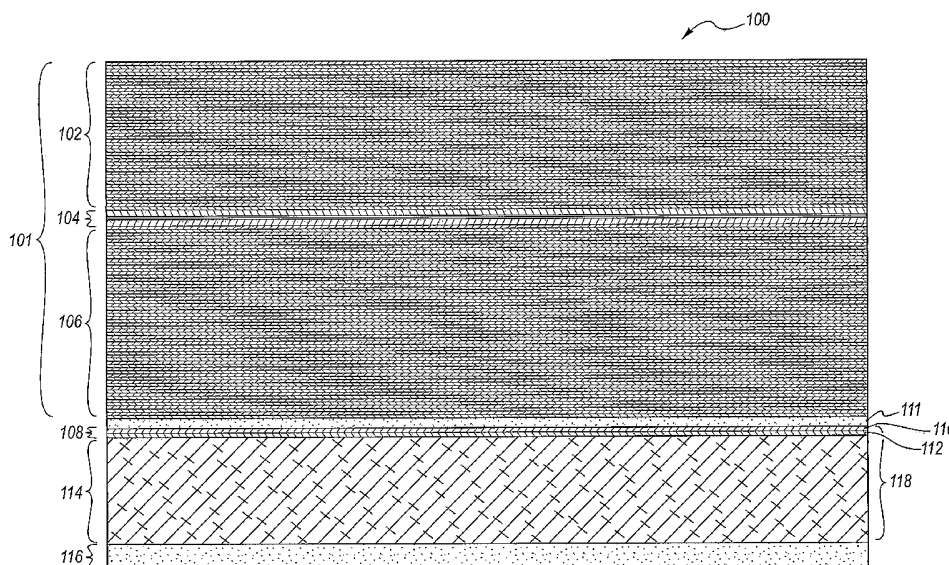
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(54) Title: LIGHT EMITTING DEVICE WITH AN INTEGRATED MONITOR PHOTODIODE BACKGROUND OF THE INVENTION



(57) Abstract: A monolithically formed laser and photodiode. The monolithically formed laser and photodiode includes a Vertical Cavity Surface Emitting Laser (VCSEL) that includes a first PN junction. The first PN junction includes a first p layer and a first n layer. A tunnel diode is connected to the VCSEL both physically and electronically through a wafer fabrication process. A photodiode is connected to the tunnel diode. The photodiode is connected to the tunnel diode by physical and electronic connections. The tunnel diode and photodiode may share some common layers. The tunnel diode includes a second PN junction. The monolithically formed laser and photodiode allow for an integrated structure with diode biasing flexibility including the use of a single supply to bias both the laser and photodiodes.

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5 LIGHT EMITTING DEVICE WITH AN INTEGRATED MONITOR PHOTODIODE

BACKGROUND OF THE INVENTION

1. THE FIELD OF THE INVENTION

The invention generally relates to vertical cavity surface emitting lasers. More specifically, the invention relates to using photodiodes in conjunction with vertical cavity
10 surface emitting lasers.

2. DESCRIPTION OF THE RELATED ART

Lasers have become useful devices with applications ranging from simple laser pointers that output a laser beam for directing attention, to high-speed modulated lasers useful for transmitting high-speed digital data over long distances. Several different types
15 of lasers exist and find usefulness in applications at the present time. One type of laser is the edge emitter laser which is formed by cleaving a diode from a semiconductor wafer. Cleaving a diode from a semiconductor wafer forms mirrors that form a laser cavity defined by the edges of the laser diode. Edge emitter lasers may be designed to emit a laser beam more strongly from one of the edges than the other edges. However, some
20 laser energy will be emitted at the other edges. Edge emitter lasers are commonly used when high optical power is needed.

A second type of commonly used laser is known as a vertical cavity surface emitting laser (VCSEL). A VCSEL is formed in part by forming a first mirror from Distributed Bragg Reflector (DBR) semiconductor layers. The DBR layers alternate high
25 and low refractive indices so as to create the mirror effect. An active layer is then formed on the first mirror. A second mirror is formed on the active layer using more DBR semiconductor layers. Thus the VCSEL laser cavity is defined by top and bottom mirrors which causes a laser beam to be emitted from the surface of the laser. Laser diodes generally operate using a forward bias. To forward bias a laser diode, a voltage is applied
30 to the anode and a lower voltage or ground is connected to the cathode.

In some simple applications, the lasers may be operated open loop. I.e., the lasers do not require feedback, or can operate satisfactorily without feedback. For example, in most laser pointer applications, the output power of the laser beam may be controlled without reference to the actual output power. In other applications, it may be very
35 important to precisely gauge the amount of actual output power emitted by the laser while it is operating. For example, in communications applications it may be useful to know the actual output power of the laser such that the output power of a laser may be adjusted to comply with various standards or other requirements.

5 Many applications use a laser in combination with a photodiode or other photosensitive device to control the output of the laser. A photodiode has current characteristics that change as light impinges the diode. The photodiode either has no bias or is implemented in a reverse bias configuration such that the cathode is connected to a high voltage while the anode is connected to a low voltage or ground. In a photodiode in
10 the reverse biased or unbiased configuration, current is generated within the photodiode as light impinges the photodiode. An appropriately placed photodiode may be used as one element in the feedback circuit for controlling the laser. For example, a photodiode used in an edge emitter laser application may be placed on one of the edges of the edge emitter laser diode. While the power output at each of the edges may be different, the
15 power at each of the edges is proportional by some factor to the power output at the other edges on in edge emitter laser diode. Similarly, the photodiode may be used to monitor the output power from other types of lasers.

Various challenges exist when implementing a laser diode and photodiode together. While the laser diode and photodiode share a similar construction and
20 composition, they have generally been implemented as separate devices. This allows a single power supply to be used for both biasing the laser diode and photodiode which are biased using opposite polarities. Using two discrete components, however, results in an increase of cost.

Attempts have been made to integrate the laser diode and photodiode
25 monolithically on a single wafer substrate. However, as mentioned above, this may require the use of two power supplies such as in the case when the laser diode and photodiode share a common cathode or anode. Additionally, the photodiode may be placed on top of the VCSEL or within a mirror that is part of the VCSEL. This however has the unfortunate drawback of causing the photodiode to become a part of the optics,
30 particularly the mirror, of the laser thus altering the optical characteristics of the laser.

Therefore, what would be advantageous are mechanisms for implementing laser diodes and photodiodes monolithically where a single power supply can be used.

BRIEF SUMMARY OF THE INVENTION

One embodiment of the invention includes an optoelectronic device that combines
35 a laser diode and photodiode monolithically. The optoelectronic device includes a VCSEL diode that includes a first PN junction. The first PN junction includes a first p layer and a first n layer. A tunnel diode is connected to the VCSEL diode monolithically. The tunnel diode includes a heavily doped n⁺ layer and a heavily doped p⁺ layer. A

5 photodiode is monolithically connected to the tunnel diode. The photodiode includes a second PN junction. The second PN junction may include one of the heavily doped p⁺ and n⁺ layers of the tunnel diode depending on other features of the optoelectronic device.

Another embodiment of the invention includes a method for making
10 optoelectronic devices such as monolithically formed laser diodes and photodiodes. The method includes forming a photodiode on a wafer. A tunnel diode is then formed on the photodiode, where the tunnel diode is electrically connected to the photodiode. A VCSEL diode is then formed on the tunnel diode, where the VCSEL diode is electrically connected to the tunnel diode.

15 Some embodiments of the invention may include other features such as optimizations for use in self mixing applications. By increasing a laser diodes susceptibility to light reflections and by allowing laser wavelength to be controlled using a bias current, a self mixing optoelectronic device can be created. These devices may be used in creating various devices such as cursor pointers, imaging devices, linear
20 measurement devices and the like.

Some embodiments of the invention allow for a single structure that includes both a laser diode and a photodiode. Further, because of the particular construction of these embodiments, a single power supply, as opposed to a positive and negative supply, can be used to power both laser diode and photodiode biasing circuits.

25 These and other advantages and features of the present invention will become more fully apparent from the following description and appended claims, or may be learned by the practice of the invention as set forth hereinafter.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWINGS

In order that the manner in which the above-recited and other advantages and
30 features of the invention are obtained, a more particular description of the invention briefly described above will be rendered by reference to specific embodiments thereof which are illustrated in the appended drawings. Understanding that these drawings depict only typical embodiments of the invention and are not therefore to be considered limiting of its scope, the invention will be described and explained with additional specificity and
35 detail through the use of the accompanying drawings in which:

Figure 1 illustrates an epitaxial structure including a laser diode, a tunnel diode and photodiode;

5 Figure 2A illustrates a monolithic structure including a laser diode and photodiode, where the monolithic structure also includes photolithographically formed contacts and isolation barriers;

 Figure 2B illustrates a top view of a monolithically formed laser diode and photodiode;

10 Figure 3 illustrates a schematic representation of the device shown in Figure 2A;

 Figure 4 illustrates a monolithic structure including a laser diode and photodiode connected by a tunnel diode where a contact is connected to the n layer of the tunnel diode;

15 Figure 5 illustrates a monolithic structure where optimizations are made for lasers used in self mixing applications; and

 Figure 6 illustrates a threshold current versus temperature graph useful for illustrating optimizations in linewidth enhancement factor for lasers used in self mixing applications.

DETAILED DESCRIPTION OF EMBODIMENTS THE INVENTION

20 Some exemplary embodiments of the present invention include monolithically formed laser diodes and photodiodes. The laser diodes and photodiodes are connected through tunnel diodes such that a single power supply may be used to power the laser diodes and photodiodes. Appropriate contacts are formed to allow access to the various junctions of the laser diodes, tunnel diodes, and photodiodes.

25 Referring now to Figure 1, an example of an optoelectronic device including an epitaxial structure 100 that includes layers formed on a wafer substrate is shown. In one embodiment, the structure 100 is grown using a GaAs wafer. However, other III-V (three-five) semiconductor combinations may also be used. Figure 1 shows a vertical cavity surface emitting laser (VCSEL) 101. The VCSEL 101 in the example shown includes a top mirror 102, an active region 104, and a bottom mirror 106. A PN junction
30 exists in the active region 104.

 A spacer layer 111 is formed below the bottom mirror 106. The spacer layer 111 is preferably about $5\lambda/4$ or $7\lambda/4$ where λ is the wavelength of light that the VCSEL 101 is designed to emit. The spacer layer 111 may be any odd integral multiple of the
35 wavelength (i.e., $\frac{\lambda(1+n)}{4}$ where n is an integer) that is thick enough to account for non-uniformities that may occur in various layers of the epitaxial structure 100. The spacer layer 111 must be sufficiently thick such that when etching is done to the spacer layer 111

5 the deepest non-uniformities do not go all the way through the spacer layer 111 and the shallowest portions reach the spacer layer 111. The goal, therefore is to expose the spacer layer 111 without going all the way through the spacer layer 111 at any point. Subsequent processing uses a selective etchant such as dilute hydrofluoric acid to uniformly expose the layer underlying the spacer.

10 The epitaxial structure 100 further includes a tunnel diode 108, formed on and electrically connected to the VCSEL 101. The tunnel diode 108 includes a heavily doped n+ layer 110 and a heavily doped p+ layer 112.

The epitaxial structure 100 further includes a photodiode, formed on and electrically connected to the tunnel diode 108, that includes a second PN junction that is
15 comprised of the heavily doped p+ layer 112 of the tunnel diode 108 and a lightly doped photodiode n layer 114. An additional p layer (not shown) that is not as heavily doped may also be fabricated between the p+ layer 112 and the photodiode n layer 114. In one embodiment, the epitaxial structure 100 is constructed on an n type substrate 116. Although the substrate 116 is shown in Figure 1 as a thin layer, the substrate is actually in
20 one embodiment of the invention in the hundreds of microns whereas the epitaxial structure including the photodiode, the tunnel diode 108, and the laser diode 101 is about 10 microns. The epitaxial structure 100 is generally grown using a metal organic chemical vapor phase deposition (MOCVD) process.

The top mirror 102 is a distributed Bragg reflector (DBR) that generally comprises
25 a number of alternating layers with high and low indexes of refraction. This creates a mirror with high reflectivity, around 99.5%. In the example shown, the top mirror is constructed of p type materials such as carbon doped aluminum gallium arsenide (AlGaAs), where the fraction of Al can vary from 0% to 100%. The top mirror 102 includes about 20 mirror periods where each period includes a high index of refraction
30 layer and a low index of refraction layer.

The active region 104 includes a number of quantum wells for stimulating the emission of laser energy. In the embodiment shown, active region 104 is less than 1 micron.

Below the active region is a bottom mirror 106. The bottom mirror is comprised
35 of about 30 to 35 doped n type mirror periods. Silicon is one example of a dopant that may be used in the bottom mirrors.

The tunnel diode 108, as mentioned previously, includes a heavily doped n+ layer 110 and a heavily doped p+ layer 112. To accomplish the heavy doping, it may be

5 desirable to create a super lattice structure. For example, instead of only GaAs layers, it may be desirable to include both layers of GaAs and InGaAs to tailor the bandgap and doping properties to improve the tunnel diode 108. It is also desirable that the tunnel diode 108 be somewhat transparent so as to allow optical energy to pass through to the photodiode layer 114. This may be done in one embodiment by increasing doping on the
10 heavily doped n+ layer 110 so as to increase transparency through the so-called Burstein shift.

It would advantageous to balance the thickness of the heavily doped p+ layer 112 such that appropriate conduction exists through the tunnel diode 108 while maintaining appropriate transparency. Thus, in one embodiment of the invention, the heavily doped
15 p+ layer 112 is about 50 to 100 nanometers and preferably at least 3×10^{19} of p type material (e.g., when used in an 850 nm laser). The heavily doped n+ layer may be nearly any practicable thickness without imposing an optical penalty.

Below the tunnel diode 108 is a photodiode 118. The photodiode 118 should be constructed so as to have an appropriate responsivity to incoming light. Thus, in one
20 embodiment of the invention, the photodiode 118 includes a lightly doped n layer 114 that is approximately three microns or less when the VCSEL 101 is designed to emit an 850 nm wavelength. One embodiment of the invention includes a lightly doped n layer 114 that is about 1.5 microns. Notably, the thickness of the lightly doped n layer 114 can be used to tailor the responsivity and speed of the photodiode.

25 Referring now Figure 2A, an embodiment is shown where contacts and oxide layers are formed through a photolithographic process. The formation of contacts allows appropriate biasing to be applied to, and signals to be read from the VCSEL 101 and photodiode 118. The lithographic process includes a series of acts where photoresist is applied to the epitaxial layers 100. The photoresist is then exposed lithographically to
30 various patterns. Lithographic exposure allows a pattern of photoresist to remain on the epitaxial layers 100 while the remainder of the photoresist may be washed from the epitaxial layers 100.

The patterns of photoresist that remain on the epitaxial layers 100 block ions from being implanted in the epitaxial layers 100, metal from being deposited on the epitaxial
35 layers 100, and etching solutions from etching portions of the epitaxial layers 100. Thus, using appropriate photolithographic processes a monolithic structure 200 that includes a VCSEL 101, a tunnel diode 108 and a photodiode 118 may be constructed with

5 appropriate contacts and with appropriate isolation from other devices on a wafer being fabricated simultaneously.

In the example shown in Figure 2A, a tunnel diode contact 222 is formed such that it connects to the heavily doped p layer 112. A VCSEL contact 224 is formed such that it connects to the top mirror 102 of the VCSEL 101. Another contact may be formed on the
10 bottom of the substrate 116 to provide the appropriate contact to the photodiode 118. Isolation barriers 226 are formed to isolate the VCSEL 101 and the photodiode 118 from other devices being formed on the substrate 116. An aperture 228 is oxidized into the top mirror 102. The aperture 228 is used primarily to direct current flow through the VCSEL 101.

15 More specifically, the optoelectronic device 200 is fabricated from an epitaxial structure 100. The epitaxial structure 100 has a dielectric such as silicon dioxide or silicon nitride grown on it to form a portion of the dielectric layer 230. A shallow trench mask is used to remove portions of the oxide using an etching process. An etch process is used to form the shallow trench 232 in the VCSEL 101. The aperture 228 can then be
20 oxidized into the VCSEL 101.

A deep trench mask can then be used to etch the deep trench 234. An etch can be used to etch down to a thick AlAs spacer 111. This spacer may be AlAs or another AlGaAs composition, so long as the fraction of aluminum is substantially greater than that in the underlying layer. A stop etch may be used to etch through the AlAs spacer 111
25 to the n⁺ layer 110. Another etch can be used to etch through the n⁺ layer 110 to the p⁺ layer 112. At this point, an additional oxide may be grown that forms additional portions of the dielectric layer 230. Portions of the dielectric layer 230 are removed followed by deposition of metal to form contacts 222, 224 on the optoelectronic device 200.

A top view of the optoelectronic device structure and contacts is shown in Figure
30 2B. Figure 2B illustrates the placement of the tunnel diode contact 222 and the VCSEL contact 224. While a single tunnel diode contact 222 is shown, in other embodiments, an additional tunnel diode contact may be formed opposite the tunnel diode contact 222 to provide for additional wire bonding options when packaging the optoelectronic device structure.

35 Referring now Figure 3, a schematic diagram illustrating the device of Figure 2 shown. The schematic 300 of Figure 3 illustrates a VCSEL diode 302 with a VCSEL contact 353 at the anode of the VCSEL diode 302. The cathode of the VCSEL diode 302 is connected to the cathode of a tunnel diode 308. The tunnel diode 308 anode is

5 connected to the anode of a photodiode 318 at node 362. A tunnel junction contact 358 is available at the anode of the tunnel diode 308 and the anode of the photodiode 318. A photodiode contact 360 is connected to the photodiode 356 cathode. The tunnel junction contact 358 may, in one application be connected to ground. A supply voltage may be connected to the VCSEL contact 353 and the photodiode contact 360. Alternatively, if
10 different voltage levels are desirable for biasing the VCSEL diode 302 and the photodiode 318 one of the voltage levels can be derived from the supply voltage such as by using a boost converter or other voltage converter. In any case, using the particular embodiment illustrated, a single voltage supply source may be used to operate both the VCSEL diode 302 and the photodiode 318.

15 Other embodiments may also be implemented within the scope of embodiments of the present invention. For example, Figure 4 illustrates an example where the tunnel diode contact 222 is connected to the heavily doped n⁺ layer 110 as opposed to the connection to the heavily doped p⁺ layer 112 as is shown in Figure 2A. Schematically, this results in the tunnel junction contact 358 shown in Figure 3 being located between
20 the cathodes (at node 362) of the VCSEL diode 302 and the tunnel diode 308. This may be advantageous in some embodiments as it reduces the amount of current that must pass through the tunnel diode 308.

In the embodiment shown in Figure 3, the current from the VCSEL diode 302 must also pass through the tunnel diode 308. This current can be quite large compared to
25 the current through the photodiode 318. By placing the tunnel junction contact between the cathodes of the VCSEL diode 302 and the tunnel diode 308 (node 352), only the current passing through the photodiode 318 need pass through the tunnel diode 308. This allows the tunnel diode layers to be minimized so as to maximize their transparency. Various other changes may also be implemented such as reversing the order of the PN
30 junction layers in each of the diodes.

Referring once again to Figure 2A, an optimization of the optoelectronic device 200 that mitigates the effects of spontaneous emissions from the active region 104 will be discussed. The active region has emissions that may not necessarily be at the lasing wavelength, i.e., the wavelength at which the VCSEL 101 is intended to operate. It is
35 desirable to detect just the lasing wavelength at the photodiode 118. To reduce the amount of spontaneous emissions that reach the photodiode 118, layers in the bottom mirror 106 with a high Ga fraction, i.e., the high index of refraction layers, may have the

5 amount of Ga in them optimized such that they become heavily absorbing below the lasing wavelength.

While the above description has been framed in the context of VCSEL diodes, other light generating devices may also be used. For example, a resonant cavity light emitting diode (RCLED) may be used as a light generating device in place of the VCSEL
10 diodes.

Some embodiments of the invention may find particular usefulness in self mixing laser applications. Self mixing laser applications make use of laser power reflected back into the laser cavity and more especially into the active region. This reflected laser power changes the output of the laser. The output of the laser may be changed such as by
15 changing the wavelength of the laser or by causing the laser output to be modulated in various beat patterns. A change in the wavelength of the laser, however, may be almost undetectably small. Thus the change in the wavelength may be detected as a shift in phase of an optical signal. By continuously monitoring output from the laser, information can be gathered about conditions external to the laser. For example, using appropriate
20 modulation and digital signal processing, information can be gathered about the distance of objects from a laser, movement of objects about the laser and the like. This allows the laser to be used in applications such as imaging, linear measurements, cursor pointers and the like.

The laser output may be modulated by increasing and decreasing the wavelength
25 of the beam output by the laser, for example by changing the temperature of the laser. Thus, if the temperature of the laser can be controlled, the wavelength output from the laser can also be controlled. By modulating the laser wavelength, a chirp, e.g. rising and falling frequencies, can be used in a Doppler effect detector. An integrated photodiode is therefore useful for monitoring laser output power in self mixing applications where a
30 Doppler effect causes a change in the output of the laser.

In some embodiments of the invention, a structure is fabricated to optimize or enhance the effects of reflections, temperature changes and the like. This can be accomplished by optimizing optical characteristics such as by allowing more light to be reflected from outside the laser into the active region. Other optimizations contemplate
35 optimizing the laser to be effected by temperature changes such as by changing the thermal conductivity/resistance or thermal mass characteristics of the laser. Still other optimizations alter the threshold current versus temperature operating characteristics to change the characteristics of the laser by optimizing the linewidth enhancement factor.

5 The following optimizations are particularly well suited for self mixing applications. It should therefore be understood that these optimizations are exemplary only, and not required by all embodiments of the invention. In fact, the optimizations used in self mixing applications are often the opposite of optimizations that may be used in other application. For example, while self mixing applications make use of
10 temperature changes and reflections, other applications, such as communications applications, attempt to minimize these and other effects. These optimizations for use in self mixing applications will now be discussed with reference to Figure 5. While characterized as optimizations, optimize as used herein does not require that the components be optimized to their absolute optimal configuration, but rather that the
15 components be fabricated so as to increase sensitivity to reflections and thermal changes. Figure 5 illustrates optimizations for self mixing applications to increase the sensitivity of an optoelectronic device to light reflections, temperature changes and the like. Figure 5 illustrates an optoelectronic device 500 that includes a VCSEL 501 with integrated photodiode.

20 OPTICAL CHARACTERISTIC OPTIMIZATION

 One method of increasing the sensitivity of the VCSEL 501 is by fabricating the optoelectronic device 500 to allow more light to be reflected into the active region 504. This may be done by changing the doping levels of the alternating layers in the top mirror 502 to be more lightly doped. This causes the top mirror 502 to be more translucent to
25 light reflected back into the active region 504.

 By reducing the doping in the alternating layers of the top mirror 502, electrical resistance is increased. This causes the VCSEL 501 to run at a higher temperature. Thus by reducing the doping in the top mirror 502, sensitivity of the VCSEL 501 is compounded by the combined effects of a more translucent top mirror 502 and increased
30 temperature sensitivity of the VCSEL 501. Temperature sensitivities will be covered in more detail below.

 The opaqueness of the VCSEL 501 may also be optimized by appropriately selecting the thickness of a dielectric layer 530 on the top mirror 502. Ordinarily, it is desirable to make the top mirror 502 as reflective as possible. However, in self mixing
35 applications it may be desirable to make the top mirror 502 less reflective and more translucent. To make the top mirror 502 more reflective a dielectric layer 530 is placed on the top mirror where the dielectric layer has a thickness that is nearly some integral multiple of a half wavelength of the wavelength for which the laser is designed. To make

5 the top mirror 502 more translucent, the dielectric layer 530 can be designed such that is a multiple of an odd quarter wavelength. For example, the thickness of the dielectric layer 530 may be selected such that the thickness is $\frac{2n+1}{4}\lambda$ where n is an integer and λ is the wavelength of design for the laser. Optimizing the dielectric layer 530 allows for other thicknesses to be used depending on the desired opaqueness of the top mirror 502.

10 THERMAL CHARACTERISTIC OPTIMIZATION

Another way of increasing the sensitivity of the VCSEL 501 is by changing the thermal characteristics of the epitaxial layers. More particularly, it may be desirable to increase the sensitivity of the optoelectronic device 500 to temperature variations so as to be able to control modulation of the output wavelength of the VCSEL 501.

15 Optimizations of thermal characteristics may fall into different categories, such as for example, optimizations to thermal impedance and optimizations to thermal mass.

Thermal impedance is a measure of a devices ability to conduct heat. The less able a device is to conduct heat, the quicker the temperature will rise in the device as heat is applied. For example, if a device is less conductive of thermal energy, a given level of current will cause the operating temperature of the device to rise more quickly. This causes a corresponding change in the wavelength at which the device, such as a VCSEL 501, operates.

20

Thermal mass is a measure of a components temperature rise for a given amount of heat. A greater thermal mass means that the temperature does not increase as rapidly for a given amount of heat and the temperature change occurs more slowly. One way of increasing thermal mass is by increasing the physical mass of the device or by increasing connections to devices capable of dissipating heat. For example, connecting a heatsink, such as a large piece of metal with a large amount of surface area increases the thermal mass. In some embodiments of the invention, such as self mixing applications, it is desirable to *reduce* the thermal mass to optimize wavelength changes based on current changes in the VCSEL 501.

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To make the optoelectronic device 500 more sensitive to temperature variations by decreasing thermal mass, the contacts such as contacts 524, 522 may be optimized by minimizing their physical size to reduce their heatsinking effect. In one embodiment, the contacts are designed to be the minimal size sufficient for carrying an amount of current needed to appropriately bias and power the various diodes in the optoelectronic device 500.

35

5 One way of increasing thermal sensitivity in the optoelectronic device 500 by decreasing thermal conductivity is by forming a trench around the active region. This prevents some amount of thermal conduction from heat generating parts of the optoelectronic device 500 to other materials that may provide heatsinking functionality. In one embodiment, forming a trench may be accomplished by optimizing the shallow
10 trench 532 to extend below the active region 504. In the example shown in Figure 5, by extending the shallow trench 532 below the active region 504, the VCSEL 501 becomes more sensitive to temperature variations.

 Another method of optimizing the thermal conductivity of the optoelectronic device including the optoelectronic device 500 is by selecting an appropriate construction
15 of the bottom mirror 506. The mirrors may be formed of layers alternating low and high index of refraction layers. Notable, binary type materials, i.e., those formed from two elements, are more thermally conductive than ternary type materials, i.e., those formed from three elements. In Figure 5, the high index of refraction layers and low index layers in the bottom mirror 506 are AlGaAs where the Al fraction may be selected from the
20 range from 0% to 100. The lower refractive index layers have a higher Al fraction. By forming a number of ternary layers, such as AlGaAs near the active region and keeping binary layers such as AlAs and GaAs layers, if present, further from the active region, the thermal conductivity can be decreased.

 Yet another method of optimizing the thermal conductivity for self mixing
25 applications of the optoelectronic device involves increasing the number of mirror periods in the mirrors 502, 506. This causes a decrease in the thermal conductivity of the VCSEL 501. To maintain an appropriate reflectivity of the mirrors 502, 506, it may be useful to vary the Al/Ga ratio in the individual mirror periods to maintain a total desired reflectivity for all of the mirror periods when combined to form the mirrors 502, 506.

30 There is some need to balance thermal conductivity and thermal mass so as to preserve the ability to change temperature, and thus wavelength, quickly. Namely, if thermal mass is decreased, the thermal impedance can be increased and if the thermal impedance is increased, the thermal mass can be decreased.

 Increasing the electrical impedance of a device generally causes more heat to be
35 generated in the device for a given current. For a constant thermal mass and thermal conductivity, increases in heat generated increases operating temperature. As mentioned above, one way of increasing the electrical impedance is by reducing the doping in the top

5 mirror 502. A similar reduction in the doping in the bottom mirror 506 will have a similar effect.

Another method of optimizing the electrical impedance of the optoelectronic device 500, is by controlling the size of the aperture 528. By oxidizing the aperture 528 further into the VCSEL 501 structure, the opening of the aperture 528 is decreased
10 thereby decreasing the area available for current flow through the VCSEL 501. This in turn increases the electrical impedance of the VCSEL 501. By controlling the electrical impedance of the VCSEL 501, current controls can be used to regulate the heat generated by the VCSEL 501 and therefore the operating temperature of the VCSEL 501.

LINEWIDTH ENHANCEMENT FACTOR OPTIMIZATION

15 One optimization alters the characteristics of the laser so as to change the threshold current versus temperature operating characteristics, which affects the linewidth enhancement factor. Referring now Figure 6, a curve 600 is shown that illustrates the relationship between the threshold current and the operating temperature of a VCSEL. Shown on the curve 600 is a point T_0 where the threshold voltage for the VCSEL is at its
20 lowest value with respect to the temperature. By adjusting T_0 to higher values relative to the expected operating temperature, a VCSEL can be optimized for sensitivity in self mixing applications. The linewidth enhancement factor is increased as T_0 is moved above the operating temperature.

The present invention may be embodied in other specific forms without departing
25 from its spirit or essential characteristics. The described embodiments are to be considered in all respects only as illustrative and not restrictive. The scope of the invention is, therefore, indicated by the appended claims rather than by the foregoing description. All changes that come within the meaning and range of equivalency of the claims are to be embraced within their scope.

5 CLAIMS

What is claimed is:

1. An optoelectronic device comprising:
a VCSEL diode comprising a first PN junction with a first p layer and a first n layer;
10 a tunnel diode coupled monolithically to the VCSEL diode, the tunnel diode comprising a heavily doped n⁺ layer and a heavily doped p⁺ layer; and
a photodiode coupled monolithically to the tunnel diode, the tunnel diode comprising a second PN junction.
2. The optoelectronic device of claim 1, wherein the second PN junction
15 comprises one of the heavily doped n⁺ layer and the heavily doped p⁺ layer.
3. The optoelectronic device of claim 1 wherein the first n layer is attached at the n⁺ layer.
4. The optoelectronic device of claim 1 wherein the first p layer is attached at
20 the p⁺ layer.
5. The optoelectronic device of claim 1 further comprising a tunnel junction contact coupled to the n⁺ layer.
6. The optoelectronic device of claim 1 further comprising a tunnel junction contact coupled to the p⁺ layer
- 25 7. The optoelectronic device of claim 1, wherein the first PN junction comprises:
a p type DBR mirror;
a VCSEL active region coupled to the p type DBR mirror;
an n type DBR mirror coupled to the VCSEL active region; and
30 wherein the p type DBR mirror comprises layers with doping optimized for a more translucent mirror.
8. The optoelectronic device of claim 1 further comprising:
a first contact coupled to the VCSEL;
a second contact coupled to the tunnel diode;
35 a third contact coupled to the photodiode;
wherein the contacts are optimized to reduce the thermal mass of the optoelectronic device.

- 5 9. The optoelectronic device of claim 1 wherein the VCSEL comprises an active region, the optoelectronic device further comprising a trench about the active region, the trench optimized to reduce the thermal conductivity of the optoelectronic device.
- 10 10. The optoelectronic device of claim 9, wherein the trench comprises an optimized radius.
11. The optoelectronic device of claim 1, wherein the tunnel diode comprises an InGaAs superlattice.
- 15 12. The optoelectronic device of claim 1, further comprising a dielectric layer disposed on the VCSEL, the dielectric layer being optimized to allow the VCSEL to be more translucent.
13. The optoelectronic device of claim 12, wherein the dielectric layer has a thickness of about $((2n+1)/4)*\lambda$ for n equal to an integer and λ equal to the wavelength of design for the VCSEL.
- 20 14. The optoelectronic device of claim 1, wherein the first PN junction comprises:
 a p type DBR mirror;
 a VCSEL active region coupled to the p type DBR mirror;
 an n type DBR mirror coupled to the VCSEL active region; and
 wherein the n type DBR mirror comprises layers of alternating high and
25 low refractive index layers, wherein a plurality of the low refractive index layers near the active region comprise AlGaAs and a plurality of the low refractive index layers further from the active region comprise AlAs and GaAs to optimize the thermal conductivity of the optoelectronic device for use in self mixing applications.
- 30 15. The optoelectronic device of claim 1, the VCSEL further comprising a T_0 optimized for self-mixing applications.
16. The optoelectronic device of claim 1, the VCSEL further comprising a bottom mirror, the bottom mirror comprising alternating high and low index of refraction layers wherein at least a portion of the alternating
35 layers of the bottom mirror comprise an optimized Ga fraction for reducing spontaneous emissions from reaching the photodiode.
17. A method of making an optoelectronic device, the method comprising:
 forming a photodiode on a wafer;

5 forming a tunnel diode on the photodiode, the tunnel diode being electrically connected to the photodiode; and

 forming a VCSEL diode on the tunnel diode, the VCSEL diode being electrically connected to the tunnel diode.

18. The method of claim 16, wherein forming a photodiode on a wafer
10 comprises forming an n layer and a p+ layer.

19. The method of claim 18, wherein forming a tunnel diode comprises forming an n+ layer on the p+ layer.

20. The method of claim 19, wherein forming a laser diode comprises forming a laser n layer on the n+ layer and a laser p layer on the p+ layer.

15 21. An optoelectronic device comprising:
 an LED comprising a first PN junction with a first p layer and a first n layer;

 a tunnel diode coupled monolithically to the RCLED, the tunnel diode comprising a heavily doped n+ layer and a heavily doped p+ layer; and

20 a photodiode coupled monolithically to the tunnel diode, the tunnel diode comprising a second PN junction.

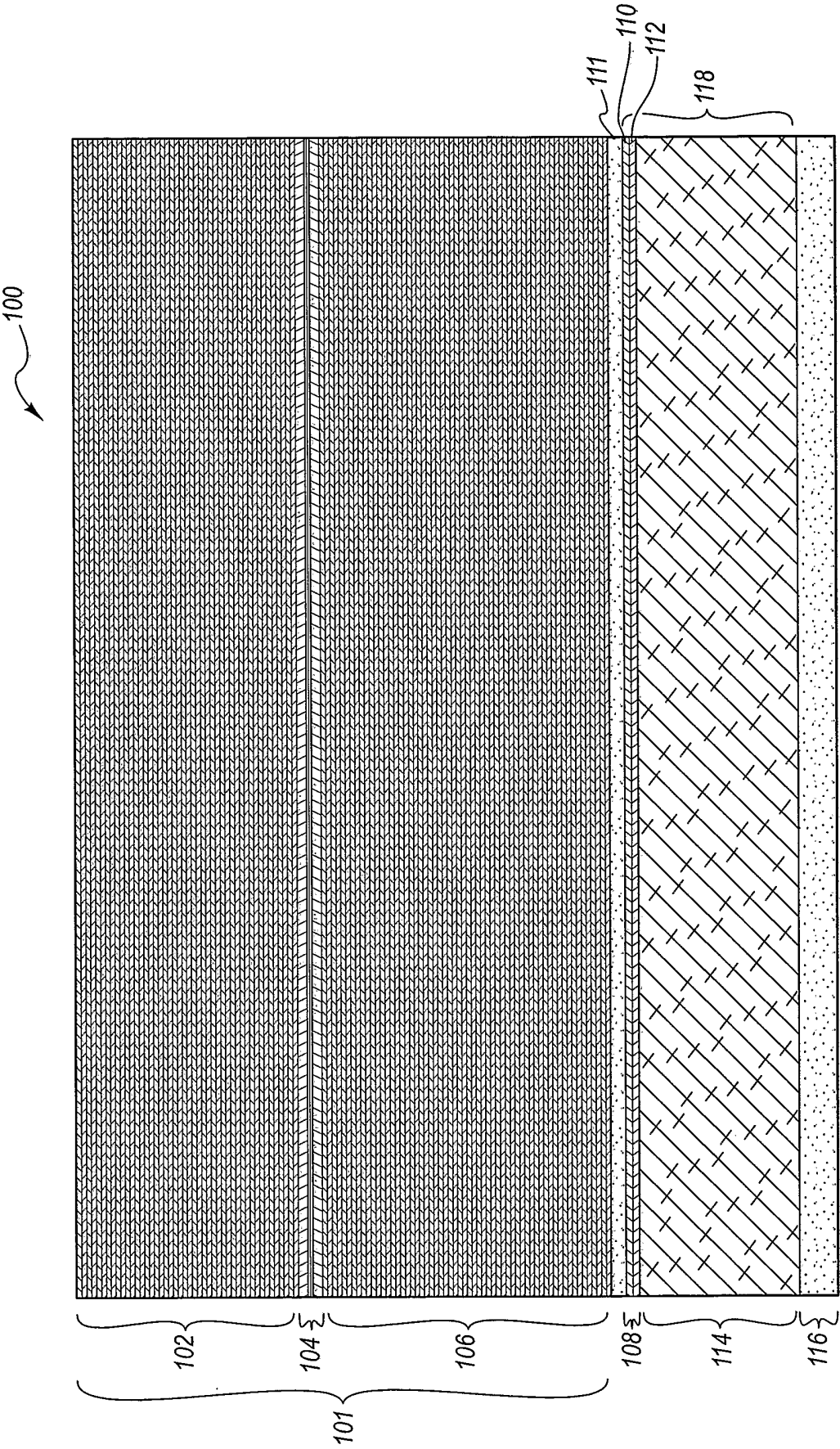


Fig. 1

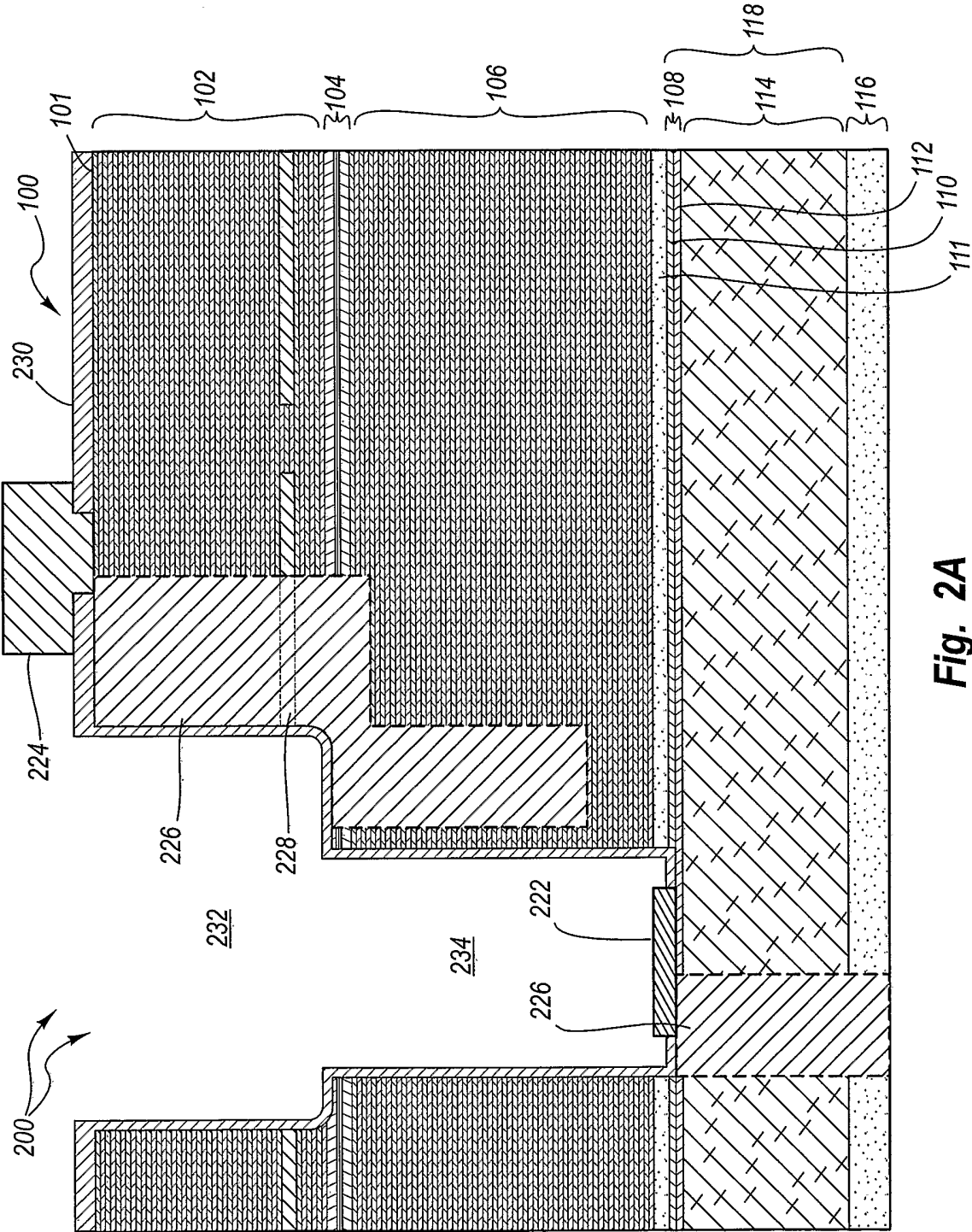


Fig. 2A

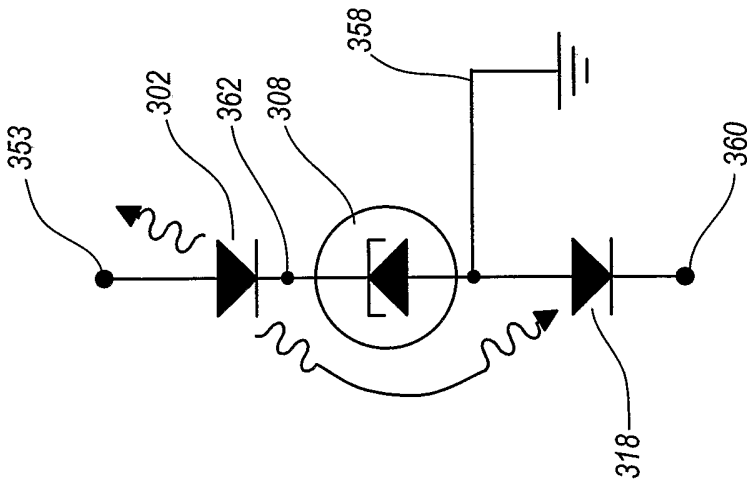


Fig. 3

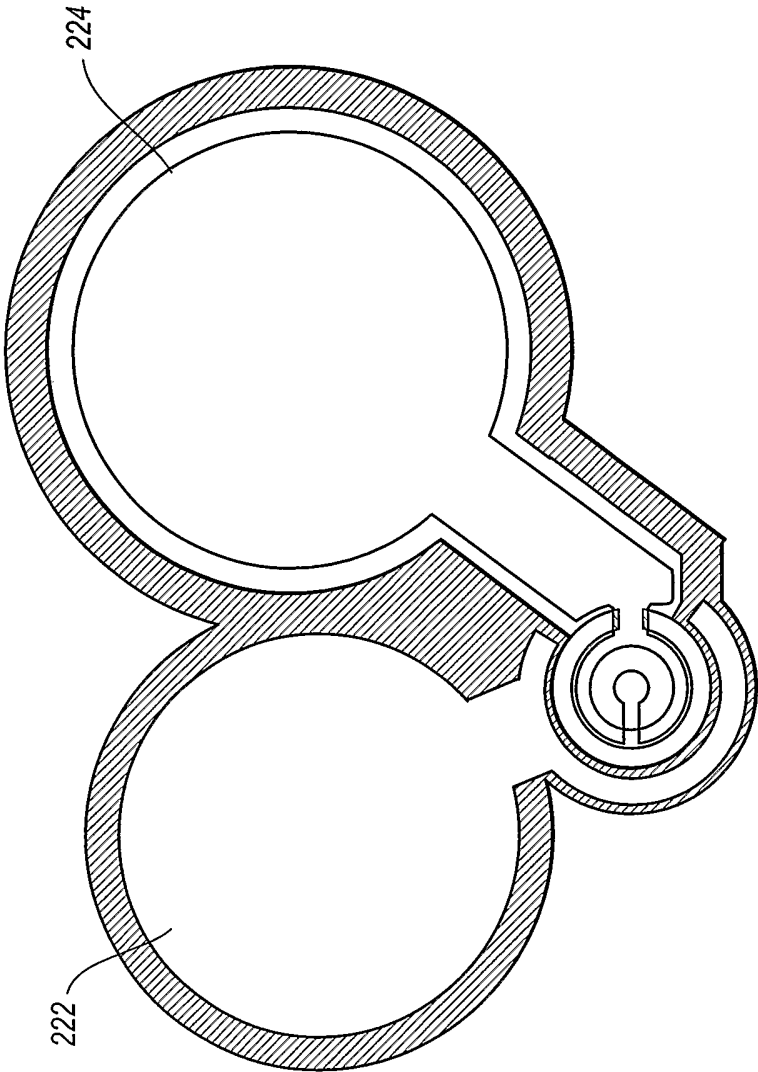


Fig. 2B

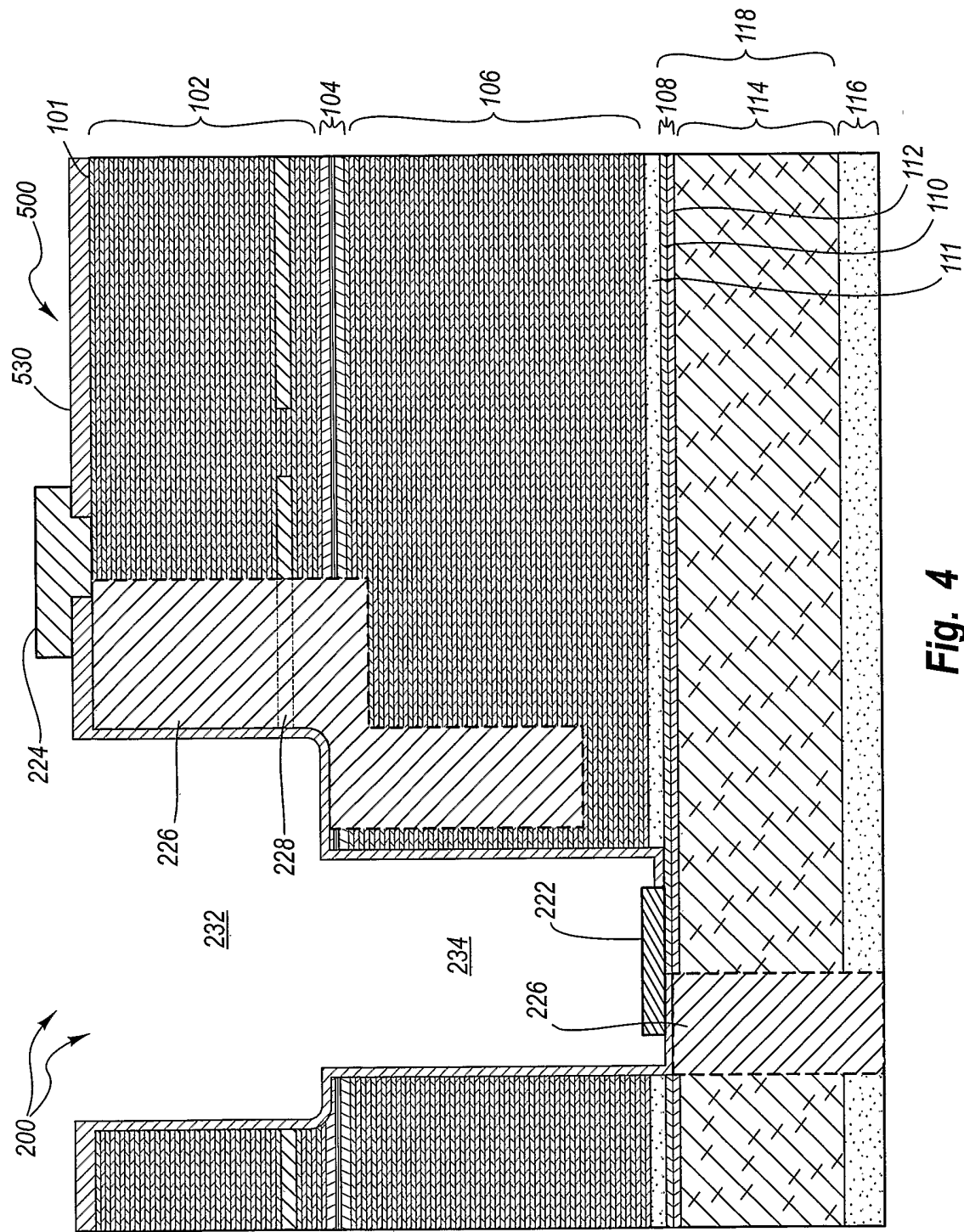


Fig. 4

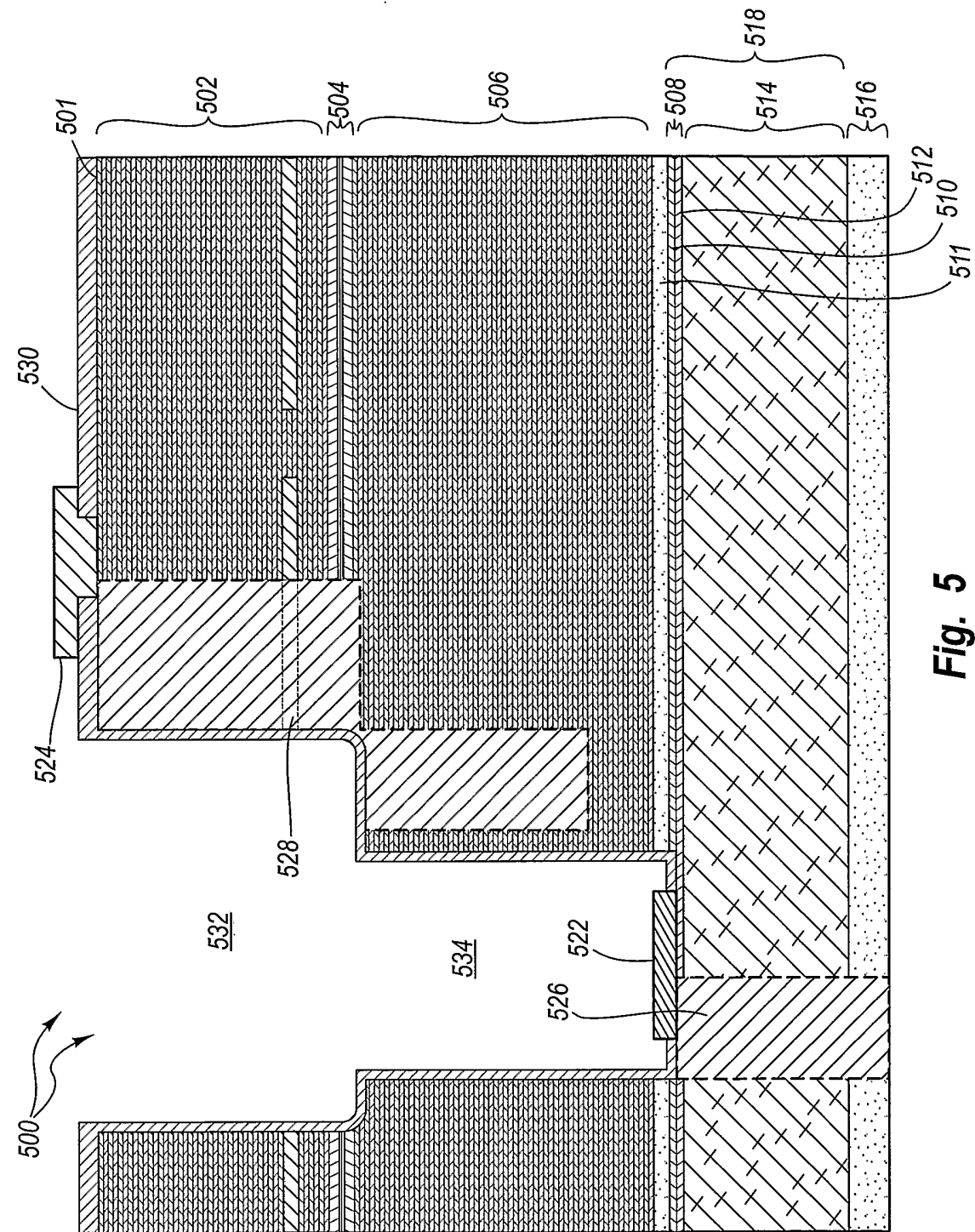


Fig. 5

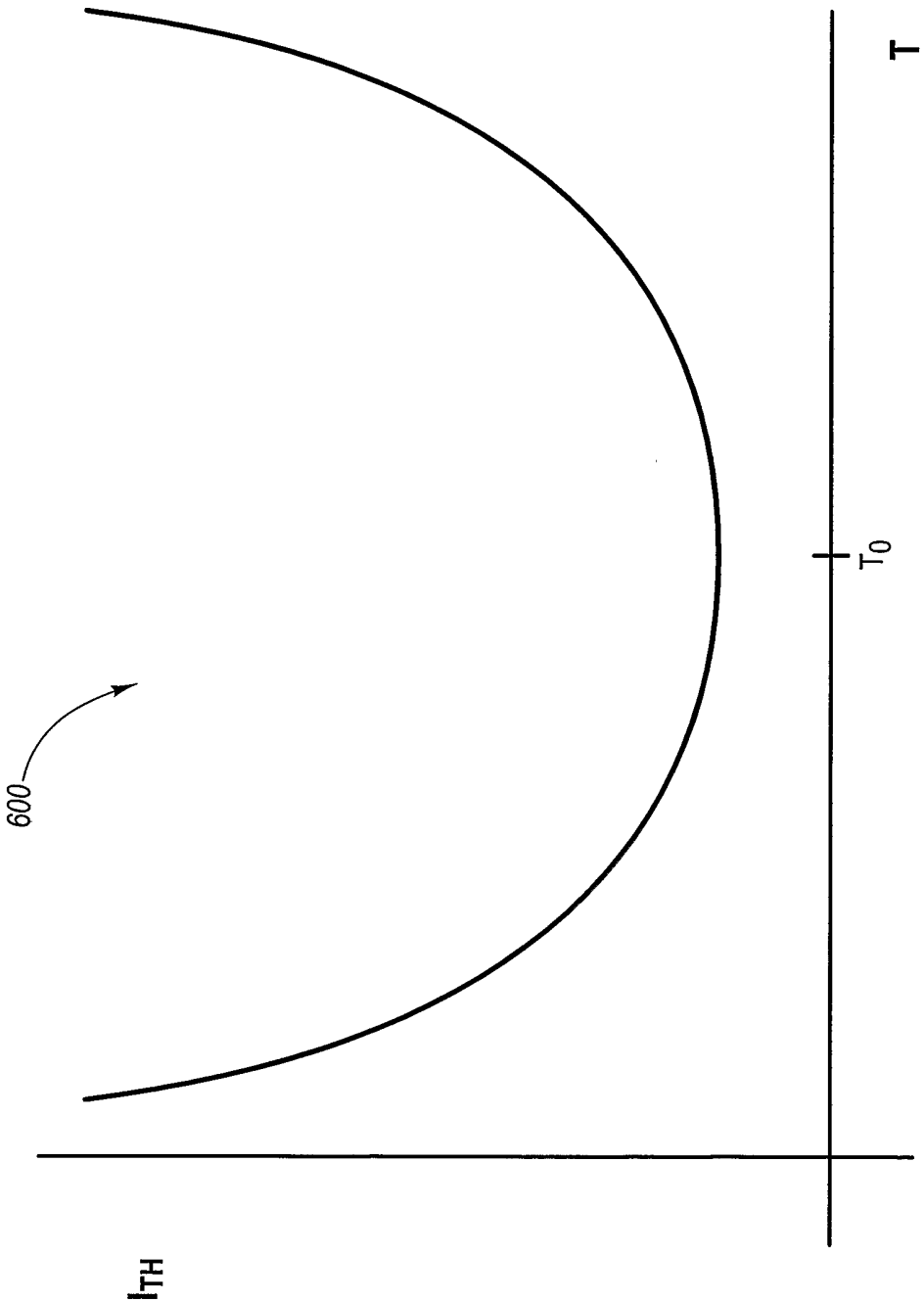


Fig. 6