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(54) **Apparatus and method for providing user-generated key schedule in a microprocessor cryptographic engine**

(57) The present invention provides an apparatus and method for performing cryptographic operations on a plurality of input data blocks within a processor. In one embodiment, an apparatus for performing cryptographic operations is provided. The apparatus includes a cryptographic instruction, keygen logic, and execution logic. The cryptographic instruction is received by a computing device as part of an instruction flow executing on the computing device. The cryptographic instruction prescribes one of the cryptographic operations, and also prescribes that a user-generated key schedule be employed when executing the one of the cryptographic operations. The keygen logic is operatively coupled to the cryptographic instruction. The keygen logic directs the computing device to load the user-generated key schedule. The execution logic is operatively coupled to the keygen logic. The execution logic employs the user-generated key schedule to execute the one of the cryptographic operations.

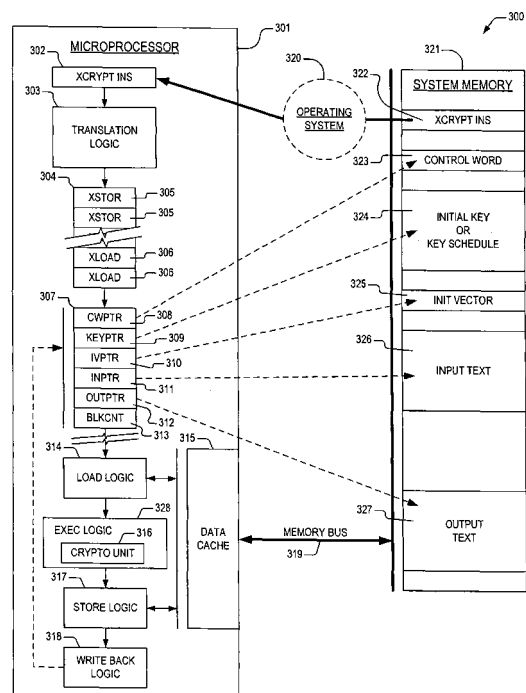


FIG. 3

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	WO 01/17152 A (COPPE UFRJ COORDENACAO DOS PRO [BR]; CARDOSO SALOMAO SERGIO LUIZ [BR];) 8 March 2001 (2001-03-08) * abstract * * page 1, line 1 - line 10 * * page 7, line 24 * * figures 3,4 *	1-3,5-8, 10,12	INV. H04L9/06
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			TECHNICAL FIELDS SEARCHED (IPC)
			H04L
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 12 February 2007	Examiner Liebhardt, Ingo
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons</p> <p>& : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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12-02-2007

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