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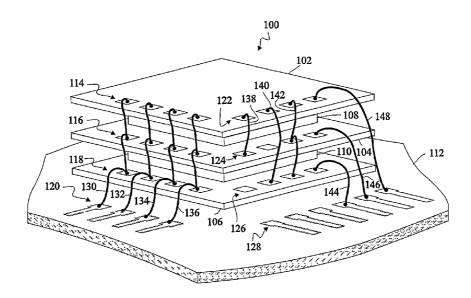
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(54) Title: STACKED DIE MODULE



(57) Abstract: Semiconductor dies (101/104/106) are stacked offset from one another so that terminals (122/124/126) located along two edges of each die are exposed. The two edges of the dies having terminals may be oriented in the same direction. Electrical connections (130/132/134) may connect terminals on one die with terminals on another die and the stack may be disposed on a wiring substrate (112) to which the terminals of the dies may be electrically connected.





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A. CLASSIFICATION OF SUBJECT MATTER IPC: H01L 23/02(2006.01),23/52(2006.01),23/34(2006.01),21/44(2006.01)			
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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where a		
X Y	US 5,998,864 (Khandros) 07 December 1999 (07.12.1999), column 6, lines 40-60.		1,5- 9,11,13,14,16,17,19- 25,32,39-40 and 45-46
			2-4,10,12,15,18,26- 31,41-44
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Y			34-38
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