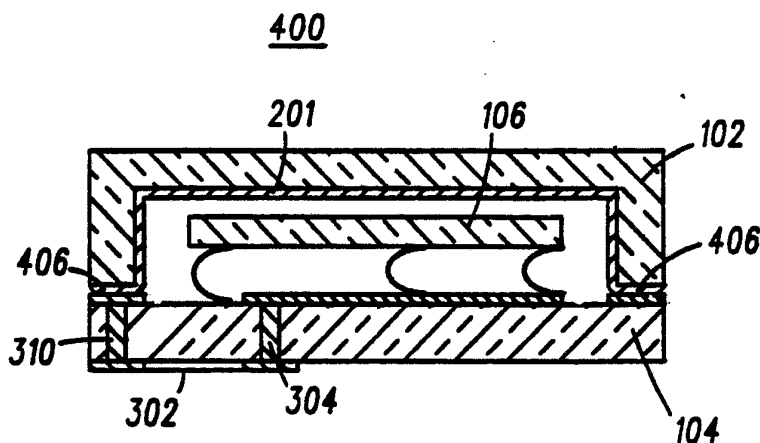




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(21) International Application Number: PCT/US92/01218 (22) International Filing Date: 13 February 1992 (13.02.92) (30) Priority data: 663,979 4 March 1991 (04.03.91) US (71) Applicant: MOTOROLA, INC. [US/US]; 1303 East Algonquin Road, Schaumburg, IL 60196 (US). (72) Inventors: KNECHT, Thomas, A. ; 6410 Coachlight, Crystal Lake, IL 60012 (US). MANCINI, Brian, M. ; 855 Seneca, Carol Stream, IL 60188 (US). ACHILLE, Jean-Robert ; 313 Plymouth Lane, Bloomingdale, IL 60108 (US). SIEBEN, David, J. ; 215 S. Hale, Palatine, IL 60067 (US).		(74) Agents: PARMELEE, Steven, G. et al. ; Motorola, Inc., Intellectual Property Dept., 1303 East Algonquin Road, Schaumburg, IL 60196 (US). (81) Designated States: JP, KR. Published <i>With international search report.</i>

(54) Title: SHIELDING APPARATUS FOR NON-CONDUCTIVE ELECTRONIC CIRCUIT PACKAGE

**(57) Abstract**

A shielding apparatus for a non-conductive package having an insulated lid (102) is disclosed. The insulated lid (102) is retained in contact with a substrate (104) which holds an electrical circuit. The shielding apparatus includes a first conductive flash (201) deposited on the interior surface of the insulated lid (102), to produce a substantially contiguous conduction layer, and a second conductive flash (203) deposited over a film (110) disposed on the edge of the insulated lid (102), to produce a partially contiguous conduction layer. The shielding apparatus also includes a means (108) for affixing the insulated lid (102) to the substrate (104), to produce an enclosure for isolating the electrical circuit from extraneous radio frequency (RF) energy.

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SHIELDING APPARATUS FOR NON-CONDUCTIVE ELECTRONIC CIRCUIT PACKAGE

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Field of the Invention

10 The present invention relates generally to shielding of electronic circuitry, and more particularly to a shielding apparatus for use with non-conductive electronic circuit packages.

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Background of the Invention

20 The requirement for isolation of radio frequency (RF) energy from electronic circuitry has existed since the beginning of the electronic age. For every circuit that generates, and is sensitive to, this form of energy, there is a desire to electrically isolate this circuit from the external environment in which it operates. In general, it is highly desirable to minimize the effect that a given circuit has on its environment as well as the effect that the environment has on the circuit. In particular, when designing a shield for a frequency control device, such as an oscillator circuit, it is critical to isolate the circuit from other RF sources which may be emitting signals whose primary or harmonic frequencies are found near the frequency of oscillation.

25 The aforementioned requirement is especially true for components used in communication systems where even small levels of RF radiation may cause degradation in critical electrical parameters of the target circuit. This problem is accentuated when shielding must be provided while maintaining a hermetically sealed, or airtight, joint

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between the cover and the substrate holding the electronic circuit, such as in a quartz crystal resonator, or filter. Hermeticity is required in order to prevent air contaminants from coming in contact with the oscillating quartz plate, as even microscopic particulates have a dramatic impact on the frequency characteristics of the device. This requirement presents the added challenge of designing a highly manufacturable process (one with minimal material utilization) to provide an electrical path between the ground plane of the electrical circuit and the shielding enclosure. Though it represents a serious challenge to the electronic package designer, such a ground path is a necessary feature if the component is expected to comply with the ever-increasing parametric specifications regarding attenuation of extraneous RF energy. An example of this is the noise-floor, or ultimate-attenuation, specification for a quartz crystal filter, which greatly depends on the effectiveness of the RF shielding provided. In particular, there are some applications which require a 65-70 dB noise floor, a level which is virtually unattainable without adequate shielding.

U.S. Patent No. 4,739,453, "Shielding Apparatus for a Printed Circuit Board" suggests the use of plated through-holes, or vias, separated by a predetermined distance, to serve as an RF shield. These through-holes, whose spacing determines the wavelength of signals that are shielded, are used to provide an electrical path between the respective ground planes on several layers of a multiple layer printed circuit board. This technique addresses only systems using metallic cases for shielding, and does not address any systems having hermeticity requirements, such as the aforementioned frequency control devices.

Shielding for components such as quartz resonators and filters is typically provided by a conductive metal

enclosure, or lid. The lid is then affixed to the substrate holding the circuit using processes which are well known in the art. Unfortunately, such metal lids are often cost prohibitive in the manufacturing of such components, where the cost of a stamped metal lid often exceeds 20% of the total product cost. In today's market, where competitive pricing is not a luxury, but a necessity for market survival, product cost reductions are often the manufacturers' highest priority. Focusing on the cost problem, many manufacturers have begun using a molded lid, often constructed of a non-conductive material, such as ceramic. Such materials also perform better than metal through the reflow process, where undesired wicking of solder-based metallization cannot otherwise be adequately controlled. Unfortunately, the very nature of the non-conductive material further compounds the problem of providing adequate shielding, as electrical conduction, and subsequent dispersal of undesired RF energy, is the foundation of any good shielding system.

Accordingly, there exists a dire need for a cost effective, easily manufacturable, means for shielding an electronic circuit in a non-conductive enclosure from extraneous RF energy, while complying with any hermeticity requirements, such as those found in frequency control devices.

Summary of the Invention

The present invention encompasses a shielding apparatus for a non-conductive package having an insulated lid. The insulated lid is retained in contact with a substrate which holds an electrical circuit. The shielding apparatus includes a first conductive flash deposited on the interior surface of the insulated lid, to produce a substantially contiguous conduction layer, and a second conductive flash deposited over a film disposed on the edge of the insulated lid, to produce a partially contiguous conduction layer. The shielding apparatus also includes a means for affixing the insulated lid to the substrate, to produce an enclosure for isolating the electrical circuit from extraneous radio frequency (RF) energy.

Brief Description of the Drawings

FIG. 1 is a cross sectional view of a surface-mount quartz crystal filter which is known in the art.

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FIG. 2A is an isometric view showing the arrangement of components used in the shielding process, in accordance with one embodiment of the present invention.

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FIG. 2B is a cross-sectional view of a portion of the lid shown in FIG. 2A prior to any metallization being deposited.

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FIG. 2C is a cross-sectional view of a portion of the lid shown in FIG. 2A after metallization is deposited.

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FIG. 2D is a top view of a portion of one of the conductive flashes shown in FIG. 2C.

FIG. 3A is an isometric view of the substrate, in accordance with one embodiment of the present invention.

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FIG. 3B is a cross-sectional view of a portion of the substrate shown in FIG. 3A.

30

FIG. 4A is a cross-sectional view of the metals which make up the conductive flashes shown in FIG. 2C before the reflow process, in accordance with one embodiment of the present invention.

35

FIG. 4B is a cross-sectional view of the metals which make up the conductive flashes shown in FIG. 2C after reflowing the lid to the substrate shown in FIG. 3A, in accordance with one embodiment of the present invention.

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FIG. 4C shows a cross-sectional view of an assembled quartz crystal filter, in accordance with the present invention.

FIG. 5 is a flow diagram depicting the procedural steps for carrying out one embodiment of the present invention.

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FIG. 6 shows a radio having a circuit which employs the shielding apparatus, in accordance with the present invention.

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Detailed Description of a Preferred Embodiment

FIG. 1 shows a cross-sectional view of a typical, surface-mountable monolithic quartz crystal filter. Such a device is often used in the receiving and transmitting ends of a communication system, an embodiment which is later described. Referring to FIG. 1, quartz plate 106 is mounted, via mount structures 116, to substrate 104. Metallization layer 112 serves as an electrical ground plane for the crystal filter circuit disposed on the substrate 104. Since such a device is highly sensitive to changes in its immediate environment, the lid 102 is generally affixed, as later described, to the substrate 104 in such a way as to provide an airtight seal between the two. Lid 102, which may be constructed of a conductive material or of a non-conductive material, has disposed on it a film 110, which may be a solder based alloy, on its edge. In the preferred embodiment, lid 102 is constructed of a non-conductive ceramic material. Similarly, layer 108 is disposed peripherally about the electrical circuit on the substrate 104. A reflow process is then used to affix the lid 102 to the substrate 104 by bonding the materials of film 110 and layer 108 to form an airtight, or hermetic, seal. A typical hermeticity requirement for such devices may be given in terms of a "leak rate" on the order of 1×10^{-8} std. cc/sec.

FIG. 2A shows how the conductive shielding layer is deposited on the interior surface of lid 102, in the preferred embodiment of the invention. A portion of the lid 102 is shown, which is to have a film 110 deposited on its edge. A wire mesh 202 is then placed in such a way as to rest on top of the film 110. A conductive flash, which may comprise several layers of metallization deposited in the same plating process, is then deposited on the interior

surface, and edges, of the lid 102, using a plating source 207. The plating source 207 may be embodied using any number of well known plating configurations (e.g., E-beam evaporator, thermal evaporator, sputtering machine, etc.).

5 Sputtering machines, in particular, are well suited for carrying out this particular embodiment of the invention, as they readily adapt to a batch process, such that many lids 102 may be placed on a sputtering platen, in a random arrangement, before a continuous wire mesh 202 is placed

10 over them. It should be noted that the orientation of the grid pattern in the wire mesh 202 with respect to the edges of the lids 102, is unimportant. It is only important that the portion of conductive flash deposited on the film 110 is partially contiguous, while the portion reaching the

15 interior surface is entirely contiguous.

FIG. 2B shows a cross-sectional view of the plating arrangement shown in FIG. 2A, prior to the deposition of the conductive flashes. It can be seen that wire mesh 202 comes in direct contact with the film 110 such that when

20 plating source 207, removed a distance 206 from the wire mesh 202, emits metallization 204 toward the lid 102, part of the film 110 is left unplated. In contrast, because of the separation 205 between the mesh screen 202 and the interior surface of the lid 102, the interior surface is

25 coated with a conductive flash which is entirely contiguous. FIG. 2C shows the cross-sectional view of a shielded lid 250, in accordance with one embodiment of the present invention. Conductive flash 203 is a partially contiguous conduction layer deposited on the film 110,

30 while conductive flash 201 is a substantially contiguous conduction layer deposited on the interior surface of the lid 102. It should be noted that conductive flash 201 is sufficiently thick, even at its minimum thickness points, to readily conduct electric energy from all points on the

interior surface of the the lid 102, to any other point on that surface. FIG. 2D shows a close-up top view of a portion of conductive flash 203, shown in FIG. 2C. The "waffle" pattern of the wire mesh 202 produces the pattern shown in area 231 which appears on film 110 after the deposition process. The voids, shown between sections of conductive flash 203, provide for the controlled wicking characteristics between the film 110 and the material used to affix the lid 102 to the substrate 104 during the reflow process.

FIG. 3A shows a substrate 104 for a monolithic quartz crystal filter, in accordance with the present invention. Disposed on the substrate 104, is a conductive area 112 which serves as the electrical ground plane for the electrical circuit. Layer 108, which may be a solder-based alloy, is disposed on the same side of the substrate 104 and acts as a mating metal during the subsequent reflow process. Plated through-holes, or vias 303, combined with conductive areas 301, serve as pin-outs to bring the input and output ends of the filter to the other side of the substrate for surface mounting. Via 304 acts in concert with via 310 to electrically connect the circuit ground plane 112 with layer 108, and ultimately, the shielded lid 250 which is subsequently affixed. FIG. 3B shows a cross-sectional view of a portion of the substrate 104 shown in FIG. 3A, depicting the electrical connection between vias 304 and 310. Ground plane 112 is electrically connected to via 304, while layer 108 is electrically connected to via 310. Conductive strip 302, disposed on the opposite side of substrate 104 as the ground plane 112, is then used to electrically connect via 304 with via 310. This connection allows the circuit ground plane to be held at the same electric potential as

the shielded lid without compromising the hermeticity of the assembled package.

FIG. 4A shows a close-up view of the arrangement shown in FIG. 2C, depicting the metallic composition of
5 conductive flashes 201 and 203, as they may appear before the reflow process. In the preferred embodiment, it is assumed that film 110 and layer 108 both consist of a solder-based alloy, while the lid 102 is formed of a ceramic material. Conductive flash 201 comprises a base
10 metal 403 which is non-wettable with solder, a metallization layer 402 which is wettable with solder, and a layer 401 which, like layer 403, is non-wettable with solder. Similarly, conductive flash 203 comprises the same metallic composition, a solder-wettable metal 402
15 (e.g., silver) sandwiched between two solder-non-wettable metals 401, 403 (e.g., chromium). The like composition of conductive flashes 201 and 203 further makes it possible to deposit them onto the lid 102 during the same deposition process. FIG. 4B shows a close-up view of the
20 metallic composition of the arrangement shown in FIG. 4A, after the reflow process. Lid 102 and substrate 104 are physically joined together by the solder composition 406 formed when reflowing the solder layer 108 with solder film 110. Note that metallization layers 401 and 403
25 prevent undesired wicking onto the substrate 104, and the lid 102, respectively.

FIG. 4C shows a cross-sectional view of the assembled circuit 400, as it may appear in one embodiment of the invention. Non-conductive lid 102 is affixed to
30 substrate 104 using the reflow solder composition 406. Plated vias 304 and 310, in combination with conductive strip 302, serve to electrically connect the circuit ground plane 112 to conductive flash 201. This connection

completes the ground path, providing a sound, effective shield against the ill effects of extraneous RF energy.

FIG. 5 shows the shielding process flow 500 in accordance with the present invention. A conductive solder is deposited at 502 on the edge of the lid 102. A wire mesh is then placed at 504 over one or more lids, whose edges are facing a plating source, such that the mesh is resting on the soldered edge. The first and second conductive flashes are then deposited at 506 through the wire mesh and onto the lid 102. As mentioned earlier, the first and second conductive flashes comprise a three metal system as described in FIGS. 4A and 4B, in the preferred embodiment of the invention. The lid edges are then aligned at 508 with solder layer 108 deposited on the substrate 104. The joined lid and substrate are then reflowed at 510 to create a conductive, hermetic seal between the two entities. The process flow then ends at 512, yielding a hermetically sealed shielding enclosure for the circuit disposed inside the non-conductive package.

FIG. 6 shows a radio transceiver 600 which employs the assembled circuit 400, in accordance with the present invention. In this particular embodiment, assembled circuit 400 represents a quartz crystal filter which may be used for filtering electrical signals in the transmitting and/or receiving ends of a radio, for example a Motorola DYNATAC Cellular Radiotelephone (as shown in Motorola Instruction Manual No. 68P81049E72, pg. 5, dated November 5, 1985). In the receiving end 601, an antenna 605 is coupled to a preselector stage 607. The preselected signal is then sent through an intermediate frequency (IF) filter stage before being passed to a detector 609. The filter stage, which may include buffers and filters 400, typically include at least one 45 MHz quartz crystal filter, though it is understood that alternate embodiments may be

employed. In the transmitting end 603, filter 400 is coupled between a frequency synthesizer 615 and a modulator 613. In the preferred embodiment, the modulated signal is then passed to a power amplifier 611
5 before being transmitted via antenna 605. It should be noted that the transmitting and receiving functions of the radio transceiver 600 can be, and often are, split between two or more separate housings. The filter 400 of the present invention is equally well suited for these
10 embodiments. It should further be noted that, though not presented here, any electrical device which is sensitive to extraneous RF energy, may make use of the invention. Examples of such devices may, for example, oscillators which reside in the frequency synthesizer 615, or multi-
15 pole filters residing in the preselector 607.

What is claimed is:

Claims

- 1) A shielding apparatus for a non-conductive package which has an insulated lid, the insulated lid having an edge and an interior surface, the edge being retained in contact with a substrate having an electrical circuit disposed thereon, the shielding apparatus comprising:
- a film disposed on the edge of the insulated lid;
 - a first conductive flash disposed on the interior surface, to produce a substantially contiguous conduction layer thereon;
 - a second conductive flash selectively disposed on said film, to produce a conduction layer which at least partially covers said film; and
 - means, disposed on the substrate and substantially aligned with said film disposed on the edge of the insulated lid, for affixing the insulated lid to the substrate, whereby an enclosure for isolating the electrical circuit from extraneous radio frequency energy is produced.

2) A shielding apparatus in accordance with claim 1, wherein said means for affixing further comprises means for providing a hermetic seal between the insulated lid and the substrate.

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3) A shielding apparatus in accordance with claim 1, further comprising a conductive area disposed on the substrate and forming an electrical ground plane for the electrical circuit.

10

4) A shielding apparatus in accordance with claim 3, further comprising means for joining said conductive area to said enclosure.

15 5) A shielding apparatus in accordance with claim 1, wherein said film further comprises a conductive material.

6) A shielding apparatus for a non-conductive package having an insulated substrate, the insulated substrate having an electrical circuit disposed thereon and having a first surface and a second surface, the electrical circuit including a conductive area disposed on the first surface, the shielding apparatus comprising:

means, disposed peripherally about the electrical circuit on the first surface, for affixing a lid to the substrate whereby a hermetically-sealed enclosure for isolating the electrical circuit from extraneous radio frequency energy is produced; and

means for connecting the conductive area to said hermetically-sealed enclosure, said means for connecting further comprising:

a first via extending through the substrate within a boundary defined by the conductive area on the substrate, said first via having a first interior surface which is substantially coated with a conductive material, whereby the conductive area is electrically connected to the first via;

a second via extending through the substrate and substantially aligned with said means for affixing, said second via having a second interior surface which is substantially coated with a conductive material, whereby said hermetically sealed enclosure is electrically connected to the second via; and

a conductive strip disposed on the second surface extending from the first via to the second via, whereby the first via is electrically connected to the second via.

- 7) A shielding apparatus for use with an insulated enclosure, the insulated enclosure having an insulated lid and an insulated substrate, the insulated substrate having an electrical circuit disposed thereon and having a first surface and a second surface, the insulated lid having a first interior surface and an edge, the electrical circuit including a conductive area disposed on the first surface, the shielding apparatus comprising:
- a film disposed on the edge of the insulated lid;
 - 10 a first conductive flash disposed on the first interior surface to produce a substantially contiguous conduction layer thereon;
 - a second conductive flash selectively disposed on said film, to produce a conduction layer which at least
 - 15 partially covers said film;
 - means, disposed peripherally about the electrical circuit on the first surface and substantially aligned with said film disposed on the edge of the insulated lid, for affixing the insulated lid to the substrate, whereby a
 - 20 hermetically-sealed enclosure for isolating the electrical circuit from extraneous radio frequency energy is produced; and
 - means for connecting the conductive area to said hermetically-sealed enclosure, said means for connecting
 - 25 further comprising:
 - a first via extending through the substrate within a boundary defined by the conductive area on the substrate, said first via having a first interior surface which is substantially coated with a conductive material,
 - 30 whereby the conductive area is electrically connected to the first via;
 - a second via extending through the substrate and substantially aligned with said means for affixing, said second via having a second interior surface which is

substantially coated with a conductive material, whereby said hermetically sealed enclosure is electrically connected to the second via; and

- 5 a conductive strip disposed on the second surface extending from the first via to the second via, whereby the first via is electrically connected to the second via.

8) A shielding apparatus in accordance with claim 7, wherein said film further comprises a conductive material.

9) A radio transmitter comprising:

frequency synthesizer means for generating radio frequency (RF) signals;

an antenna for transmitting said RF signals; and

5 an electrical device which is sensitive to extraneous RF energy, coupled between said frequency synthesizer means and said antenna, said electrical device having an insulated lid and an insulated substrate, the insulated substrate having an electrical circuit disposed thereon and
10 having a first surface and a second surface, the insulated lid having a first interior surface and an edge, the electrical circuit including a conductive area disposed on the first surface, the electrical device further comprising:

15 a film disposed on the edge of the insulated lid;

a first conductive flash disposed on the first interior surface to produce a substantially contiguous conduction layer thereon;

20 a second conductive flash disposed on said film to produce a partially contiguous conduction layer thereon;

means, disposed peripherally about the electrical circuit on the first surface and substantially aligned with said film, for affixing the insulated lid to the
25 substrate, whereby a hermetically-sealed enclosure for isolating the electrical circuit from extraneous radio frequency energy is produced; and

30 means for connecting the conductive area to said hermetically-sealed enclosure, said means for connecting further comprising:

a first via extending through the substrate within a boundary defined by the conductive area on the substrate, said first via having a first interior surface which is substantially coated with a conductive

material, whereby the conductive area is electrically connected to the first via;

- 5 a second via extending through the substrate and substantially aligned with said means for affixing, said second via having a second interior surface which is substantially coated with a conductive material, whereby said hermetically sealed enclosure is electrically connected to the second via; and

- 10 a conductive strip disposed on the second surface extending from the first via to the second via, whereby the first via is electrically connected to the second via.

- 10) A method of shielding a non-conductive package having an insulated lid, the insulated lid having an edge and an interior surface, the edge being retained in contact with a substrate having an electrical circuit disposed thereon,
- 5 the method comprising the steps of:
- disposing a film on the edge of the insulated lid;
 - disposing a first conductive flash on the interior surface to produce a substantially contiguous conduction layer thereon;
 - 10 disposing a second conductive flash on said film to produce a partially contiguous conduction layer thereon; and
 - affixing the insulated lid to the substrate, whereby an enclosure for isolating the electrical circuit from
 - 15 extraneous radio frequency (RF) energy is produced.

1 / 3

FIG. 1

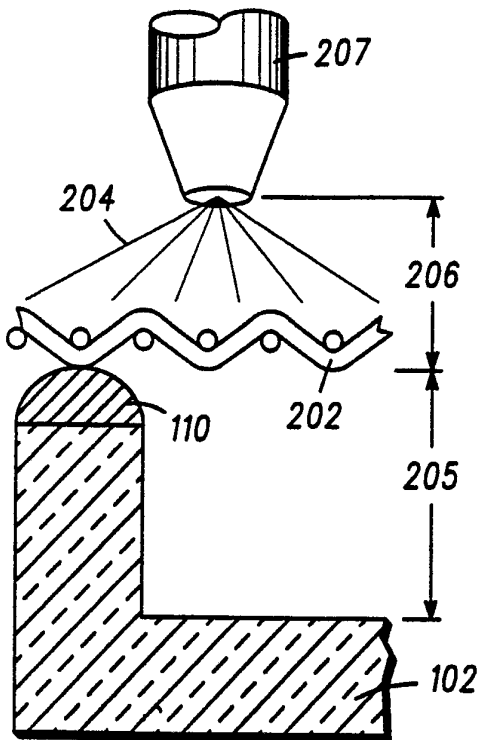
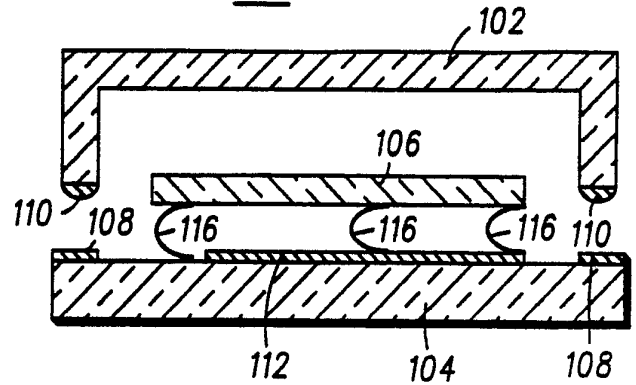


FIG. 2B

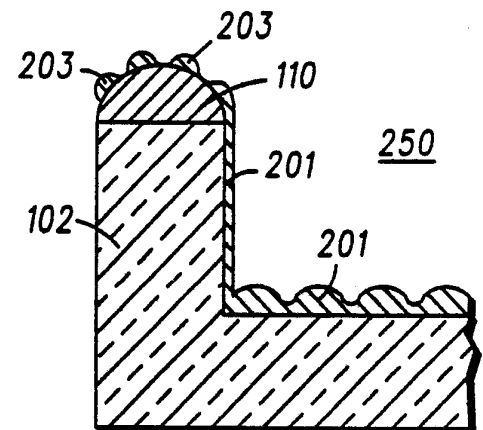


FIG. 2C

FIG. 2D

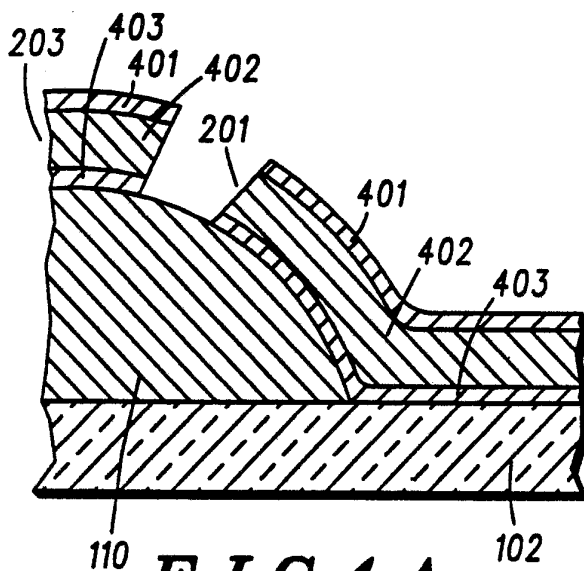
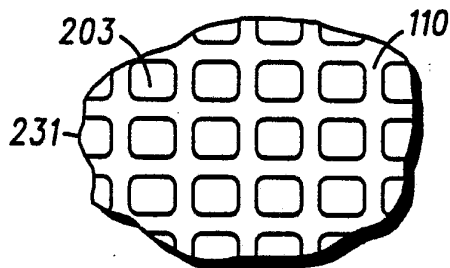


FIG. 4A

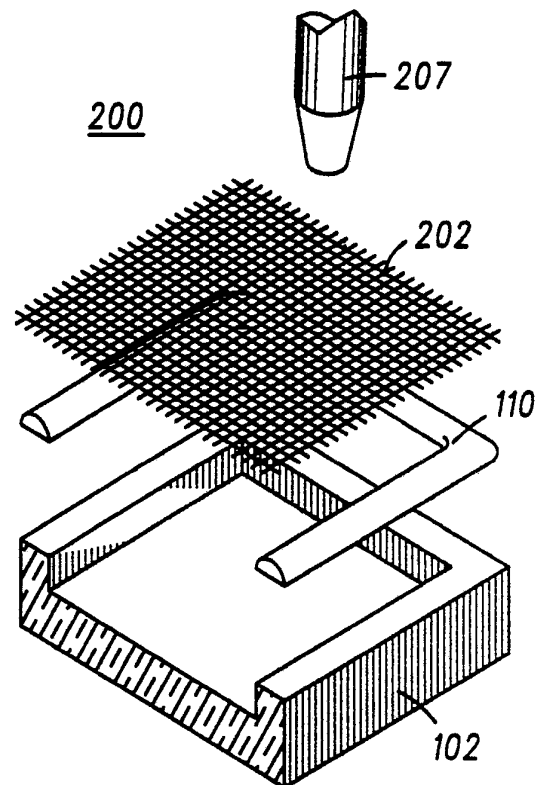
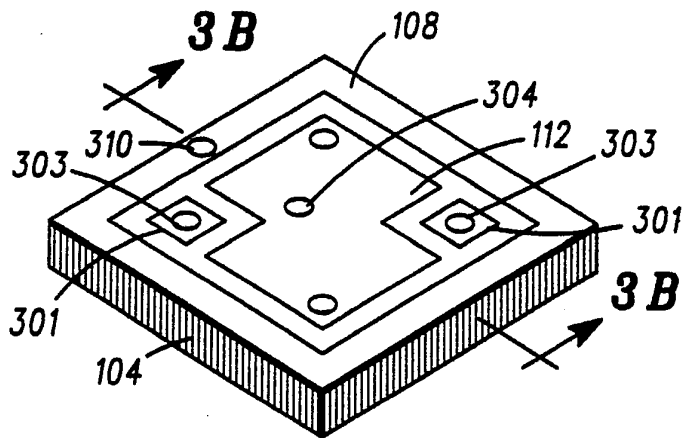
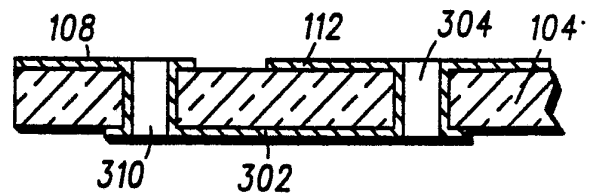
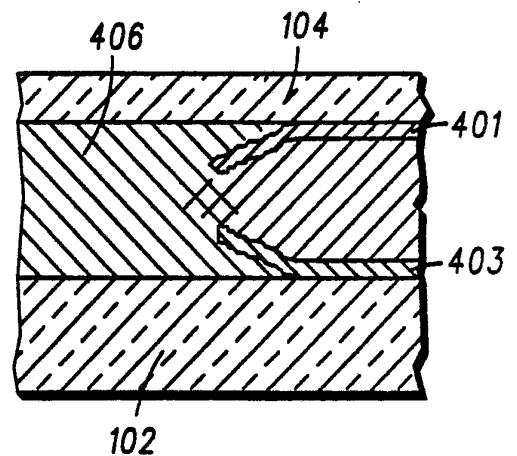
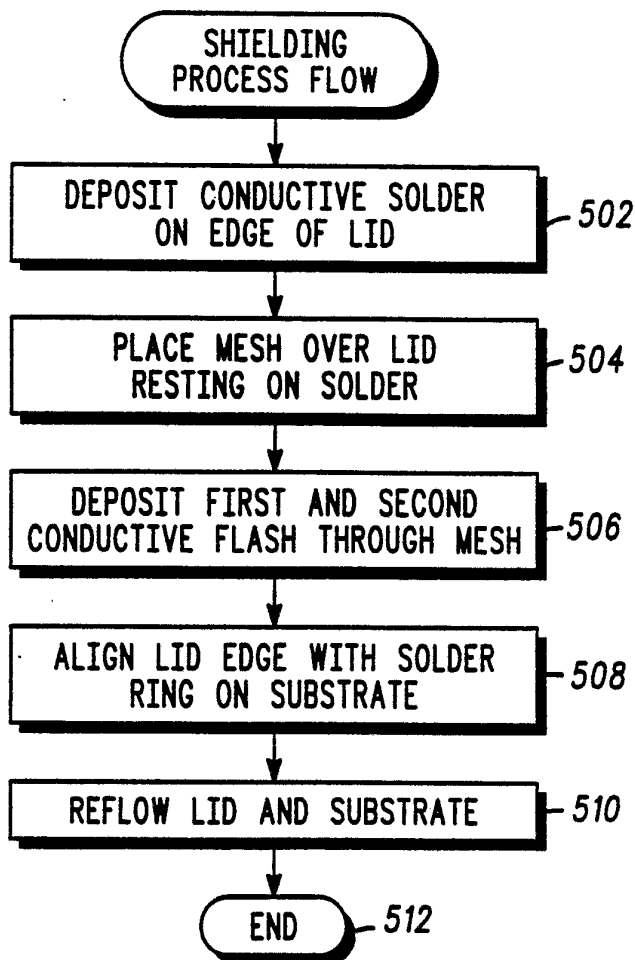


FIG. 2A

2 / 3

**FIG. 3A****FIG. 3B****FIG. 5**500**FIG. 4B**

3/3

FIG. 4C

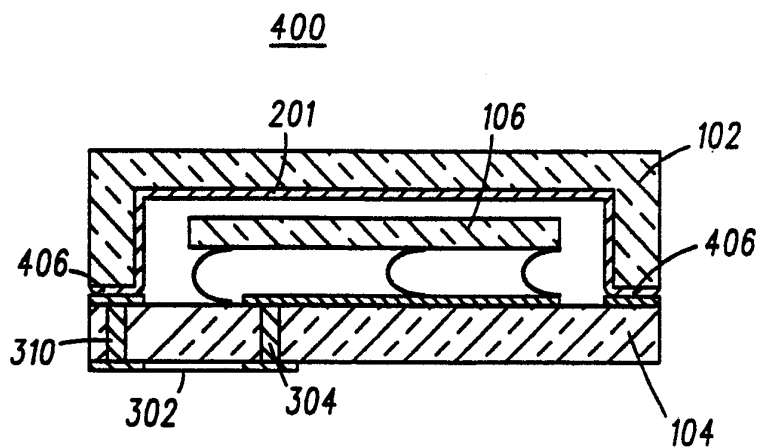
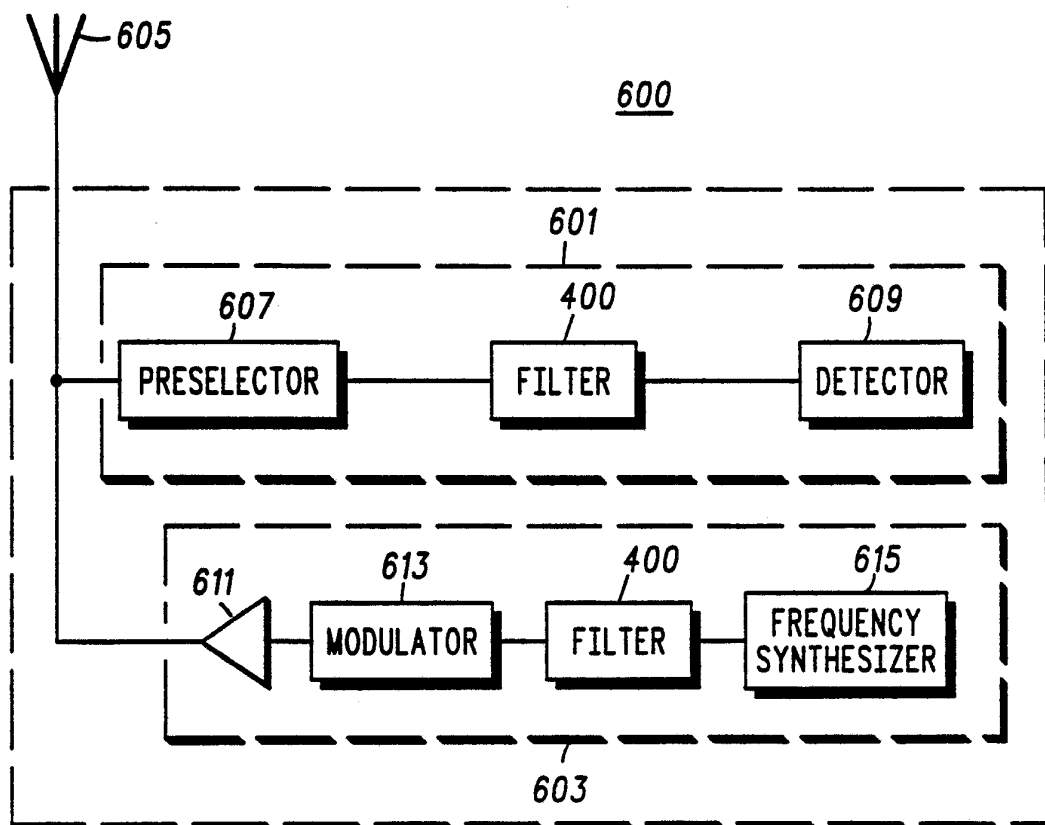


FIG. 6



INTERNATIONAL SEARCH REPORT

International Application No. PCT/US92/01218

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) *		
According to International Patent Classification (IPC) or to both National Classification and IPC		
IPC(5): H05K 9/00		
U.S. CL. 361/424		
II. FIELDS SEARCHED		
Minimum Documentation Searched ⁷		
Classification System	Classification Symbols	
U.S.	29/25.35 357/84 427/239,250,282,287 174/35R,35MS,51 361/390-395,397,399,424,427 439/108,607 228/121,123,124,214,215 455/154,158,183	
Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched ⁸		
III. DOCUMENTS CONSIDERED TO BE RELEVANT ⁹		
Category *	Citation of Document, ¹¹ with indication, where appropriate, of the relevant passages ¹²	Relevant to Claim No. ¹³
Y	US, A, 4,567,317 (Ehrlich et al.) 28 January 1986 See entire document	1,2,5,7-10
Y	US, A, 4,861,941 (Kubo et al.) 29 August 1989 See entire document	3,4,6-10
Y	US, A, 4,848,643 (Frishe et al.) 18 July 1989 See entire document	7-9
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IV. CERTIFICATION		
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International Searching Authority	Signature of Authorized Officer	
ISA/US	Donald A. Sparks	