

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
20 March 2008 (20.03.2008)

PCT

(10) International Publication Number
WO 2008/033428 A3

- (51) International Patent Classification:
G01R 31/02 (2006.01) H05K 3/36 (2006.01)
G01R 31/28 (2006.01)
 - (21) International Application Number:
PCT/US2007/019863
 - (22) International Filing Date:
12 September 2007 (12.09.2007)
 - (25) Filing Language: English
 - (26) Publication Language: English
 - (30) Priority Data:
60/844,139 12 September 2006 (12.09.2006) US
60/921,812 3 April 2007 (03.04.2007) US
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 - (81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH,
CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG,
ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL,
IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK,
LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW,
MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL,
PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY,
TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA,
ZM, ZW.
 - (84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, PL,
PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM,
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:
— with international search report
- (88) Date of publication of the international search report:
27 November 2008

(54) Title: COMPLIANCE PARTITIONING IN TESTING OF INTEGRATED CIRCUITS

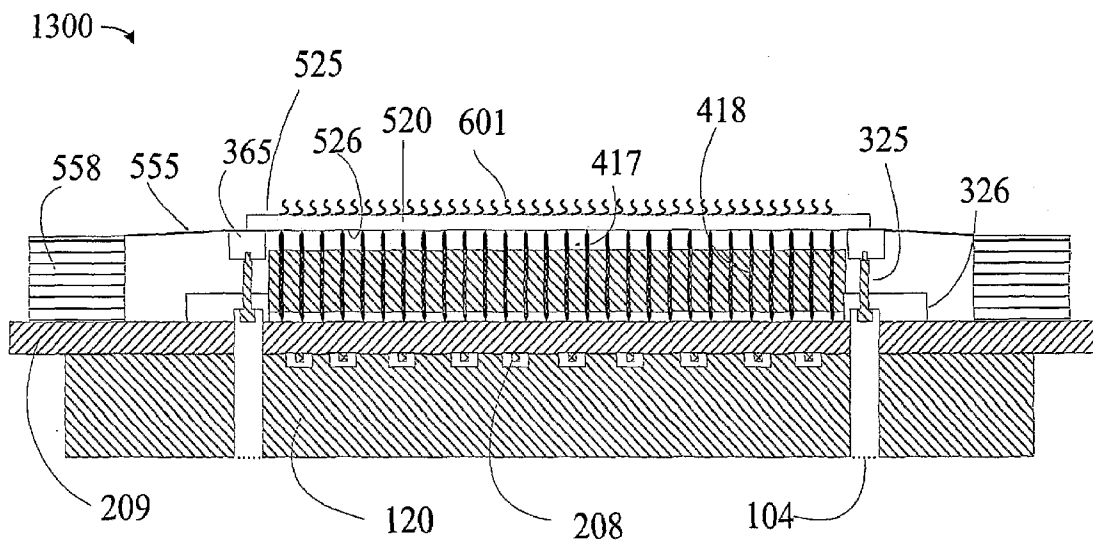


FIG. 13

(57) Abstract: Probecard architectures partition the spring compliance required for IC testing between several different components. Such architectures can provide shorter springs, better impedance control, improved power/ground distribution and more direct paths to tester electronics. The probecards can also use thinner interconnector substrates to conform to the planarity of a DUT and may suspend such a substrate by wires attached to a perimeter edge of the substrate to permit the substrate to tilt. Tilting can also be facilitated by positioning tester-side springs away from the perimeter of the substrate. Low compliance MEMS probes for such architectures can be provided on replaceable coupons having attachment points away from electrical connections, and a method for fabricating probe springs can plate spring material on a membrane deformed by contact with a bumped substrate.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US07/19863

A. CLASSIFICATION OF SUBJECT MATTER
 IPC: **G01R 31/02(2006.01);G01R 31/28(2006.01);H05K 3/36(2006.01)**

 USPC: 324/754,158.1;29/830
 According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED
 Minimum documentation searched (classification system followed by classification symbols)
 U.S. : 324/754,158.1;29/830;438/128,14

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,806,181 A (Khandros et al) 15 September 1998 (15.09.1998), see whole document	1-2, 6-12, 14-23, 25-32, 35-36
Y	US 5,806,181 A (Khandros et al) 15 September 1998, (15.09.1998), see whole document, especially Figure 7	13, 24, 33-34

Further documents are listed in the continuation of Box C.

See patent family annex.

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- "&" document member of the same patent family

Date of the actual completion of the international search
 31 July 2008 (31.07.2008)

Date of mailing of the international search report
15 AUG 2008

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