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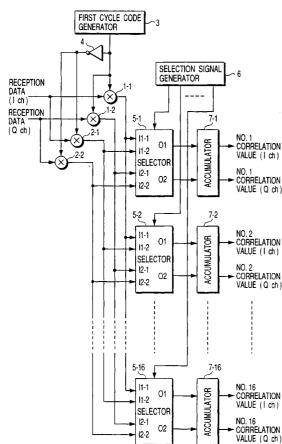
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(54) Title: CODE DETECTION CIRCUIT AND CODE DETECTION METHOD



(57) Abstract: Each of multipliers (1, 2) is used to respectively multiply reception data by a first cycle code generated in a first cycle code generator (3) and by an inverted first cycle code obtained by inverting the first cycle code in a polarity judging section (4). An output from each of the multipliers (1, 2) is supplied to 16 selectors (5) associated with 16 types of Hadamard Sequence patterns. To the selectors (5) are respectively fed selection signals of 16 patterns determined by figuring out exclusive ORs from a code obtained by converting each chip in a second cycle code from "1" to "0" and "-1" to "1" with respect to each of 16 types of the Hadamard Sequence patterns. The selectors (5) select and output each of the multipliers (1, 2) based on the selection signals so that outputs are accumulated in accumulators (7).

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A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H04B1/707

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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

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A	US 5 436 941 A (BULLOCK SCOTT R ET AL) 25 July 1995 (1995-07-25) column 1, line 29 -column 2, line 35; claims 1-20; figure 2/	1,4

χ	Further documents are listed in the continuation of box C.	χ	١
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