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(54) Title: PROGRAMMABLE SYSTEM ON A CHIP

(57) Abstract: A programmable system-on-a-chip integrated circuit device comprises a programmable logic block (12), a non-volatile memory block (14), an analog sub-system (16), an analog input/output circuit block (22), and a digital input/output circuit block 20. A programmable interconnect architecture (Fig. 2) includes programmable elements and interconnectors. Ones of the programmable elements are coupled to the programmable logic block, the non-volatile memory block, the analog sub-system, the analog input/output circuit block, the digital input/output circuit block, and to the interconnect conductors, such that inputs and outputs of the programmable logic block, the non-volatile memory block, the analog sub-system, the analog input/output circuit block, and the digital input/output circuit block can be programmably coupled to one another.

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INTERNATIONAL SEARCH REPORT

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7)  : H03K 19/177
US CL  : 326/38-41

According to International Patent Classification (IPC) or to both national classification and IPC.

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 326/38-41

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Form PCT/ISA/210 (second sheet) (April 2005)
Continuation of B. FIELDS SEARCHED Item 3:
EAST
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