The circuit operates a main switch (TH1) to pass an A.C. supply through an external load when a metallic object is in proximity to a coil (L1) driven by an oscillator circuit, which is connected to a level detector circuit controlling (TH1). The circuit is powered by a power supply stage including storage capacitor (C7), the charging current thereof being controlled by a transistor (T10) and MOSFET (T11). Upon initial application of the A.C. supply, the level detector circuit is overridden by a circuit including capacitor (C3) and transistor (T4) until a predetermined period of time has elapsed, so as to prevent the generation of erroneous “on” signals before the oscillator has stabilised.
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<table>
<thead>
<tr>
<th>Code</th>
<th>Country</th>
</tr>
</thead>
<tbody>
<tr>
<td>AT</td>
<td>Austria</td>
</tr>
<tr>
<td>AU</td>
<td>Australia</td>
</tr>
<tr>
<td>BB</td>
<td>Barbados</td>
</tr>
<tr>
<td>BE</td>
<td>Belgium</td>
</tr>
<tr>
<td>BG</td>
<td>Bulgaria</td>
</tr>
<tr>
<td>BR</td>
<td>Brazil</td>
</tr>
<tr>
<td>CF</td>
<td>Central African Republic</td>
</tr>
<tr>
<td>CG</td>
<td>Congo</td>
</tr>
<tr>
<td>CH</td>
<td>Switzerland</td>
</tr>
<tr>
<td>CM</td>
<td>Cameroon</td>
</tr>
<tr>
<td>DE</td>
<td>Germany, Federal Republic of</td>
</tr>
<tr>
<td>DK</td>
<td>Denmark</td>
</tr>
<tr>
<td>FI</td>
<td>Finland</td>
</tr>
<tr>
<td>FR</td>
<td>France</td>
</tr>
<tr>
<td>GA</td>
<td>Gabon</td>
</tr>
<tr>
<td>GB</td>
<td>United Kingdom</td>
</tr>
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<td>HU</td>
<td>Hungary</td>
</tr>
<tr>
<td>IT</td>
<td>Italy</td>
</tr>
<tr>
<td>JP</td>
<td>Japan</td>
</tr>
<tr>
<td>KP</td>
<td>Democratic People's Republic of Korea</td>
</tr>
<tr>
<td>KR</td>
<td>Republic of Korea</td>
</tr>
<tr>
<td>LI</td>
<td>Liechtenstein</td>
</tr>
<tr>
<td>LK</td>
<td>Sri Lanka</td>
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<tr>
<td>LU</td>
<td>Luxembourg</td>
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<tr>
<td>MC</td>
<td>Monaco</td>
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<tr>
<td>MG</td>
<td>Madagascar</td>
</tr>
<tr>
<td>ML</td>
<td>Mali</td>
</tr>
<tr>
<td>MR</td>
<td>Mauritania</td>
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<tr>
<td>MW</td>
<td>Malawi</td>
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<tr>
<td>NL</td>
<td>Netherlands</td>
</tr>
<tr>
<td>NO</td>
<td>Norway</td>
</tr>
<tr>
<td>RO</td>
<td>Romania</td>
</tr>
<tr>
<td>SD</td>
<td>Sudan</td>
</tr>
<tr>
<td>SE</td>
<td>Sweden</td>
</tr>
<tr>
<td>SN</td>
<td>Senegal</td>
</tr>
<tr>
<td>SU</td>
<td>Soviet Union</td>
</tr>
<tr>
<td>TD</td>
<td>Chad</td>
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<tr>
<td>TG</td>
<td>Togo</td>
</tr>
<tr>
<td>US</td>
<td>United States of America</td>
</tr>
</tbody>
</table>
"LOW LEAKAGE, A.C. PROXIMITY SWITCH CIRCUIT"

DESCRIPTION

This invention relates to proximity switches and particularly, but not exclusively, to the type in which an induction coil is driven by an oscillator and the presence of a metallic object in proximity to the switch causes a change in the amplitude of oscillation which is detected by a detector circuit to close a switching element such as a thyristor or transistor. Such devices are well known in principle, one example being described in our British Patent No. 1531217.

Another device of this general type is described in our PCT/GB/83/00149, which is particularly concerned with ensuring low power dissipation in and minimum voltage drop across the device in the "on" state.

It is desirable in such switches that the "off" state residual current be minimal, and one object of the present invention is to provide a proximity switch circuit which furthers this aim, and which is applicable to switches utilising an induction coil sensor as described above as well as other sensor types.

A problem associated with switches hitherto known is that, upon initial application of the supply voltage, false "on" signals may be generated prior to stabilisation of the sensing circuitry. It is a further object of the present invention to obviate or mitigate the occurrence or erroneous signals under these conditions.

According to one aspect of the invention, a proximity switch for use with an A.C. supply to control a load connected in series with the switch comprises:

- a power supply including a storage capacitor;
- and
- a controllable switching element connected, in use, across said AC supply and having a control input;
characterised in that said power supply further includes means for limiting the current charging said storage capacitor to a predetermined maximum value when the switch is "off".

Preferably, said limiting means includes a variable resistance device connected in series with said storage capacitor and means responsive to the level of the charging current for controlling the resistance of said device.

Preferably also, said variable resistance device is a MOSFET and said charging current responsive means controls the MOSFET gate voltage.

Preferably also, said means for controlling the gate voltage of said MOSFET includes a transistor, the base-emitter voltage of said transistor being proportional to the charging current of said storage capacitor.

Preferably also, the switch includes means for bypassing said transistor when the switch is "on".

Preferably also, said sensor means comprises an induction coil, an oscillator connected to drive the coil, and a level detector connected to the oscillator to provide a detector output signal in response to reduction in the amplitude of oscillation caused by proximity of a metallic object to the coil.

According to a second aspect of the invention, a proximity switch for use with an AC supply to control a load connected in series with the switch comprises:

- a power supply including a storage capacitor;
- an induction coil;
- an oscillator connected to drive the coil;
- a level detector connected to the oscillator to provide a detector output signal in response to reduction in the amplitude of oscillation caused by proximity of a metal object to the coil; and
- a controllable switching element connected, in
use, across said A.C. supply and having a control input;

characterised by time delay means for overriding said level detector upon initial application of said A.C. supply so as to prevent generation of a detector output signal until a predetermined period of time has elapsed.

Preferably, said time delay means includes a controlled conduction device connected so as to clamp the output of said level detector to a predetermined level and thereby prevent the generation of an output signal.

Preferably also, said controlled conduction device is a transistor whose base-emitter voltage is controlled such that the transistor switches off after a predetermined period of time, enabling the level detector to operate normally.

Preferably also, said controlled conduction device is responsive to timing means comprising a capacitor.

An embodiment of the invention will now be described, by way of example only, with reference to the accompanying drawings in which Fig. 1 is a circuit diagram of an inductive type proximity switch circuit embodying the invention, and

Fig. 2 is a block diagram illustrating the application of the invention to proximity switches utilising sensors of other types.

Referring now to the Fig. 1, an inductive type proximity switch circuit embodying the invention includes a full-wave rectifier bridge BR1 connected to an A.C. supply voltage via terminals 1 and 2 and an externally connected series load (not shown), and means for sensing the presence of a metal object in proximity to the switch comprising an induction coil L1 driven by
an oscillator circuit comprising transistors T1 and T2, capacitor C1 and resistors R1, R2 and R3.

The presence of a metal object in proximity to the coil L1 loads the oscillator and reduces the amplitude of the oscillation. This reduction of amplitude is sensed by a level detector circuit comprising transistor T3, capacitor C4, resistors R4, R6 and R7 and light-emitting diode (l.e.d.) LL1. The l.e.d. LL1 provides a visual indication of the state of the switch.

When the switch is "on", a thyristor TH1 conducts causing current to flow through the external load.

Power for the low voltage powered circuitry is provided by a power supply circuit including capacitor C7 and zener diode Z1.

The operation of the circuit will now be described, firstly considering the case where the A.C. supply voltage is applied to the terminals 1 and 2 with no target object present.

The A.C. supply voltage is applied to the terminals 1 and 2 from an external source (via the externally connected series load) and is rectified by the bridge rectifier BR1 so that a D.C. voltage appears across points 3 and 4, point 3 being positive. The gate of MOSFET T11 is now forward biased by the voltage applied via resistor R21, allowing current to flow along the path 3, drain-source of T11, diodes D1A and D1B, resistor R20, capacitor C7, 4.

The amount of current that can flow is limited by the amount that can pass through R20 before the voltage developed across R20 is sufficient to cause the base-emitter of transistor T10 to be forward biased. At this point, T10 will turn on and current will flow from 3, through R21, collector-emitter of T10 and C7 before returning to 4. At some stage the current
flowing will be great enough to result in the voltage drop across R21 being sufficient to starve the gate of T11 of sufficient voltage to maintain T11 in full drain-source conduction.

The net result is that the current charging capacitor C7 is limited to a pre-set value, typically 1.5 mA.

The voltage across C7 will rise until zener diode Z1 conducts via resistor R19 and base-emitter of transistor T8.

The voltage across C7 then forms the power supply voltage for the low voltage powered circuitry. The value is typically 6V and the circuit is designed to maintain this voltage for an input A.C. supply voltage in the range 20 to 264 volts.

It can be seen then, that as the maximum level of the C7 charging current is limited, any switch on surge registered in the external load is limited to this low value. The capacitors C7 and C5 constitute a storage medium to maintain the voltage at a relatively constant level during the time that the A.C. supply voltage falls to zero at each half cycle.

The case where the circuit is detecting a metal object will now be considered.

When a target object is detected, the voltage at point 5 rises positively with respect to 4. This results in current flowing in the path 5, R12, R13, R14, 4. The voltage drop across R14 is sufficient to forward bias the gate-cathode of thyristor TH1, switching TH 1 to the "on" state. Load current will then flow from the external A.C. voltage source, through the external load and bridge BR1, and hence from point 3, through the anode-cathode of TH1, and return to the supply via 4 and BR1.

At the same time that the voltage at 5 rises
positively, transistor T6 base-emitter will be forward biased via R11, causing T6 to turn on.

At the instant that thyristor TH1 switches into conduction, the voltage between points 3 and 4 collapses almost to zero, the A.C. supply voltage now being developed across the external load. This state will be maintained until the supply current approaches zero when the thyristor TH1 will automatically switch off. The voltage across 3 and 4 will then increase as the A.C. supply increases on the next half-cycle.

Since T6 is now turned on, the base-emitter of transistor T9 is forward biased and the emitter-collector of T9 conducts. Accordingly, the current to operate the power supply (i.e. the C7 charging current) now follows the path 3, T11 drain-source, D1A, T9 emitter-collector, C7, 4. Thus, T9 effectively by-passes the current limiter transistor T10, enabling the power supply capacitor C7 to charge rapidly. The time taken for the voltage across C7 to reach the breakdown voltage of the zener diode Z1 is therefore kept to a minimum, resulting in a relatively low on-state voltage being developed across the terminals 1 and 2.

When the breakdown voltage of Z1 is reached, current will flow into R19 and transistor T8 will become forward biased, turning on T8 and thereby turning off transistor T7. Upon T7 turning off, current will be allowed to flow into the gate of thyristor TH1 via R12 and R13, and TH1 will again switch into conduction. This process is repeated each half-cycle of the supply voltage.

A second zener diode Z2 protects the gate of the MOSFET T11 from excessive voltage.

Diode D1A prevents discharge of C7 at the time that thyristor TH1 conducts. Without D1A, C7 would
discharge via T9 (reverse biased collector-emitter),
the internal diode of T11 (shown in broken lines),
TH1 anode-cathode and back to the negative terminal
of C7. Diode D1B is included to ensure that the
base of T10 is starved of current while T9 is
conducting.

The proximity switch circuit further includes
a circuit comprising transistor T4, resistors R5
and R8 and capacitor C3 to override the oscillator and
level detector circuitry until that circuitry has
stabilised its working point after initial application
of the supply voltage. If this was not included,
the thyristor TH1 could receive a false "on" signal
with no metal target object present.

When the power supply first builds up following
application of the A.C. voltage, C3 charges up via R8,
R5 and T4 base-emitter. T4 switches on, thereby
clamping the base of T5 to point 6. This ensures that
T5 cannot turn on and supply a positive voltage level
to point 5.

After a predetermined time, the current flowing
in C3 will reduce to a sufficiently low value for T4
to turn off, this enabling T5 to operate normally.

The power supply circuitry described above may
also be applied to proximity switches utilising other
types of sensor head, as will now be described with
reference to Fig. 2 which shows a block diagram of a
proximity switch 8, connected to an ac supply via a
series load 11, and comprising sensor means 9 and a power
supply and output stage 10. Reference numerals 1, 2,
5, 6 and 7 correspond to those of Fig. 1.

The circuitry contained within block 10 is
identical to that of Fig. 1 to the right of points 5, 6
and 7. The power supply stage embodying the invention
may thus be coupled to other types of sensor (such as
the Siemens TCA 305 or alternative) by connecting lines 5 and 6 across the sensor output, line 7 being a ground line.
CLAIMS

1. A proximity switch for use with an AC supply to control a load connected in series with the switch comprising:
   a power supply including a storage capacitor;
   an induction coil;
   an oscillator connected to drive the coil;
   a level detector connected to the oscillator to provide a detector output signal in response to reduction in the amplitude of oscillation caused by proximity of a metal object to the coil; and
   a controllable switching element connected, in use, across said AC supply and having a control input;
   characterised by time delay means for overriding said level detector upon initial application of said AC supply so as to prevent generation of a detector output signal after a predetermined period of time has elapsed.

2. A proximity switch as claimed in claim 1 wherein said time delay means includes a controlled conduction device connected so as to clamp the output of said level detector to a predetermined value and thereby prevent the generation of an output signal.

3. A proximity switch as claimed in claim 2 wherein said controlled conduction device is a transistor whose base-emitter voltage is controlled such that the transistor switches off after a predetermined period of time, enabling the level detector to operate normally.

4. A proximity switch as claimed in claim 2 or claim 3 wherein said controlled conduction device is responsive to timing means comprising a capacitor.

5. A proximity switch for use with an AC supply to control a load connected in series with the switch comprising:
   a power supply including a storage capacitor;
   sensor means;
   a controllable switching element connected, in use, across said AC supply and having a control input;
   said power supply including means for limiting the current charging said storage capacitor to a predetermined maximum value when the switch is "off";
   characterised in that said current limiting means includes a variable resistance device connected in series with said storage
capacitor and means responsive to the level of the charging current for controlling the resistance of said device.

6. A proximity switch as claimed in claim 5, in which said variable resistance device is a MOSFET and said charging current responsive means controls the MOSFET gate voltage, as a function of said charging current.

7. A proximity switch as claimed in claim 6 wherein said means for controlling the gate voltage of said MOSFET includes a transistor, the base-emitter voltage of said transistor being proportional to the charging current of said storage capacitor.

8. A proximity switch as claimed in claim 5, further including means for by-passing said charging current responsive means when the switch is "on".
Fig. 2
**INTERNATIONAL SEARCH REPORT**

International Application No PCT/GB 86/00562

**I. CLASSIFICATION OF SUBJECT MATTER**

According to International Patent Classification (IPC) or to both National Classification and IPC

IPC: H 03 K 17/95

**II. FIELDS SEARCHED**

Minimum Documentation Searched

<table>
<thead>
<tr>
<th>Classification System</th>
<th>Classification Symbols</th>
</tr>
</thead>
<tbody>
<tr>
<td>IPC</td>
<td>H 03 K; H 02 M</td>
</tr>
</tbody>
</table>

Documentation Searched other than Minimum Documentation to the extent that such Documents are Included in the Fields Searched

**III. DOCUMENTS CONSIDERED TO BE RELEVANT**

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of Document, with Indication, where appropriate, of the relevant passages</th>
<th>Relevant to Claim No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>X</td>
<td>FR, A, 2292242 (SQUARE D COMPANY) 18 June 1976, see page 3, line 35 - page 7, figure &amp; GB, A, 1531217 (cited in the application)</td>
<td>1-4</td>
</tr>
<tr>
<td>X</td>
<td>WO, A, 83/04458 (SQUARE D COMPANY) 22 December 1983 see page 3, line 3 - page 6, line 28; figure 1</td>
<td>5</td>
</tr>
<tr>
<td>A</td>
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<td>1-4,8</td>
</tr>
<tr>
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<td>FR, A, 2497423 (LA TELEMECANIQUE ELECTRIQUE) 2 July 1982 see page 5, line 27 - page 6, line 27; figure</td>
<td>6,7</td>
</tr>
</tbody>
</table>

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**IV. CERTIFICATION**

Date of the Actual Completion of the International Search: 10th December 1986

Date of Mailing of this International Search Report: 22 JAN 1987

International Searching Authority: EUROPEAN PATENT OFFICE

Signature of Authorized Officer: M. VAN MOL

Form PCT/ISA/210 (second sheet) (January 1985)
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<table>
<thead>
<tr>
<th>Patent document cited in search report</th>
<th>Publication date</th>
<th>Patent family member(s)</th>
<th>Publication date</th>
</tr>
</thead>
<tbody>
<tr>
<td>FR-A- 2292242</td>
<td>18/06/76</td>
<td>GB-A- 1531217</td>
<td>08/11/78</td>
</tr>
<tr>
<td></td>
<td></td>
<td>CA-A- 1060115</td>
<td>07/08/79</td>
</tr>
<tr>
<td>WO-A- 8304458</td>
<td>22/12/83</td>
<td>AU-A- 1607283</td>
<td>30/12/83</td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP-A- 0111517</td>
<td>27/06/84</td>
</tr>
<tr>
<td>DE-C- 3146709</td>
<td>24/02/83</td>
<td>JP-A- 58097920</td>
<td>10/06/83</td>
</tr>
</tbody>
</table>

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